6-93 OMB No. 0651 0011 (exp. 4/94)	3HEET U.S. DEPARTMENT OF COMMERCE Patent Office
OMB No. 0651-0011 (exp. 4/94) 102296539	
To the Honorable Commissioner of Patents and Trademarks. Pleas	
Name of conveying party(ies): Andrew W. Lai	2. Name and address of receiving party(ies):
	Party (100).
11/15/02	Name: Xilinx, Inc.
Additional name(s) of conveying party(ies) attached? [] Yes [X] No	Internal Address:
3. Nature of Conveyance:	Street Address: 2100 Logic Drive
[X] Assignment [] Merger	
[] Security Agreement [] Change of Name	City: San Jose State: California Zip: 95124
Execution Date: <u>11-12-02</u>	Additional name(s) & address(es) attached? [] Yes [X] No
4. Application number(s) or patent number(s):	
If this document is being filed together with a new application, the fili	ng date of the application is: 11-15-02
A. Patent Application No.(s)	B. Patent No.(s)
Andrew W. Lai "Structures and Methods for Testing Programmable Logic Devices Having Mixed-Fabric Architectures"	
109/15	
10/295715 Additional number	rs attached? [] Yes [X] No
Name and address of party to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved: <u>one</u>
Name: LOIS D. CARTIER	7. Total fee (37 CFR 3.41):\$ 40.00
Internal Address: Xilinx, Inc.	[] Enclosed [X] Authorized to be charged to deposit account
Street Address: 2100 Logic Drive	8. Deposit Account Number: 24-0040
City: San Jose State: California Zip: 95124	(Attach duplicate copy of this page if paying by deposit account)
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9. Statement and signature.	
To the best of my knowledge and belief, the foregoing information is to	rue and correct and any attached copy is a true copy of the
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LOIS D. CARTIER 40041	and the second
LOIS D. CARTIER, 40,941 Name of Person Signing Signature	November 15, 2002 Date
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ASSIGNMENT

	of 340 Lower Vintners Circle, Fremont, CA 94539
	of of
hereby sell, assign and transfer to	o Xilinx, Inc., a Delaware corporation, having a place of business at 2100 nia 95124, its successors and assigns, the entire right, title and interest
	METHODS FOR TESTING PROGRAMMABLE LOGIC DEVICES AVING MIXED-FABRIC ARCHITECTURES
and all patent applications and p continuations and extensions t applications; I/we authorize the invention, and to claim all right execute all papers useful in conn do everything possible to aid s expense, in obtaining and enfor	Inited States patent application on or about the date of this assignment, patents of every country for said invention, including divisions, reissues, thereof, and all rights of priority resulting from the filing of said above-named assignee to apply for patents of foreign countries for said its of priority without further authorization from me/us; I/we agree to nection with said United States and foreign applications, and generally to said assignee, its successors, assigns and nominees, at its request and recing patents for said invention in all countries; and I/we request the rademarks to issue all patents granted for said invention to the above-nd assigns.
,	
Executed this <u>A A</u> day of	1004111)(1) , 20 02. Andrew W. Lai
State of <u>California</u>)	- Athela .
State of <u>California</u>) County of <u>Santa Clara</u>) On <u>Andrew W. Lara</u> of satisfactory evidence to be acknowledged to me that he/she	Andrew W. Lai CAPACITY CLAIMED BY SIGNER [X] oneself/themselves 1 12 200 before me, Pat Slaback, Notary Public, personally

LDC:pbs

RECORDED: 11/15/2002

PATENT REEL: 013521 FRAME: 0706