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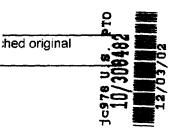
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102307725

Execution Date:

November 26, 2002 November 26, 2002 November 26, 2002 November 26, 2002 PATENT



Alexis FARCY
Philippe CORONEL
Pascal ANCEY
Joaquin TORRES

2. Name and address of receiving party:

To the Honorable Commissioner of Patents

STMICROELECTRONICS S.A. 29 Boulevard Romain Rolland F-92120 Montrouge, France

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10/308482

5. Name and address of party to whom correspondence concerning document should be mailed:

Customer No. **23334**Jose Gutman
FLEIT, KAIN, GIBBONS, GUTMAN & BONGINI P.L.
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PATENT REEL: 013551 FRAME: 0711

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ASSIGNMENT

name and address of assignee

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STMicroelectronics S.A. 29 boulevard Romain Rolland F-92120 Montrouge, France

(hereinafter ASSIGNEE) all right, title and interest for the United States, its territories and possessions in and to this invention relating to

title of invention

INTEGRATED CIRCUIT COMPRISING AN AUXILIARY
COMPONENT, FOR EXAMPLE A PASSIVE COMPONENT OR A
MICROELECTROMECHANICAL SYSTEM, PLACED ABOVE AN
ELECTRONIC CHIP, AND THE CORRESPONDING FABRICATION
PROCESS

as set forth in this United States Patent Application

check one	√	executed concurrently herewith
		executed on
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Docket No. 01-GR1-165

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NAME AND SIGNATURE OF INVENTOR(S)

Name:Alexis FARCY Signature: Date: 11.26.02

Name:Philippe CORONEL Signature: Date: 11.26.02

Name:Pascal ANCEY Signature: Date: 11.26.02

Name:Joaquin TORRES Signature: Date: 11.26.02

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