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Execution Date: November 28, 2002

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4. Application number(s) or patent number(s):

10/318167

If this document is being filed together with a new application, the execution date of the application is: November 28, 2002

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:



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6. Total applications and patents involved: 1

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In consideration of value received, I, having a mailing address at c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura 1-chome, Minato-ku, Tokyo 105-8001, Japan and a residence as stated below next to my name, the sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are listed below) of an invention described in an application for United States patent entitled:

SEMICONDCUTOR INTEGRATED CIRCUIT ADAPTED TO OUTPUT PASS/FAIL RESULTS OF
INTERNAL OPERATIONS

said application having been filed on
and assigned Application No. :
sell and assign to
KABUSHIKI KAISHA TOSHIBA
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1-1 Shibaura 1-chome, Minato-Ku, Tokyo, Japan

its successors, assigns or nominees, hereinafter referred to as
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as disclosed, shown and described in said application for United States
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