

01-13-2003

Attorney Docket No. 021734

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Conveying Party (ies): (Last, First, Middle Initial) Execution Date
 First party Name: NISHIKAWA, Nobuyuki November 13, 2002
 Second party Name:
 Third party Name:
 Additional name(s) of conveying party(ies) attached? Yes ___ No XX

Receiving Party (ies):

First party Name: FUJITSU LIMITED
 Address: 1-1, Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi,
 Kanagawa 211-8588 Japan
 Second party Name:
 Address:
 Additional name(s) & address(es) attached? Yes ___ No XX

Domestic Representative Name and Address

Armstrong, Westerman & Hattori, LLP
 Suite 1000, 1725 K Street
 Washington, D.C. 20006
 (202) 659-2930



23850

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Pages Enter the total number of pages of the attached conveyance document: 1
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Application Number(s) or Patent Number(s) Additional numbers attached? Yes ☐ No ☒
A. Patent Application No.(s) B. Patent No.(s)

10,337,377

If this document is being filed together with a new application, enter the date the application was signed by the first named executing inventor: November 13, 2002

(Do not recite both application and patent numbers.)

Patent Cooperation Treaty (PCT)Enter PCT application number only if

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Number of Properties Enter the total number of properties involved: 1

Fee Amount Fee Amount for Properties Listed (37 CFR 3.41). \$40.00

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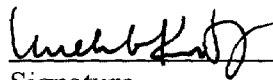
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Statement and signature

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

William G. Kratz, Jr.

Name of Person Signing



Signature

January 7, 2003

Date

22,631

Registration No.

U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter ASSIGNOR) by
FUJITSU LIMITED

1-1, Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8588 Japan

(hereinafter ASSIGNEE), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

**SEMICONDUCTOR MEMORY DEVICE HAVING CYLINDER-TYPE STACKED
 CAPACITOR AND METHOD FOR FABRICATING SUCH A SEMICONDUCTOR
 MEMORY DEVICE**

for which application for Letters Patent of the United States was executed on even date herewith unless otherwise indicated below:

filed on _____, Serial No. _____.

(ARMSTRONG, WESTERMAN & HATTORI, LLP is hereby authorized to insert the series code, serial number and/or filing date hereon, when known)

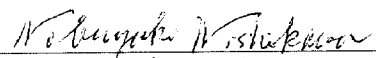
and all Letters Patent of the United States to be obtained therefore on said application or any continuation, divisional, substitute, reissue or reexamination thereof for the full term or terms for which the same may be granted.

The ASSIGNOR agrees to execute all papers necessary in connection with the application and any continuation, divisional, reissue or reexamination applications thereof and also to execute separate assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.

The ASSIGNOR agrees to execute all papers necessary in connection with any interference, litigation, or other legal proceeding which may be declared concerning this application or any continuation, divisional, reissue or reexamination thereof or Letters Patent or reissue patent issued thereon and to cooperate with the ASSIGNEE in every way possible in obtaining and producing evidence and proceeding with such interference, litigation, or other legal proceeding.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

(Signatures)

 (Signature)	Nobuyuki Nishikawa (Type Name)	<u>Nov. 13 2002</u> (Date)
_____ (Signature)	_____ (Type Name)	_____ (Date)
_____ (Signature)	_____ (Type Name)	_____ (Date)
_____ (Signature)	_____ (Type Name)	_____ (Date)
_____ (Signature)	_____ (Type Name)	_____ (Date)
_____ (Signature)	_____ (Type Name)	_____ (Date)

NO LEGALIZATION REQUIRED