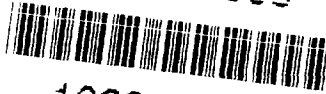


01-16-2003



OMB No. 0651-0027 (exp. 5/31/2002)

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To the Honorable Commissioner of Patents and Trademarks.

Send original documents or copy thereof.

1. Name of conveying party(ies):

James S. Prater

1-14-03

2. Name and address of receiving party(ies)

Name: National Semiconductor, Inc.

Internal Address: 2900 Semiconductor Drive  
Mailstop D3-579  
Santa Clara, CA 95051

Additional name(s) of conveying party(ies) attached?  Yes  No

3. Nature of conveyance:

- Assignment  Merger
- Security Agreement  Change of Name
- Other \_\_\_\_\_

Street Address: 2900 Semiconductor Drive  
Mailstop D3-579

City: Santa Clara State: CA ZIP: 80528

Execution Date: January 7, 2003

Additional name(s) & address(es) attached?  Yes  No

4. Application number(s) or patent number(s): NSC.01US01 (P-04954)

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s): 09/872,271  
Title: Over-sampling A/D converter with adjacent  
Channel power detection  
Filed: June 1, 2001

B. Patent No.(s)

Additional numbers attached?  Yes  No

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5. Name and address of party to whom correspondence concerning document should be mailed

Name: William W. Cochran II

Internal Address: The Law Offices of William W. Cochran, LLC

3555 Stanford Road, Suite 230

Fort Collins, CO 80525

Street Address: 3555 Stanford Road, Suite 230

City: Fort Collins State: CO ZIP: 80525

6. Total number of applications and patents involved:

7. Total fee (37 CFR 3.41).....\$40.00

Enclosed

Authorized to be charged to deposit account

8. Deposit account number:

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9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document

William W. Cochran

8 Jan 03

Name of Person Signing

Signature

Date

01/16/2003 LHWELLER 00000012 09872271 Total number of pages including cover sheet, attachments and document: [2]

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