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1. Tadayuki Taura 2. Shigeru Atsumi  
3. Toru Tanzawa

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☐ Yes ☒ No

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Execution Date: January 7, 2003

4. Application Number(s) or Patent Number(s):

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application Number(s):

10/234,704

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B. Patent Number(s):

5. Name and address of party to whom correspondence concerning the document should be mailed:

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January 24, 2003

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**PATENT**  
**REEL: 013702 FRAME: 0271**

# A S S I G N M E N T

In consideration of value received, I, having a mailing address at  
c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura  
1-chome, Minato-ku, Tokyo 105-8001, Japan and a residence as stated below  
next to my name, the sole inventor (if only one name is listed below)  
or a joint inventor (if plural inventors are listed below) of an  
invention described in an application for United States patent entitled:

NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND METHOD OF RETRIEVING FAULTY  
IN THE SAME

said application having been filed on September 5, 2002  
and assigned Application No. 10/234,704 ;  
sell and assign to  
KABUSHIKI KAISHA TOSHIBA  
a corporation of Japan, having a business address at  
1-1 Shibaura 1-chome, Minato-Ku, Tokyo, Japan

its successors, assigns or nominees, hereinafter referred to as  
"Assignee", my entire right, title and interest in and to said invention  
as disclosed, shown and described in said application for United States  
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☒ executed concurrently herewith;

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# A S S I G N M E N T

02S0812

Inventor: (Signature)

Date

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