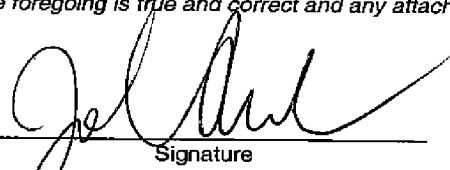


FORM PTO-1595 (Rev. 3/01) OMD NO. 0651-0011 exp. 5/31/2002		Recordation Form Cover Sheet PATENTS ONLY		U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office	
To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.					
1. Name of conveying party(ies): Chameleon Systems, Inc.		2. Name and address of receiving party(ies): Name: INTEL CORPORATION			
Additional name(s) of conveying party(ies) attached? <input checked="" type="checkbox"/> No <input type="checkbox"/> Yes		Internal Address: _____			
3. Nature of Conveyance <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other:		Street Address: 2200 Mission College Blvd.			
Execution Date(s): May 31, 2003		City: Santa Clara State/Province: CA Zip: 95052			
		Country: U.S.A.			
		Additional name(s) & address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No			
4. Application Number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is: _____					
A. Patent Application No.(s)		B. Patent No.(s)			
		5,986,534 6,347,346 5,970,254 6,349,346 6,243,808 6,519,674 6,298,472 6,539,477			
Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No					
5. Name and address of party to whom correspondence concerning document should be mailed: Edwin H. Taylor Name: Blakely, Sokoloff, Taylor & Zafman LLP Internal Address: _____ Street Address: 12400 Wilshire Boulevard, 7th Floor City: Los Angeles State: California Zip: 90025		6. Total number of applications and patents involved: 6			
		7. Total Fee (37 CFR 3.41).....\$40.00 <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to be charged to deposit account			
		8. Deposit Account Number: 02-2666 (Attach duplicate copy of this page if paying by deposit account)			
DO NOT USE THIS SPACE					
9. Statement and signature. <i>To the best of my knowledge and believe, the foregoing is true and correct and any attached copy is a true copy of the original document</i>					
John P. Ward Reg. No. 40,216 Name of Person Signing		 Signature		June 23, 2003 Date	
Total number of pages including cover sheet, attachments, and document: 8					

Mail documents to be recorded with required cover sheet information to:

Mail Stop Assignment Recordation Services
Director of the United States Patent and Trademark Office
P.O. Box 1450
Alexandria, Virginia 22313-1450Attorney Docket No. 42P16933, 42P16934, P16979,
 42P16937, 42P16941, P16983
 42P16942, 42P16943,

700034524

PATENT
REEL: 013758 FRAME: 0096

CH \$320.00 022666 5966534

EXHIBIT B**Assignment of Patent Rights**

Whereas, Chameleon Systems, Inc., a California corporation (hereinafter COMPANY) is the sole and exclusive owner of certain United States and/or foreign patents and/or patent applications listed in Exhibit A annexed hereto (collectively referred to as the "Patents"); and

Whereas Intel Corporation, a Delaware corporation, with an office at 2200 Mission College Blvd., California 95052, (hereinafter INTEL) is desirous of acquiring the right, title and interest in, to and under the said Patents (and all foreign counterparts and related foreign patents).

Now, Therefore,

For good and valuable consideration, the receipt of which is hereby acknowledged, COMPANY does hereby sell, assign, transfer and set over to INTEL, the Patents aforesaid, and any inventions claimed in said Patent, any reissue or reissues of said Patents already granted and which may be granted, any certificates of reexamination already granted and which may be granted the same to be held and enjoyed by INTEL for its own use and enjoyment, and for the use and enjoyment of its successors, assigns or other legal representatives, to the end of the term or terms for which said Patents are or may be granted, reissued or extended as fully and entirely as the same would have been held and enjoyed by COMPANY, if this assignment and sale had not been made; together with all claims for damages by reason of past infringement of said Patents, with the right to sue for, and collect the same for its own use and behalf, and for the use and behalf of its successors, assigns or other legal representatives.

And, COMPANY, hereby authorizes and requests the Commissioner of Patents and Trademarks to issue any and all Letters Patents of the United States on said inventions to INTEL as assignee of the entire interest, and hereby covenants that COMPANY has full right to convey the entire interest herein assigned, and that, except as otherwise provided between the parties, COMPANY has not executed, and will not execute, any agreements in conflict therewith.

In Witness Whereof, the parties, by their duly authorized representatives, have executed this Assignment.

DATE: March 31st, 2003

By: Peter Feist

Title: Chief Executive Officer

A handwritten signature in black ink, appearing to read "Peter Feist", written over a horizontal line.

Signature

Exhibit A
Page 1

EXHIBIT A

Transferred Patents, Patent Applications, Invention Disclosures

Matter Number and Description	Country	Serial Number/Patent Number	Filing Date/Grant Date	Status
034560-001 Method for Compiling High Level Programming Languages	US	5,970,254	Filed 6/27/97 Granted 10/19/99	Granted
034560-002 Method for Compiling High Level Programming Languages Into an Integrated Processor with Reconfigurable Logic	US	5,966,534	Filed 6/27/97 Granted 10/12/99	Granted

Exhibit A
Page 2

Matter Number and Description	Country	Serial Number/Patent Number	Filing Date/Grant Date	Status
034560-012 Behavioral Silicon Construct Architecture and Mapping	US	6,298,472	Filed 5/7/99 Granted 10/2/01	Granted
034560-016 Control Fabric Unit Including Associated Configuration Memory and PSOP State Machine Adapted to Provide Configuration Address to Reconfigurable Functional Unit	US	6,349,346	Filed 9/23/99 Granted 2/19/02	Granted

Exhibit A
Page 3

Matter Number and Description	Country	Serial Number/Patent Number	Filing Date/Grant Date	Status
034560-017 Digital Data Bit Order Conversion Using Universal Switch Matrix Comprising Rows of Bit Swapping Selector Groups	US	6,243,808	Filed 3/8/99 Granted 6/5/01	Granted
034560-018 Local Memory Unit System With Global Access For Use On Reconfigurable Chips	US	6,347,346	Filed 6/30/99 Granted 2/12/02	Granted

Exhibit A

Page 4

Matter Number and Description	Country	Serial Number/Patent Number	Filing Date/Grant Date	Status
034560-030 Configuration Bits Layout	US	09/507,337	Filed 2/18/00	Allowed
034560-034 System And Method For Control Synthesis Using A Reachable States Look-up	US	09/518,553	Filed 3/3/00	Allowed