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3. Nature of conveyance:				
Execution Date: <b>November 12,11,6,5, 2003</b> .	Additional name(s) & address(es) attached? ☐ Yes ☒ No			
Application number(s) or patent number(s):     If this document is being filed together with a new application, the execution date of the application is:     November 12.11.6.5.2003.				
A. Patent Application No.(s)	B. Patent Registration No.(s)			
Additional number(s) att	ached 🗆 Yes 🗵 No			
Name and address of party to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved 1			
Name: Antonelli, Terry, Stout & Kraus, LLP	7. Total fee (37 CFR 3.41)			
Internal Address:	☐ Authorized to be charged to deposit account			
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9. Statement and signature.  To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.  William I. Solomon, Reg# 28,565  Name of Person Signing  Date				
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Total number of pages including cover sheet, attachments, and document: 2 Mail documents to be recorded with required cover sheet information to: Commissioner of Patent & Trademarks, Box Assignments Washington, D.C. 20231

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**PATENT** REEL: 013769 FRAME: 0995

## ASSIGNMENT (譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by HITACHI, LTD. and Eastern Japan Semiconductor Technologies, Inc., corporations organized under the laws of Japan, located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan, and 3-2, Fujihashi 3-chome, Ome-shi, Tokyo, Japan, respectively, receipt of which is hereby acknowledged I do hereby sell and assign to said HITACHI, LTD. and Eastern Japan Semiconductor Technologies, Inc., their successors and assigns, all my right, title and interest, in and for the United States of America, in and to

## FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire owner ship of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD. and Eastern Japan Semiconductor Technologies, Inc., their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD. and Eastern Japan Semiconductor Technologies, Inc.

Signed on the date(s) indicated aside signatures:

INVENTOR(S)			
(発明者フルネームサイン)	)		

RECORDED: 02/12/2003

Date Signed (署名日)

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6) _				
7) _				
8) _		-		
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PATENT REEL: 013769 FRAME: 0996