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| 3.         | Nature of conveyance:  X  |
| 4.         | Application number(s) or patent number(s):    This document is being filed together with a new application:   The execution date(s) of the application is/are:   September 20, 2002     The title is: METHOD AND PROGRAM FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUITS OR   This document is being filed after filing of the application: |
|            | (a) Patent Application No(s)/, filed; or (b) Patent No(s), issued .   |
| 5.         | Name and address of party to whom correspondence concerning document should be mailed: STAAS & HALSEY LLP Our Docket: 1095.1257  Attention: William F. Herbert 700 Eleventh Street, N.W., Suite 500 Washington, D.C. 20001  |
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**PATENT** REEL: 013803 FRAME: 0817

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| METHOD AND PROGRAM FO  | OR DESIGNING SE   | MICONDUCTOR INTEG   | GRATED CIRCUITS   |
| relating to International Patent Application PCT/JP_executed on even date herewith or, if not so executed,   |   | which application for Letters Pat   | tent of the United States was   |
| (a) executed on  | ; (Insert date of   | of execution of application, if not   | concurrent)   |
| (b) filed on;  | N.W., Washi   | ed attorney of STAAS & HALSEY<br>ngton, D.C. 20001 (202/434-1500<br>the specified data, when known.             |   |
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| 1)   | Sep. 20, 2  | 002 Yasıkı  | Seki<br>Jko SEKI  |
| 2)   |   |   |   |
| 3)   |   |   |   |
| 4)   |   |   |   |
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**RECORDED: 02/21/2003** 

PATENT **REEL: 013803 FRAME: 0818**