



Tab settings

102414792

To the Honorable Commissioner of Patents and Trademarks. Please return the attached original documents or copy thereof.

1. Name of conveying party(ies):

Alain Blossé
Krishnaswamy Ramkumar
Prabhuram Gopalan

3.28.03

2. Name and address of receiving party(ies)

Name: CYPRESS SEMICONDUCTOR CORPORATION

Internal Address:

Street Address:

3939 North First Street

City: San Jose State: CA Zip: 95134

Additional name of conveying party(ies) attached? Yes No

3. Nature of conveyance:

- Assignment Merger
 Security Agreement Change of Name
 Other

Execution Date: March 28, 2003

Additional Name(s) & address(es) attached? Yes No

4. Application number(s) or patent number(s):

10402750

If this document is being filed together with a new application, the execution date of the application is: March 28, 2003

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning this document should be mailed:

Name: OKAMOTO & BENEDICTO, LLP

Internal Address:

Street Address: P. O. BOX 641330

City: SAN JOSE State: CA Zip: 95164-1330

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41) \$ 40.00

- Enclosed
 Authorized to be charged to deposit account

8. Deposit account number:

(Attach duplicate copy of this page if paying by deposit account)

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9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Patrick D. Benedicto, Reg. No. 40,909

Patrick D. Benedicto

Name of Person Signing

Signature

March 28, 2003

Date

Total number of pages including cover sheet, attachments, and documents: 3

Mail documents to be recorded with required cover sheet information to: Commissioner of Patents & Trademarks, Box Assignments Washington, D.C. 20231

J1011 U.S. PTO



03/28/03

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the person(s) named below (referred herein as "INVENTOR" whether singular or plural) has sold, assigned, and transferred and does hereby sell, assign, and transfer to **Cypress Semiconductor Corporation**, a **Delaware** corporation, having a place of business at **3939 North First Street, San Jose, CA., 95134**, ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. The entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the provisional application filed under 35 U.S.C. §111(b) or non-provisional application filed under 35 U.S.C. §111(a) and entitled "**GATE ELECTRODE FOR MOS TRANSISTORS**" ("APPLICATION"), the declaration for which was executed on March 28, 2003:

is to be filed herewith
 was filed on _____,
now bearing U.S. serial number _____; and

2. The entire worldwide right, title, and interest in and to:
(a) the APPLICATION; (b) all applications claiming priority from the APPLICATION;
(c) all divisional, continuation, substitute, renewal, reissue, and other related applications thereto which have been or may be filed in the United States or elsewhere in the world;
(d) all patents (including reissues and re-examinations) which may be granted on the applications set forth in (a), (b), (c) and (d) above; and (e) all right of priority in the APPLICATION, together with all rights to recover damages for infringement of provisional rights.

INVENTOR agrees that ASSIGNEE may apply for and receive patents for SUBJECT MATTER in ASSIGNEE's own name.

INVENTOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all assignments, oaths, powers of attorney, applications, and other papers (and/or documents) necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests herein conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the SUBJECT MATTER and for vesting in ASSIGNEE the rights, titles, and interests herein conveyed. INVENTOR further agrees to provide any successor, assign, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

INVENTOR represents that INVENTOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the INVENTOR has not made and will not hereafter make any assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

INVENTOR grants the attorney(s) of record the power to insert on this Assignment any further identification that may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

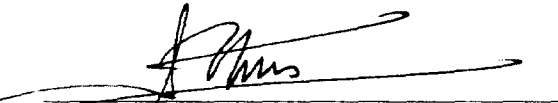
Title of Document: ASSIGNMENT

Re:
Title: GATE ELECTRODE FOR MOS TRANSISTORS
Filed: (if applicable)
Atty. Docket No. 10002.003100 (PM03001)

This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

Name and Signature _____

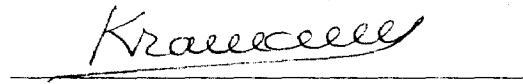
Date of
Signature


Alain Blossé

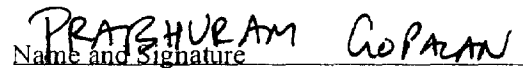
28th March, 2003

Name and Signature _____

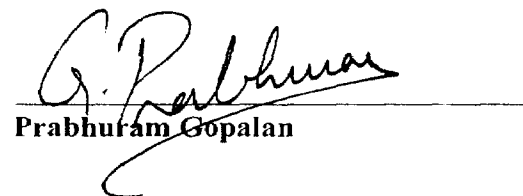
Date of
Signature


Krishnaswamy Ramkumar

23rd March, 2003


Name and Signature

Date of
Signature


Prabhuram Gopalan

28th March 2003