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OMB No. 0651-0011(exp. 4/					
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Name of conveying	party:	11112	2. Name and ac	ddress of receiving party(ies)	
Kazushige HOTT Yoshio KUROSA\ Seii SATO		7.14.0)		FUJITSU DISPLAY TECHNOLOGIES CORPORATION	-
Takuya WATANA			Internal Addr	ess: 1-1, Kamikodanaka 4-chome,	-
Hiroyuki YAEGAS	SHI		Street Addres	ss: <u>Nakahara-ku, Kawasaki-shi,</u>	-
Additional name(s) of conv	veying party(ies) attached? () Yes(X)No	Kanagawa 2	11-8588 Japan	
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4. Application number	r(s) or patent number(s):				
If this document is	being filed together with a	new application, the	execution date of	the application is:	
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10/314,892					
		Additional numbers atta	· ched? () Yes (X)) No	
5. Name and address	of party to whom correspo	ndence	6. Total numbe	er of applications and patents involved:	
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UTILITY PATENT

ASSIGNMENT

FOR VALUE RECEIVED, the receipt of which is hereby acknowledged, the following inventor or inventors (hereinafter called "ASSIGNOR")

Kazushige HOTTA Name: Yoshio KUROSAWA Name: Address:

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hereby assigns, transfers and sets over to:

FUJITSU DISPLAY TECHNOLOGIES CORPORATION Name:

Address: 1-1, Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8588 Japan

(hereinafter called "ASSIGNEE"), the entire worldwide right, title and interest in and to the invention known as:

THIN FILM TRANSISTOR SUBSTRATE AND METHOD OF MANUFACTURING THE SAME

for which a United States Patent Application was executed on even date herewith or, if not so executed, was:

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Date Feb. 10, 2003	Signature of inventor
Date Feb. 10, 2003	Signature of inventor 里澤 紀 推 Yoshio KUROSAWA
Pate Feb. 10, 2003	Signature of inventor
Feb. 10, 2003	Signature of inventor
Date Feb. 10, 2003	Signature of inventor)

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