

05-22-2003



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ION FORM
ETAttorney Docket No.
02-5949/L13.12-0221e United States Patent and Trademark Office: Please record the
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1. Name of conveying party(ies):

- (1) Michael O. Jenkins
(2) Brett D. Hardy
(3) Prashant K. Singh
(4) Donald C. Grillo
(5) Jeffrey S. Kueng

5.21.03

Additional name(s) of conveying party(ies)
attached? [] Yes [] No2. Name and address of receiving
party(ies):Name: LSI Logic CorporationStreet Address: 1551 McCarthy BoulevardCity Milpitas State CA ZIP 95035Additional name(s) & address(es) attached?
[] Yes [] No

3. Nature of Conveyance:

- ☒ Assignment [] Merger
[] Security Agreement
[] Change of Name [] Other

Execution Date: 1) December 18, 2002, 2-5)
December 10, 2002

4A. Application No.(s)

10/281,811, filed October 28, 2002

If this document is being filed together with a
new application, the execution date(s) of the
application is: 1) December 18, 2002, 2-5)
December 10, 2002

Additional numbers attached? [] Yes [] No

4B. Patent No.(s)

Additional numbers attached?
[] Yes [] No5. Name and address of party to whom corres-
pondence concerning document should be mailed:Name: Timothy R. CrollStreet Address: LSI Logic Corporation
1551 McCarthy Boulevard
M/S D-106City Milpitas State CA ZIP 950356. Total number of applications and
patents involved: [1]7. Total fee (37 CFR 3.41):.....\$ 40.00

8. Method of Payment

- [] Enclosed
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To the best of my knowledge and belief, the foregoing information is true and correct and any
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David D. Brush
Name of Person Signing

Signature

Date

May 16, 2003

Total number of pages submitted: [4]

PATENT
REEL: 014081 FRAME: 0785

**PATENT RECORDATION FORM
COVER SHEET**

01-10-2003

cket No.
3.12-0221



102334602

To the Honorable Director of the United States Patent and Trademark Office
attached original document or copy thereof.

: Please record the

1. Name of conveying party(ies):

- (1) Michael O. Jenkins
- (2) Brett D. Hardy
- (3) Prashant K. Singh
- (4) Donald C. Grillo
- (5) Jeffrey S. Kueng

1-6-03

Additional name(s) of conveying party(ies)
attached? [] Yes [] No

2. Name and address of receiving
party(ies):

Name: LSI Logic Corporation

Street Address: 1551 McCarthy Boulevard

City Milpitas State CA ZIP 95035

Additional name(s) & address(es) attached?
[] Yes [] No

3. Nature of Conveyance:

- ☒ Assignment [] Merger
- ☐ Security Agreement
- ☐ Change of Name [] Other _____

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Additional numbers attached?
[] Yes [] No

FINANCE SECTION
NOV 10 2003
AM 7:36

5. Name and address of party to whom corres-
pondence concerning document should be mailed:

Name: Timothy R. Croll
Street Address: LSI Logic Corporation
1551 McCarthy Boulevard
M/S D-106

City Milpitas State CA ZIP 95035

6. Total number of applications and
patents involved: [1]

7. Total fee (37 CFR 3.41):.....\$ 40.00

8. Method of Payment

- ☐ Enclosed
- ☒ The Director is authorized to
charge payment of any additional
recording fees or credit any
overpayment to deposit account
No. 12-2252.

01/09/2003 TBIAZ1 00000230 122252 D.D.BRUSH
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9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any
attached copy is a true copy of the original document.

David D. Brush
Name of Person Signing

Signature

12/30/02
Date

Total number of pages submitted: [4]

ASSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, we, the undersigned Michael O. Jenkins, Brett D. Hardy, Prashant K. Singh, Donald C. Grillo and Jeffrey S. Kueng, do hereby sell, assign, and transfer to **LSI LOGIC CORPORATION**, a corporation of Delaware, having a place of business at 1551 McCarthy Boulevard, Milpitas, California 95035 ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements which are disclosed in the application for United States Letters Patent,

[] which has been executed by the undersigned concurrently herewith,
[x] which was filed on October 28, 2002 and assigned Application No. 10/281,811,

and is entitled: ESD PROTECTION CIRCUIT FOR LOW AMPLITUDE SIGNALS and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and

Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will executed all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; executed all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assigned, its successors, assigns, and representatives, all facts known to the undersigned relating to said improvements and the history thereof; and generally do everything possible which said Assignee, its successors, assigns or representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications to patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives; and

Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant mortgage, license, or other agreement affecting the rights and properly herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Name: 
Michael O. Jenkins

Date 18 DEC 02

Witness: 

Date Dec. 18, 2002

Witness
Printed: Connie del Castillo

Witness
Address: LSI Logic Corporation
Intellectual Property
Corporate Legal Department
1551 McCarthy Blvd., MS D-106
Milpitas, California 95035

Name: Brett D. Hardy
Brett D. Hardy

Date 12/10/02

Witness: Charles R Wait

Date 12/10/2002

Witness
Printed: Charles R Wait

Witness
Address: 7008 Lanhon Lane
Edina, Mn 55439

Name: Prashant K. Singh
Prashant K. Singh

Date 12/10/2002

Witness: Charles R Wait

Date 12/10/2002

Witness
Printed: Charles R Wait

Witness
Address: 7008 Lanhon Lane
Edina, Mn 55439

Name: Donald C Grillo
Donald C. Grillo

Date 12/10/2002

Witness: Charles R Wait

Date 12/10/2002

Witness
Printed: Charles R Wait

Witness
Address: 7008 Lanhon Lane
Edina, Mn 55439

Name: Jeffrey S. Kueng
Jeffrey S. Kueng

Date 12/10/02

Witness: Charles R. Wait

Date 12/10/02

Witness
Printed: Charles R. Wait

Witness
Address: 7008 Lanham Lane
Edina, Mn 55439



UNITED STATES
PATENT AND
TRADEMARK OFFICE

02-5949

MAY 09, 2003

PTAS

Under Secretary of Commerce For Intellectual Property and
Director of the United States Patent and Trademark Office
Washington, DC 20231
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LSI LOGIC CORPORATION
TIMOTHY R. CROLL
1551 MCCARTHY BOULEVARD
M/S D-106
MILPITAS, CA 95035



102334602A

UNITED STATES PATENT AND TRADEMARK OFFICE
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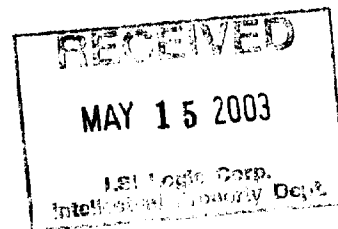
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