

05-23-2003



To the Honorable Commissioner of Pat.

102456100

Attached original documents or copy thereof.

1. Name of conveying party(ies):

5-14-03

Mississippi State University

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance:

- ☐ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☒ Other Confirmatory License

Execution Date(s): April 3, 2003☒ This instrument should be recorded on the governmental register in accordance with 37 CFR 7.1(e)(1).

2. Name and address of receiving party(ies):

Name: United States Air ForceInternal Address: AFMC LO/JAZStreet Address: 2240 B Street, Room 100City: Wright-Patterson Air Force BaseState: OhioZip: 45433-7109Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: _____

A. Patent Application No.(s):

09/796,490

B. Patent No.(s):

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Fredric L. SinderInternal Address: AFMC LO/JAZStreet Address: 2240 B Street, Room 100City: Wright-Patterson Air Force BaseState: Ohio ZIP: 45433-7109

6. Total number of applications and patents involved:

17. Total fee (37 CFR 3.41)\$ 0☐ Enclosed ☒ Not applicable**The instrument to be recorded is not an instrument affecting title (37 CFR 3.41(b)).**

8. Deposit account number:

(Attach duplicate copy of this page if paying by deposit account)

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9. Statement and signature.

*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*Fredric L. Sinder

Name of person signing

Signature

April 30, 2003

Date

Total number of pages including cover sheet, attachments, and document:



License to the United States Government

Invention Title: Complementary Accumulation-mode JFET Integrated Circuit Topology Using Wide (> 2 eV) Bandgap Semiconductors

Inventor(s): Michael Mazzola, Benjamin Blalock, Stephen Sadow, and Jeffrey Casady

Patent or Application Serial Number: 09/796,490

U.S. Filing Date: March 2, 2001

Grant/Contract Identification Number(s): F33615-00-C-2010

Foreign Applications filed/intended in (countries): PCT/US02/06431, Filed March 4, 2002

The invention identified above is a Subject Invention under **35 U.S.C. 200, et seq.**, and the Standard Patent Rights clause at **37 CFR 401.14 or FAR 52.227-11**, which are included among the terms of the above-identified grant/contract award from the United States Government. This document is confirmatory of:

1. The nonexclusive, nontransferable, irrevocable, paid-up license to practice or have practiced for or on behalf of the United States the invention described in patent application and in any and all divisions, continuations, and continuations in part, and in any and all patents and re-issues granted thereon throughout the world; and
2. All other rights acquired by the Government by reason of the above identified grant/contract award and the laws and regulations which are applicable to the award.

The Government is hereby granted an irrevocable power to inspect and make copies of the above-identified patent application.

Signed this 3rd day of April, 20 02.

By: Charles T. Rivenburgh
Charles T. Rivenburgh
(Name of Grantee./Contractor Official)

Title: Director

For: MISSISSIPPI STATE UNIVERSITY
(Organization)

At: Office of Intellectual Property and Technology Licensing
P.O. Box 5282
Mississippi State, MS 39762
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