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ATTORNEY DOCKET NO: P8987



SHEET

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To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

(a) Umesh Nair

(b)

(c)

Additional name(s) of conveying party(ies) attached?

☒ Yes ☐ No

2. Name and address of receiving party:

Name: Sun Microsystems, Inc.Street Address: 4120 Network Circle, MS SCA 12-203City: Santa Clara State: California Zip: 95054Country: USA

3. Nature of Conveyance:

☐ Assignment☐ Merger☐ Security Agreement☐ Change of Name☐ Other _____Execution Date: June 19, 2003

Name and address of receiving party(ies):

Name: _____

Street Address: _____

City: _____ State: _____ Zip: _____

Country: _____

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: June 20, 2003A. Patent Application No.(s) - 10600740Title: System for Optimizing Buffers in Integrated Circuit Design Timing Fixes

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☐ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Stephen A. TerrileInternal Address: HAMILTON & TERRILE, LLPStreet Address: P.O. BOX 203518City AUSTIN State TX ZIP 787206. Total number of applications and patents involved: 17. Total fee (37 CFR 3.41): \$40.00☒ Check for Fee Enclosed☒ Charge Deposit Account 502264 for any additional fees required for this conveyance and credit deposit account 502264 any amounts overpaid

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8. Statement and signature.

*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*Stephen A. Terrile (32,946)

Name of Person Signing


Signature6/20/03
DateTotal number of pages including cover sheet, attachments and documents: 1

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Attorney Docket No.: P8987

ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, we

Umesh Nair of Newark, California

hereby sell, assign and transfer to Sun Microsystems, Inc., a Delaware corporation, having a place of business at 4120 Network Circle, MS SCA12-203, Santa Clara, California 95054, its successors and assigns, the entire right, title and interest throughout the world in our invention in:

**SYSTEM FOR OPTIMIZING BUFFERS IN INTEGRATED
CIRCUIT DESIGN TIMING FIXES**

for which we have executed a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; we authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from us; we agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, their successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in all countries; and we request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Executed this 19th day of JUNE, 2003.



Umesh Nair