

07-11-2003



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COVER SHEET

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PATENTS ONLY

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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Zarlink Semiconductor Inc.

Additional name(s) of conveying party(ies) attached?  Yes  No

3. Nature of conveyance:

- Assignment
- Security Agreement
- Other
- Merger
- Change of Name

Execution Date: February 22, 2002

2. Name and address of receiving party(ies)

Name: DALSA Semiconductor Inc.

Address: 605 McMurray Road

City: Waterloo State: ON

Country: Canada Zip: N2V 2E9

Additional name(s) & address(es) attached?  Yes  No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: \_\_\_\_\_

A. Patent Application No. Filing Date  
09/799,494 March 7, 2001

B. Patent No.(s)

Additional numbers attached?  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Marks & Clerk

Address: P.O. Box 957, Station B

City: Ottawa State: ON

Country: Canada Zip: K1P 5S7

Customer No: 23553



6. Total number of applications and patents involved:

7. Total fee (37 CFR 3.41).....\$ 40.00

- Enclosed
- Authorized to be charged to deposit account

8. Deposit account number:

13-1717

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9. Signature.

Richard J. Mitchell  
Name of Person Signing

34,519  
Reg No.

Signature

July 4, 2003  
Date

Total number of pages including cover sheet, attachments, and documents: **12**

Mail documents to be recorded with required cover sheet information to:  
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Washington, D.C. 20231

07/10/2003 ECD/PER 00000085 09799494

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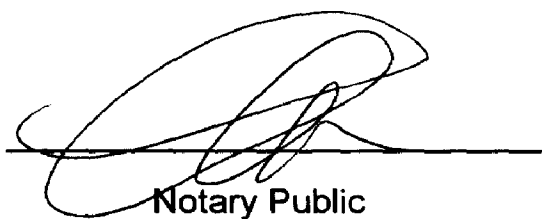
# PROVINCE OF ONTARIO

I, Mary Coleen Morrison, a Notary Public, in and for the Province of Ontario, by Royal Authority duly appointed, at the City of Ottawa, Regional Municipality of Ottawa-Carleton, in said Province,

Do certify and attest that the paper-writing hereto annexed is a true copy of a document purporting to be a Patent Assignment transferring the rights as listed on the attached Schedule A, from Zarlink Semiconductor Inc. to DALSA Semiconductor Inc., the said copy having been compared by me with the said original document, an act whereof being requested I have granted under my Notarial Form and Seal of Office to service and avail as occasion shall or may require.

In Testimony whereof I have hereto subscribed my name and affixed my Notarial Seal of Office at the City of Ottawa

this 9<sup>th</sup> day of August, 2002

  
Notary Public

PATENT ASSIGNMENT

WHEREAS Zarlink Semiconductor Inc. (hereinafter the "Assignor"), whose full post address is 400 March Road, Ottawa, Ontario, K2K 3H4, is the owner of the patents, patent applications and inventions set out in Schedule "A" hereto (hereinafter the "Patents").

AND WHEREAS DALSA Semiconductor Inc. (hereinafter the "Assignee") whose full post office address is 605 McMurray Road, Waterloo, Ontario N2V 2E9, is desirous of acquiring all of the Assignor's worldwide right, title and interest in and to the Patents;

NOW THEREFORE, in consideration of ten dollars (\$10.00), and other good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, the Assignor hereby irrevocably and unconditionally sells, assigns, transfers and conveys to the Assignee, all of its right, title and interest in Canada, the United States and all other jurisdictions throughout the world in and to the Patents and the invention or inventions described therein, and including: its corresponding right to claim priority from the Patents in any country or jurisdiction; all continuations, continuations-in-part, divisionals, re-examinations, re-issues, extensions and renewals of the Patents; its corresponding right, title and interest in and to any patents which may issue from the Patents and all rights of action appurtenant thereto, including without limitation all rights of action in respect of past infringement, all Patents to be held and enjoyed by the Assignee to the full end of the term for which the Patents are granted, as fully and entirely as the Patents could have been held and enjoyed by the Assignor if this Patent Assignment had not been made.

The Assignor hereby covenants and agrees that it will, without further consideration, and as may be requested by the Assignee from time to time, communicate with Assignee, its successors and assigns, any facts known to it respecting the Patents, sign all lawful papers when called upon to do so, execute and deliver any and all papers that may be necessary or desirable to perfect the title to the Patents in the Assignee, its successors or assigns, make all rightful oaths and generally do everything possible to aid the Assignee, to obtain full and complete assignment of the Patents.

SIGNED at Montréal, Québec , this 22<sup>nd</sup> day of February, 2002

ZARLINK SEMICONDUCTOR INC

Per: [Signature] Witness: [Signature]

Name: SS Carrigan Don McElyre

Title: Sr. VP. S.VP

Who has the authority to bind the Corporation.

The undersigned Assignee hereby accepts this Assignment

Per: *Savvas Chamberlain*

Witness: *Robert W. B.*

Name: SAVVAS CHAMBERLAIN

Title: CEO

Who has the authority to bind the Corporation.

**SCHEDULE "A"**

Bromont-Related Patent-Information

1064070 v. 3

**Bromont-Related Patents/Patent Applications  
(To Be Assigned)**

Zarlink #	Country	Title	Inventors	Filed	Serial #	Patent #	Issue Date
S-173	United Kingdom	Method of Aligning a Photolithographic Mask to a Crystal Plane	Robert ANTAKI...	8/12/2000	0029929.7		
S-173	United States	"	"	7/3/2001	09/799494		
S-173	PCT	"	"	3/12/2001	PCT/CA01/01723		
S-173	Taiwan	"	"	7/12/2001	90130415		
S-176	United Kingdom	Method of Making A Functional Device with Deposited Layers Subject to High Temperature Anneal	Luc OUELLET...	25/11/00	0028822.5		
S-176	United States	"	"	7/3/2001	09/799491		
S-176	PCT	"	"	22/11/2001	PCT/CA01/01659		
S-176	Taiwan	"	"	23/11/2001	90129092		
S-184	United Kingdom	Manufacture of Silica Waveguides with Minimal Absorption	Luc OUELLET...	12/12/2000	0030236.4		
S-184	United States	"	"	7/3/2001	09/799496		
S-184	PCT	"	"	6/12/2001	PCT/CA01/01753		
S-184	Taiwan	"	"	11/12/2001	90130656		
S-194	United Kingdom	Method of Fabricating Capacitors in Semiconductor Devices	Luc OUELLET...	23/1/98	9801322.0		
S-194	Korea	"	"	22/1/99	1977/1999		
S-194	Japan	"	"	25/1/99	11-16035		
S-194	United States	"	"	25/1/99	09/236101	6268620	7/31/2001
S-194	Canada	"	"	20/1/99	2259725		
S-196	United States	Method of Depositing Dielectric Film	Stéphane BLAIN...	5/2/2001	09/775465		
S-196	Canada	"	"	1/2/2001	tbd		
S-216	United States	Optical Quality Silica Films	Luc OUELLET...	13/4/01	09/833711		
S-220	United States	Manufacture of Integrated Fluidic Devices	Luc OUELLET...	27/04/01	09/842836		
S-221	United States	Method of Depositing Optical Films	Luc OUELLET...	31/05/01	09/867662		
S-224	United States	Micro-Fluidic Devices	Luc OUELLET...	7/24/2001	09/910795		
S-228	United States	Method of Making High-Voltage Bipolar CMOS/DMOS Devices	Luc OUELLET...	9/28/2001	09/964,472		

**Bromont-Related Patents/Patent Applications  
(To Be Assigned)**

Zarlink #	Country	Title	Inventors	Filed	Serial #	Patent #	Issue Date
S-232	United States	Method of Aligning Structures on Opposite Sides of a Wafer	Luc OUELLET	8/8/2001	09/923367		
S-234	United States	Method of Depositing an Optical Quality Silica Film by PECVD	Luc OUELLET	9/21/2001	09/956916		
S-238	United States	Method of Reducing Stress-Induced Mechanical Problems in Optical Components	Luc OUELLET...	11/10/2001	09/973778		
S-248	United States	Method of Making Specular Infrared Mirrors for Use in Optical Devices	Luc OUELLET...	11/16/2001	09/987829		
S-261	United States	Low Absorption Integrated Optical Devices	Luc OUELLET...	1/31/2002	tbd		
S-11189	Canada	Chemical Vapor Deposition Apparatus with Manifold Enveloped by Cooling Means	Guy BRIEN...	17/05/85	481854	1251100	14/03/89
S-11189	United Kingdom	"	"	18/10/85	8525789	2175011	11/11/1989
S-11189	United States	"	"	6/4/1987	35032	4747368	31/05/88
S-11118	Canada	Optical Sensor	Aktik Metin	3/24/1986	504872	1269164	5/15/1990
S-11118	United Kingdom	"	"	1/30/1987	8702075	2188482	3/7/1990
S-11118	United States	Photosensitive Diode Element & Array	"	12/14/1988	284818	4866499	9/12/1989
S-11118	United States	Optical Sensor	"	7/31/1989	386623	4965212	10/23/1990
S-111143	Canada	Curing and Passivation of Spin on Glasses by a Plasma Process	Luc OUELLET	31/05/89	601333	1339817	14/04/98
S-111143	Germany	"	"	27/04/90	P4013449.0	4013449	18/04/96
S-111143	Japan	"	"	25/05/90	136887/90	1999234	29/03/95
S-111143	Korea	"	"	20/04/90	905541	81978	3/2/1995
S-111143	Taiwan	"	"	1/6/1990	79104408	47196	3/9/1991
S-111143	United Kingdom	"	"	20/04/90	9008943.4	2235444	16/12/92
S-111143	United States	"	"	19/11/91	794789	5270267	14/12/93
S-111160	Canada	Low Profile Inker	Normand Nadeau	6/24/1988	570329	1303901	6/23/1992
S-111160	United States	"	"	5/8/1989	348503	4992729	2/12/1991