

Form PTO-1595  
(Rev. 10/02)

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OMB No. 0651-0027 (exp. 6/30/2005)

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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):  
National Semiconductor Corporation  
by John M. Clark III, Sr. V.P. and Secretary

Additional name(s) of conveying party(ies) attached?  Yes  No

3. Nature of conveyance:  
 Assignment  Merger  
 Security Agreement  Change of Name  
 Other \_\_\_\_\_

Execution Date: 08/21/2003

2. Name and address of receiving party(ies)  
Name: ADVANCED MICRO DEVICES, INC.  
Internal Address: \_\_\_\_\_  
Street Address: PO BOX 3453  
ONE AMD PLACE  
City: SUNNYVALE State: CA Zip: 94088-3553

Additional name(s) & address(es) attached?  Yes  No

4. Application number(s) or patent number(s):  
If this document is being filed together with a new application, the execution date of the application is: \_\_\_\_\_  
A. Patent Application No.(s) 10/336,137  
Atty Dkt No.: 1458-P03913D01

Additional numbers attached?  Yes  No

B. Patent No.(s) \_\_\_\_\_


5. Name and address of party to whom correspondence concerning document should be mailed:  
Name: J. Gustav Larson  
Internal Address: TOLER, LARSON & ABEL, LLP  
P.O. Box 29567  
Street Address: 5000 Plaza on the Lake  
Suite 265  
City: Austin State: TX Zip: 78746

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41).....\$ 40.00  
 Enclosed  
 Authorized to be charged to deposit account

8. Deposit account number:  
01-0365

**DO NOT USE THIS SPACE**

9. Signature.  
J. Gustav Larson, Reg. No. 39,263  
Name of Person Signing  Signature 1-16-04 Date

Total number of pages including cover sheet, attachments, and documents: 4

Mail documents to be recorded with required cover sheet information to:  
Commissioner of Patents & Trademarks, Box Assignments  
Washington, D.C. 20231

USPTO Assignment Branch Fax No.: 703-306-5995

CH \$40.00 010365 10336137

### ASSIGNMENT OF PATENT RIGHTS

This ASSIGNMENT OF PATENT RIGHTS, dated August 21, 2003 (this "Agreement"), is entered into by National Semiconductor Corporation and its Subsidiaries (as defined below) (collectively, "Assignor"), with and for the benefit of Advanced Micro Devices, Inc. ("Assignee"). For the purpose of this Agreement, "Subsidiary" means, with respect to a party, any corporation, company, or other entity, more than fifty percent (50%) of whose outstanding shares or securities (representing the right to vote for the election of directors or other managing authority) are, now or hereafter, owned or controlled, directly or indirectly by a party hereto, but such corporation, company or other entity shall be deemed to be a "Subsidiary" only so long as such ownership or control exists.

WHEREAS, Assignor has agreed to sell and assign, and Assignee has agreed to buy and acquire all of Assignor's rights, title and interests in and to the Assigned Patent Applications set forth in Exhibit A attached hereto (the "Assigned Patent Applications").

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, Assignor hereby assigns and transfers to Assignee any and all worldwide rights, title and interests Assignor holds, or may hold, in and to the Assigned Patents, together with all rights derived therefrom, including but not limited to the right to sue for and collect damages for past, present and future infringement.

Assignor further agrees that, should additional or further documentation of the assignment be required for whatever reason, Assignor will, without further consideration, provide or execute such other information or documents as may be necessary upon Assignee's reasonable request.

This Agreement shall be binding on and shall inure to the benefit of, the parties hereto and their respective successors and assigns. This Agreement will be governed by, and construed in accordance with, the internal laws of the State of California applicable to contracts executed and performed entirely therein, without regard to the principles of choice of law or conflicts or law of any jurisdiction. If any term or other provision of this Agreement is invalid, illegal or incapable of being enforced by any rule of law or public policy, all other conditions and provisions of this Agreement will nevertheless remain in full force and effect so long as the economic or legal substance of the transactions contemplated hereby is not affected in any manner materially adverse to any party. Upon such determination that any term or other provision is invalid, illegal or incapable of being enforced, the parties hereto will negotiate in good faith to modify this Agreement so as to effect the original intent of the parties as closely as possible in a mutually acceptable manner in order that the transactions contemplated hereby be consummated as originally contemplated to the greatest extent possible. This Agreement may be executed in one or more counterparts, and by the different parties hereto in separate counterparts, each of which when executed will be deemed to be an original but all of which taken together will constitute one and the same agreement.

IN WITNESS WHEREOF, Assignor has caused this Assignment of Patent Rights to be executed by its duly authorized representatives effective as of the date first written above.

NATIONAL SEMICONDUCTOR CORPORATION

By: [Signature]  
Name: John M. Clark III  
Title: Senior Vice President and Secretary

STATE OF California  
COUNTY OF Santa Clara

On this 21st day of August, 2003, before me, a Notary Public in and for said State, personally appeared John M. Clark III personally known to me (or proved to me on the basis of satisfactory evidence) to be the person(s) whose name(s) is are subscribed to the within instrument and acknowledged to me that he she/they executed the same in his her/their authorized capacity(ies), and that by his her/their signature(s) on the instrument the person(s), or entity upon behalf of which the person(s) acted, executed the instrument.

WITNESS, my hand and official seal.

[Signature]  
Notary Public

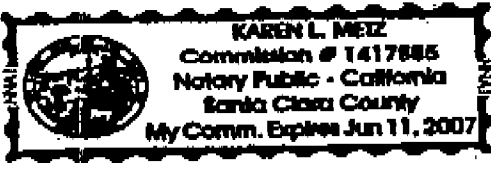


EXHIBIT A

<i>Docket Number</i>	<i>Sub Case</i>	<i>Title</i>	<i>Inventors</i>	<i>Status</i>	<i>File Date</i>	<i>Serial No.</i>	<i>Issue Date</i>	<i>Pat Number</i>
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P03913D01		COMPUTER-BASED SELECTIVE MEMORY ARCHITECTURE FOR SYSTEM ON A CHIP	KHAN EMDADUR RICHMOND TODD WILLIAM	Filed	01/03/2003	10/336137		
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