

07-22-2003

2001 Staas &amp; Halsey LLP

To the Honorable Commissioner of



102505588

ned original documents or copy thereof.

1. Name of conveying party  
Koichiro ISHIBASHI and Takahiro YAMASHITA

7-15-03

2. Name and Address of receiving party(ies):

SEMICONDUCTOR TECHNOLOGY ACADEMIC RESEARCH CENTER  
17-2, Shin Yokohama 3-chome  
Kouhoku-ku, Yokohama-shi  
Kanagawa 222-0033  
JAPAN

3. Nature of conveyance:

☒ Assignment ☐ Merger  
☐ Security Agreement ☐ Change of Name  
☐ Other: \_\_\_\_\_

Execution Date(s): July 1, 2003

4. Application number(s) or patent number(s):

10618710

☒ This document is being filed together with a new application:

(a) The execution date(s) of the application is/are: July 1, 2003

(b) The title is: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING BODY BIASING  
CIRCUIT FOR GENERATING FORWARD WELL BIAS VOLTAGE OF SUITABLE  
LEVEL BY USING SIMPLE CIRCUITRY

OR

☐ This document is being filed after filing of the application:

(a) Patent Application No(s). \_\_\_\_\_ / \_\_\_\_\_, filed \_\_\_\_\_; or

(b) Patent No(s). \_\_\_\_\_, issued \_\_\_\_\_

5. Name and address of party to whom correspondence concerning document should be mailed:

STAAS & HALSEY LLP

Our Docket: 1687.1004

Attention: H. J. Staas

1201 New York Ave., N.W., Suite 700

Washington, D.C. 20005

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41)..... (\$ 40.00 per Assignment)

☒ Enclosed

☐ Authorized to be charged to deposit account.

8. Deposit Account No.: 19-3935 (Any underpayment is authorized to be charged to this Deposit Account)  
(Attach duplicate copy of this page if paying by deposit account)

9. Statement and signature.

*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*

H. J. Staas, Reg. No. 22,010

Name of Person Signing

Signature

July 15, 2003

Date

Total number of pages including cover sheet: 2

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07/17/2003 WA0000059 10618710

03 FC:8021

40.00 DP

PATENT  
REEL: 014280 FRAME: 0521

16591 U.S. PTO  
10/6/03  
07/15/03

# U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter "ASSIGNOR") by

(Insert  
ASSIGNEE's  
Name(s)  
Address(es))

Semiconductor Technology Academic Research Center

17-2, Shin Yokohama 3-chome, Kouhoku-ku, Yokohama-shi,

Kanagawa 222-0033 Japan

(hereinafter "ASSIGNEE"), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

(Title of  
Invention)

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING BODY BIASING CIRCUIT

FOR GENERATING FORWARD WELL BIAS VOLTAGE OF SUITABLE LEVEL BY USING

SIMPLE CIRCUITRY

relating to International Patent Application PCT/JP \_\_\_\_\_/\_\_\_\_\_ and/or for which application for Letters Patent of the United States was executed on even date herewith or, if not so executed, was:

(Insert date  
of execution  
of application,  
if not  
concurrent)

(a) executed on \_\_\_\_\_;  
(b) filed on \_\_\_\_\_,  
Serial No. \_\_\_\_\_/\_\_\_\_\_;

(\_\_\_\_\_) is hereby authorized to insert in (b) the specified data, when known.

and to said application and all Letters Patent(s) of the United States granted on said application and any continuation, division, renewal, substitute, reissue or reexamination application based thereon, for the full term or terms for which the said Letters Patent(s) may be granted and including any extensions thereof (collectively, hereinafter, "said application(s) and Letters Patent(s)").

The ASSIGNOR agree(s), when requested by said ASSIGNEE and without charge to but at the expense of said ASSIGNEE, to do all acts which the ASSIGNEE may deem necessary, desirable or expedient, for securing, maintaining and enforcing protection for said invention, including in the preparation and prosecution of said application(s) and the issuance of said Letters Patent(s), in any interference, reissue, reexamination, or public use proceeding, and in any litigation or other legal proceeding which may arise or be declared in relation to same, such acts to include but not be limited to executing all papers, including separate assignments and declarations, taking all rightful oaths, providing sworn testimony, and obtaining and producing evidence.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

(Signatures)

1) <u>Koichiro Ishibashi</u>	<u>Koichiro Ishibashi</u>	<u>July 1, 2003</u>
(SIGNATURE)	(TYPE NAME)	(DATE)
2) <u>Takahiro Yamashita</u>	<u>Takahiro Yamashita</u>	<u>July 1, 2003</u>
(SIGNATURE)	(TYPE NAME)	(DATE)
3) _____	_____	_____
(SIGNATURE)	(TYPE NAME)	(DATE)
4) _____	_____	_____
(SIGNATURE)	(TYPE NAME)	(DATE)
5) _____	_____	_____
(SIGNATURE)	(TYPE NAME)	(DATE)
6) _____	_____	_____
(SIGNATURE)	(TYPE NAME)	(DATE)
7) _____	_____	_____
(SIGNATURE)	(TYPE NAME)	(DATE)
8) _____	_____	_____
(SIGNATURE)	(TYPE NAME)	(DATE)

RECORDED: 07/15/2003

PATENT  
REEL: 014280 FRAME: 0522