Form PTO-1595 08 - 13	3 - ZUU-3 U.S. DEPARTMENT OF COMMERCE
(Rsev. 03/01)	ET U.S. Patent and Trademark Office
7 9 0	
OMB No. 0651-0027 (exp. 5/31/2002)	523328 "
Tao settings ⇒ ⇒	ks: Please record the attached original documents or copy thereof.
Name of conveying party(ies):	Name and address of receiving party(ies)
D.S.P.C. TECHNOLOGIES LTD.	Name: INTEL CORPORATION
	Internal Address:
Additional name(s) of conveying party(les) attached? ☐Yes ☑No	
3. Nature of conveyance:	
	Street Address: 2200 Mission College Blvd.
☐ Security Agreement ☐ Change of Name	
Other	Santa Sunta
	City: Santa State: CA Zip: 95052
Execution Date: May 1,2003	Additional name(s) & address(es) attached? ☐ Yes ☑ No
 Application number(s) or patent number(s): If this document is being filed together with a new applic 	ation, the execution date of the application is: August 4,2003
A. Patent Application No.(s)	B. Patent No.(s)
	J. Tatom No.(c)
10/622843	
Additional numbers	attached? ☐ Yes ☒ No
5. Name and address of party to whom correspondence	6. Total number of applications and patents involved: 1
concerning document should be mailed:	
Name: Eitan, Pearl, Latzer & Cohen Zedek, LLP.	7. Total fee (37 CFR 3.41)\$ 40
Internal Address:	☐ Enclosed
	Authorized to be charged to deposit account
Charact Addresses 10 Deals Siller Disease	8. Deposit account number:
Street Address: 10 Rockefeller Plaza Suite 1001	05-0649
City: New York State: NY Zip: 10020	(Attach duplicate copy of this page if paying by deposit account)
DO NOT US	SE THIS SPACE
9. Statement and signature.	
To the best of my knowledge and belief, the foregoing in	formation is true and correct and any attached copy
is a true copy of the original document.	
Guy Yonay	August 4, 2003
Name of Person Signing	Signature Date
	ver sheet, attachments, and documents: [10]
	rith required cover sheet information to: & Trademarks, Box Assignments
Washing	ton, D.C. 20231

08/12/2003 LMJELLER 00000281 050649 10632843

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SSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, each undersigned inventor has sold and assigned, and by these presents hereby sells and assigns, unto D.S.P.C. TECHNOLOGIES LTD.

name and address of assignee

11 Ben Gurion Street Givat Shmuel 51905 Israel

(hereinafter ASSIGNEE) all right, title and interest for the United States, its territories and possessions in and to this invention relating to

A CHANNEL ESTIMATOR title of invention as set forth in this United States Patent Application check one executed concurrently herewith executed on Serial No. 09/438,475 filed November 12, 1999

in and to said United States Patent Application including any and all divisions or continuations thereof and in and to any and all Letters Patent of the United States which may issue on any such application or for said invention, including any and all reissues or extensions thereof, to be held and enjoyed by said ASSIGNEE, its successors, legal representatives and assigns to the full end of the term or terms for which any and all such Letters patent may be granted as fully and entirely as would have been held and enjoyed by the undersigned had this Assignment not been made:

Each of the undersigned hereby authorizes and requests the Commissioner of Patents and Trademarks to issue

any and all such Letters patent to said ASSIGNEE, its successors or assigns in accordance herewith;

Each of the undersigned warrants and covenants that he has the full and unencumbered right to sell and assign the interests herein sold and assigned and that he has not executed and will not execute any document or instrument in conflict herewith:

Each of the undersigned further covenants and agrees he will communicate to said ASSIGNEE, its successors, legal representatives or assigns all information known to him relating to said invention or patent application and that he will execute and deliver any papers, make all rightful oaths, testify in any legal proceedings and perform all other lawful acts deemed necessary or desirable by said ASSIGNEE, its successors, legal representatives or assigns to perfect title to said invention, to said application including divisions and continuations thereof and to any and all Letters Patent which may be granted therefor or thereon, including reissues or extensions, in said ASSIGNEE, its successors, or assigns or to assist said ASSIGNEE, its successors, legal representatives or assigns in obtaining, reissuing or enforcing Letters Patent of the United States for said invention;

Each of the undersigned hereby grants the firm of Eitan, Pearl, Latzer & Cohen-Zedek the power to insert in this Assignment any further identification which may be necessary or desirable to comply with the rules of the U.S. Patent and Trademark Office for recordation of this Assignment.

NAMES AND SIGNATURES OF INVENTORS				
Name: YELLIN, Daniel	Signature:	Date: & Feb. 12, 2000		
Name: RAINISH, Doron	Signature: ×	Date: FRB. 17, 2000		
Name: ASHKENAZI, Rony	Signature:	Date: y Feb 17, 2000		
Name:	Signature:	Date: \('		
NAMES AND SIGNATURES OF WITNESSES				
Name: V Dayed Ben Hain	n Signature: P.B.H	Date: > 17th Feb 2000		
Name: > hny Sakson	<u> </u>	Date: 14th. Feb 2000		
Note: Prima facile evidence of execution n	nay be optionally obtained by execution of this docu	ument before a U.S. Consul or before a loc		

officer authorized to administer oaths whose authority is proved by a certificate from a U.S. Consul.

Assignment of Legal Title to Patents

Whereas, D.S.P.C. Technologies Ltd., (hereinafter ASSIGNOR) is the sole and

exclusive owner of certain patent applications listed in Exhibit A annexed hereto

(collectively referred to as the "Patents"); and

Whereas Intel Corporation, a Delaware corporation, with an office at 2200

Mission College Blvd., California 95052, (hereinafter INTEL) is desirous of acquiring

bare legal title to and under the Patents for the sole purpose of registering the Patents in

the name of INTEL in the U.S. Patent Office; and

Whereas ASSIGNOR is a subsidiary of INTEL.

Now, Therefore,

For good and valuable consideration, the receipt of which is hereby acknowledged,

ASSIGNOR does hereby transfer to INTEL, bare legal title to the Patents, and bare legal

title to any inventions claimed in said Patents, any reissue or reissues of said Patents

already granted and which may be granted, and any certificates of reexamination already

granted and which may be granted, the bare legal title to same to be held by INTEL, to

the end of the term or terms for which said Patents are or may be granted, reissued or

extended as fully and entirely as such bare legal title would have been held and enjoyed

by ASSIGNOR if this assignment had not been made. Nothing in this Assignment of

Legal Title to Patents shall be construed as transferring to Intel beneficial ownership of

the Patents, which beneficial ownership, including the right to use, license, divide, exploit

and dispose of the rights to and under such Patents (other than bare legal title), shall

continue to be held by ASSIGNOR.

ASSIGNOR, hereby authorizes and requests the Commissioner of Patents to issue

any and all Letters Patents of the United States on said inventions to INTEL as assignee

of bare legal title to the Patents, and hereby covenants that ASSIGNOR has full right to

convey the legal title herein assigned, and that, except as otherwise provided between the

1

parties, ASSIGNOR has not executed, and will not execute, any agreements in conflict therewith.

ASSIGNOR and INTEL hereby agree that bare legal title to the Patents transferred under this agreement shall revert back to ASSIGNOR when more than 20 percent of every class of equity of ASSIGNOR is transferred to a third party.

In Witness Whereof, ASSIGNOR and INTEL, by their duly authorized representatives, have executed this Assignment.

DATE	5/1/03	DATE	E:
Ву:	Nancy Palmintere	Ву:	Micha Lazarus
Title:	Vice President	Title:	
,	Intel Corporation		D.S.P.C. Technologies Ltd
	Signature		Signature

Serial	Eiling Data	Attr. Dooleat	Title
{	Filing Date	Atty. Docket	11de
Number		Number	
10/109,009	29-Mar-02	P-4657-US	APPARATUS AND METHOD OF
			GENERATING OSCILLATIONS
10/026,662	27-Dec-01	P-4659-US	TRANSMITTER HAVING A SIGMA-DELTA
1			MODULATOR WITH A NON-UNIFORM
Ì			POLAR QUANTIZER AND METHODS
			THEREOF
10/274,916	22-Oct-02	P-4660-US	A METHOD TO REDUCE THE NUMBER OF
10/2/4,510	22-000-02	1-1000-05	BITS PER SOFT BIT
10/076,957	19-Feb-02	P-4661-US	RAKE RECEIVER INTERFACE
10/133,629	29-Apr-02	P-4812-US	APPARATUS AND METHOD OF
10/133,029	29-ALD1-02	F-4012-U3	TRANSMISSION LINK QUALITY
10/09/ 900	04-Nov-02	D 4012 TTC	INDICATOR
10/286,800		P-4813-US	CIPHER IMPLEMENTATION
10/331,635	31-Dec-02	P-4814-US	METHOD AND APPARATUS TO ENCODE
			LINEAR BLOCK CODES
10/261,677	02-Oct-02	P-4955-US	METHOD AND APPARATUS OF
			DE-MULTIPLEXING DATA
10/218,117	14-Aug-02	P-4956-US	METHOD AND APPARATUS OF
			GENERATING A QUALITY INDICATOR
10/254,974	26-Sep-02	P-5065-US	METHOD AND APPARATUS OF
			CROSS-CORRELATION
10/330,537	30-Dec-02	P-5323-US	PRONUNCIATION NETWORK
10/314,173	09-Dec-02	P-5324-US	METHOD AND APPARATUS TO CONTROL
			POWER OF TRANSMITTER
10/327,957	26-Dec-02	P-5325-US	METHOD AND APPARATUS OF MEMORY
			MANAGEMENT
10/310,985	06-Dec-02	P-5327-US	SEQUENTIAL DECODING OF PARITY
			CHECK CODES
09/621,388	21-Jul-00	P-5344-US	IQ MISMATCH CANCELLATION
09/387,310	31-Aug-99	P-5349-US	POWER SAVING IN COMMUNICATION
03/38/,510	31-210g-22	1-00-70-06	TERMINALS
09/459,598	14-Dec-99	P-5351-US	BAHL DECODING
	23-Oct-00	P-5365-US	
09/694,806			pi/4 QPSK MODULATOR
10/331,607	31-Dec-02	P-5443-US	METHOD OF CORRECTING PHYSICAL
			IMPAIRMENTS OF A PROGRAMMABLE
10/04	01 7	D 5444 V	FILTER
10/331,603	31-Dec-02	P-5444-US	TUNING SENSITIVITY LINEARIZATION OF
			METAL-OXIDE SEMICONDUCTOR (MOS)
			VARACTORS
10/327,058	24-Dec-02	P-5445-US	METHOD AND APPARATUS FOR
			ADAPTING REFERENCE TEMPLATES
10/327,950	26-Dec-02	P-5480-US	METHOD AND APPARATUS TO REPLY TO
			CALL
09/458,715	13-Dec-99	P-1319-US1	METHOD FOR REDUCING POWER
			CONSUMPTION IN WAIT-MODE
09/583,900	01-Jun-00	P-1155-U81	METHOD AND DEVICE FOR DETECTING
			RATE
09/583,897	01-Jun-00	P-1185-US1	METHOD AND APPARATUS FOR
		}	ACQUIRING AND TRACKING THE
		<u> </u>	SAMPLING PHASE OF A SIGNAL

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Appendix A

Serial	Filing Date	Atty. Docket	Title
Number		Number	
09/371,276	10-Aug-99	P-2421-US	BATTERY OPERATED RADIO RECEIVERS
			HAVING POWER SAVE BY REDUCING
			ACTIVE RECEPTION TIME
09/583,898	01-Jun-00	P-1107-US1	A CODE SYNCHRONIZATION UNIT AND
			METHOD
10/100,190	19-Mar-02	P-1235-US1	METHOD AND DEVICE FOR MANAGING
24, 244, 22			POWER CONSUMPTION OF A RECEIVER IN
			STAND-BY MODE
09/583,899	01-Jun-00	P-1236-US1	VOICE CHANNEL FREQUENCY
07/202,022	01 3411 00	1 1230 031	SYNCHRONIZATION
09/731,821	08-Dec-00	P-1308-US1	INITIAL FREQUENCY SYNCHRONIZATION
09//31,021	08-100-00	1-1200-001	MECHANISM
09/493,004	28-Jan-00	P-1592-US1	METHOD AND DEVICE FOR QUANTIZING
03/433,004	20-34H-00	1-1392-031	THE INPUT TO SOFT DECODERS
00/066 752	01-Oct-01	P-1987-US1	SYSTEM AND METHOD FOR JOINT TIME
09/966,753	01-001-01	1-1997-091	i e
00/066 7706	01 0 1 01	D 1007 TY00	TRACKING OF MULTIPLE PATHS
09/966,736	01-Oct-01	P-1987-US2	SYSTEM AND METHOD FOR JOINT TIME
			TRACKING OF MULTIPLE PATHS
09/417,077	13-Oct-99	P-2513-US	CONTROL OF TRANSMISSION POWER IN A
			COMMUNICATION SYSTEM
09/438,475	12-Nov-99	P-2654-US	A CHANNEL ESTIMATOR
09/599,432	22-Jun-00	P-3087-US	DECODER AND METHOD THEREOF
09/633,131	04-Aug-00	P-3148-US	C CALCULATOR
09/661,127	13-Sep-00	P-3308-US	DC OFFSET CANCELLATION
09/875,033	07-Jun-01	P-3443-US	A METHOD OF CELLULAR
05/0/5,055			COMMUNICATION
10/012,360	12-Dec-01	P-3679-US	PARTITIONING DIGITAL CIRCUITRY
	31-May-01	P-3680-US	METHOD FOR ESTIMATING CRYSTAL
09/867,571	31-1vlay-01	F-3000-08	COEFFICIENT VALUES FOR A SIGNAL
			GENERATOR
10/105 000	26 14. 02	P-3681-US	APPARATUS AND METHOD OF PROVIDING
10/105,922	26-Mar-02	P-3081-US	OUTPUT VOLTAGE
00/00= 506	20 37 01	D C COO TIG	
09/995,796	29-Nov-01	P-3682-US	SYSTEM AND METHOD FOR ERROR
	ļ	ļ	DETECTION IN ENCODED DIGITAL DATA
10/002,405	05-Dec-01	P-3734-US	METHOD AND APPARATUS FOR
			DECODING DATA
10/025,969	26-Dec-01	P-3856-US	PROGRAMMABLE BASEBAND MODULE
10/026,677	27-Dec-01	P-4028-US	METHOD AND APPARATUS FOR
			GENERATING AN OUTPUT SIGNAL
09/983,776	25-Oct-01	P-4042-US	PILOT SIGNAL SYNCHRONIZATION
			VERIFIER
09/910,769	24-Jul-01	P-4043-US	METHOD AND APPARATUS OF ZERO
05/5105/05	2. 300.01		DEFLECTION
10/026,678	27-Dec-01	P-4045-US	METHOD AND SYSTEM FOR RECORDING
10/020,076	27-1000-01	1 10 13 00	CONVERSATION
10/025,959	26-Dec-01	P-4656-US	TRANSMITTED POWER LEVEL INDICATOR
1 10/023,727	20-100-01	1	

Assignment of Legal Title to Patents

Whereas, D.S.P.C. Technologies Ltd., (hereinafter ASSIGNOR) is the sole and exclusive owner of certain patent applications listed in Exhibit A annexed hereto (collectively referred to as the "Patents"); and

Whereas Intel Corporation, a Delaware corporation, with an office at 2200 Mission College Blvd., California 95052, (hereinafter INTEL) is desirous of acquiring bare legal title to and under the Patents for the sole purpose of registering the Patents in the name of INTEL in the U.S. Patent Office; and

Whereas ASSIGNOR is a subsidiary of INTEL.

Now, Therefore,

e 💯

For good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR does hereby transfer to INTEL, bare legal title to the Patents, and bare legal title to any inventions claimed in said Patents, any reissue or reissues of said Patents already granted and which may be granted, and any certificates of reexamination already granted and which may be granted, the bare legal title to same to be held by INTEL, to the end of the term or terms for which said Patents are or may be granted, reissued or extended as fully and entirely as such bare legal title would have been held and enjoyed by ASSIGNOR if this assignment had not been made. Nothing in this Assignment of Legal Title to Patents shall be construed as transferring to Intel beneficial ownership of the Patents, which beneficial ownership, including the right to use, license, divide, exploit and dispose of the rights to and under such Patents (other than bare legal title), shall continue to be held by ASSIGNOR.

ASSIGNOR, hereby authorizes and requests the Commissioner of Patents to issue any and all Letters Patents of the United States on said inventions to INTEL as assignee of bare legal title to the Patents, and hereby covenants that ASSIGNOR has full right to convey the legal title herein assigned, and that, except as otherwise provided between the

1

parties, ASSIGNOR has not executed, and will not execute, any agreements in conflict therewith.

ASSIGNOR and INTEL hereby agree that bare legal title to the Patents transferred under this agreement shall revert back to ASSIGNOR when more than 20 percent of every class of equity of ASSIGNOR is transferred to a third party.

In Witness Whereof, ASSIGNOR and INTEL, by their duly authorized representatives, have executed this Assignment.

DATE	:	DATE	: May 1,2003
By:	Nancy Palmintere	Ву:	Micha Lazarus
Title:	Vice President	Title:	GIM
	Intel Corporation		D.S.P.C. Technologies Ltd.
			M. Lozcoole
	Signature		Signature

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Appendix A

Number Number Number O9/371,276 10-Aug-99 P-2421-US BATTERY OPERATED RADIO RECEIVERS HAVING POWER SAVE BY REDUCING ACTIVE RECEPTION TIME ACODE SYNCHRONIZATION UNIT AND METHOD	Serial	Filing Date	Atty. Docket	Title
09/371,276 10-Aug-99			· · · · · · · · · · · · · · · · · · ·	
HAVING POWER SAVE BY REDUCING ACTIVE RECEPTION TIME		10-Aug-99	P-2421-US	BATTERY OPERATED RADIO RECEIVERS
09/583,898 01-Jun-00 P-1107-US1 A CODE SYNCHRONIZATION UNIT AND METHOD				,
09/583,898 01-Jun-00 P-1107-US1 A CODE SYNCHRONIZATION UNIT AND METHOD				
METHOD	09/583-898	01-Jun-00	P-1107-US1	
10/100,190		,		(
POWER CONSUMPTION OF A RECEIVER IN STAND-BY MODE	10/100-190	19-Mar-02	P-1235-US1	
STAND-BY MODE				
O9/583,899 O1-Jun-00 P-1236-US1 VOICE CHANNEL FREQUENCY SYNCHRONIZATION O9/731,821 O8-Dec-00 P-1308-US1 INITIAL FREQUENCY SYNCHRONIZATION MECHANISM O9/493,004 28-Jan-00 P-1592-US1 METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS O9/966,753 O1-Oct-01 P-1987-US1 SYSTEM AND METHOD FOR JOINT TIME TRACKING OF MULTIPLE PATHS TRACKING OF MULTIPLE PATHS O9/417,077 13-Oct-99 P-2513-US CONTROL OF TRANSMISSION POWER IN A COMMUNICATION SYSTEM O9/438,475 12-Nov-99 P-2654-US A CHANNEL ESTIMATOR O9/599,432 22-Jun-00 P-3087-US DECODER AND METHOD THEREOF O9/633,131 O4-Aug-00 P-3148-US C CALCULATOR O9/875,033 O7-Jun-01 P-3443-US DC OFFSET CANCELLATION O9/875,033 O7-Jun-01 P-3443-US DC OFFSET CANCELLATION O9/867,571 31-May-01 P-3680-US METHOD FOR ESTIMATING CRYSTAL COEFFICIENT VALUES FOR A SIGNAL GENERATOR O9/995,796 29-Nov-01 P-3682-US SYSTEM AND METHOD FOR PROVIDING OUTPUT VOLTAGE O9/995,796 26-Dec-01 P-3682-US SYSTEM AND METHOD FOR ERROR DETECTION IN ENCODED DIGITAL DATA METHOD AND APPARATUS FOR DECODING DATA O9/983,776 25-Oct-01 P-4042-US METHOD AND APPARATUS FOR GENERATING AN OUTPUT SIGNAL PILOT SIGNAL SYNCHRONIZATION PROVIDING OUTPUT VOLTAGE O9/983,776 25-Oct-01 P-4042-US METHOD AND APPARATUS OF ZERO DEFLECTION DEFINAL SYNCHRONIZATION VERIFER COMPUNIZATION VERIFER O9/910,769 24-Jul-01 P-4043-US METHOD AND APPARATUS OF ZERO DEFLECTION DEFLOCATION		•	,	· •
SYNCHRONIZATION O9/731,821 O8-Dec-00 P-1308-US1 NITIAL FREQUENCY SYNCHRONIZATION MECHANISM MECHANISM MECHANISM O9/493,004 28-Jan-00 P-1592-US1 METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS O9/966,753 O1-Oct-01 P-1987-US1 SYSTEM AND METHOD FOR JOINT TIME TRACKING OF MULTIPLE PATHS O9/966,736 O1-Oct-01 P-1987-US2 SYSTEM AND METHOD FOR JOINT TIME TRACKING OF MULTIPLE PATHS O9/417,077 13-Oct-99 P-2513-US CONTROL OF TRANSMISSION POWER IN A COMMUNICATION SYSTEM COMMUNICATION SYSTEM A CHANNEL ESTIMATOR O9/438,475 12-Nov-99 P-2654-US A CHANNEL ESTIMATOR O9/9633,131 O4-Aug-00 P-3148-US C CALCULATOR O9/661,127 13-Sep-00 P-3308-US DC OFFSET CANCELLATION O9/875,033 O7-Jun-01 P-3443-US A METHOD OF CELLULAR COMMUNICATION O9/875,033 O7-Jun-01 P-3443-US A METHOD OF CELLULAR COMMUNICATION O9/867,571 31-May-01 P-3680-US METHOD FOR ESTIMATING CRYSTAL COEFFICIENT VALUES FOR A SIGNAL GENERATOR O9/995,796 29-Nov-01 P-3681-US APPARATUS AND METHOD OF PROVIDING OUTPUT VOLTAGE O9/995,796 26-Dec-01 P-3681-US APPARATUS AND METHOD FOR ERROR DETECTION IN ENCODED DIGITAL DATA METHOD AND APPARATUS FOR DETECTION IN ENCODED DIGITAL DATA METHOD AND APPARATUS FOR DETECTION IN ENCODED DIGITAL DATA METHOD AND APPARATUS FOR GENERATING AN OUTPUT SIGNAL PLOT SIGNAL SYNCHRONIZATION P-4042-US METHOD AND APPARATUS FOR GENERATING AN OUTPUT SIGNAL PILOT SIGNAL SYNCHRONIZATION VERIFIER O9/910,769 24-Jul-01 P-4043-US METHOD AND APPARATUS OF ZERO DEFLECTION	09/583,899	01-Jun-00	P-1236-US1	VOICE CHANNEL FREQUENCY
MECHANISM METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS				SYNCHRONIZATION
MECHANISM METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS	09/731,821	08-Dec-00	P-1308-US1	INITIAL FREQUENCY SYNCHRONIZATION
THE INPUT TO SOFT DECODERS				·
09/966,753 01-Oct-01 P-1987-US1 SYSTEM AND METHOD FOR JOINT TIME TRACKING OF MULTIPLE PATHS	09/493,004	28-Jan-00	P-1592-US1	METHOD AND DEVICE FOR QUANTIZING
TRACKING OF MULTIPLE PATHS				THE INPUT TO SOFT DECODERS
TRACKING OF MULTIPLE PATHS	09/966,753	01-Oct-01	P-1987-US1	
TRACKING OF MULTIPLE PATHS				TRACKING OF MULTIPLE PATHS
09/417,077	09/966,736	01-Oct-01	P-1987-US2	SYSTEM AND METHOD FOR JOINT TIME
COMMUNICATION SYSTEM				
09/438,475 12-Nov-99 P-2654-US A CHANNEL ESTIMATOR 09/599,432 22-Jun-00 P-3087-US DECODER AND METHOD THEREOF 09/633,131 04-Aug-00 P-3148-US C CALCULATOR 09/661,127 13-Sep-00 P-3308-US DC OFFSET CANCELLATION 09/875,033 07-Jun-01 P-3443-US A METHOD OF CELLULAR COMMUNICATION 10/012,360 12-Dec-01 P-3679-US PARTITIONING DIGITAL CIRCUITRY 09/867,571 31-May-01 P-3680-US METHOD FOR ESTIMATING CRYSTAL COEFFICIENT VALUES FOR A SIGNAL GENERATOR 10/105,922 26-Mar-02 P-3681-US APPARATUS AND METHOD OF PROVIDING OUTPUT VOLTAGE 09/995,796 29-Nov-01 P-3682-US SYSTEM AND METHOD FOR ERROR DETECTION IN ENCODED DIGITAL DATA 10/002,405 05-Dec-01 P-3734-US METHOD AND APPARATUS FOR DECODING DATA 10/025,969 26-Dec-01 P-3856-US PROGRAMMABLE BASEBAND MODULE 10/026,677 27-Dec-01 P-4028-US METHOD AND APPARATUS FOR GENERATING AN OUTPUT SIGNAL 09/983,776 25-Oct-01 P-4042-US PILOT SIGNAL SYNCHRONIZATION VERIFIER <t< td=""><td>09/417,077</td><td>13-Oct-99</td><td>P-2513-US</td><td>CONTROL OF TRANSMISSION POWER IN A</td></t<>	09/417,077	13-Oct-99	P-2513-US	CONTROL OF TRANSMISSION POWER IN A
09/599,432 22-Jun-00 P-3087-US DECODER AND METHOD THEREOF 09/633,131 04-Aug-00 P-3148-US C CALCULATOR 09/661,127 13-Sep-00 P-3308-US DC OFFSET CANCELLATION 09/875,033 07-Jun-01 P-3443-US A METHOD OF CELLULAR COMMUNICATION 10/012,360 12-Dec-01 P-3679-US PARTITIONING DIGITAL CIRCUITRY 09/867,571 31-May-01 P-3680-US METHOD FOR ESTIMATING CRYSTAL COEFFICIENT VALUES FOR A SIGNAL GENERATOR 10/105,922 26-Mar-02 P-3681-US APPARATUS AND METHOD OF PROVIDING OUTPUT VOLTAGE 09/995,796 29-Nov-01 P-3682-US SYSTEM AND METHOD FOR ERROR DETECTION IN ENCODED DIGITAL DATA 10/002,405 05-Dec-01 P-3682-US SYSTEM AND APPARATUS FOR DECODING DATA 10/025,969 26-Dec-01 P-3856-US FROGRAMMABLE BASEBAND MODULE 10/026,677 27-Dec-01 P-4028-US METHOD AND APPARATUS FOR GENERATING AN OUTPUT SIGNAL 09/983,776 25-Oct-01 P-4042-US PILOT SIGNAL SYNCHRONIZATION VERIFIER 09/910,769 24-Jul-01 P-4043-US METHOD AND APPARATUS OF ZERO DEFLECTION				COMMUNICATION SYSTEM
09/633,131 04-Aug-00 P-3148-US C CALCULATOR 09/661,127 13-Sep-00 P-3308-US DC OFFSET CANCELLATION 09/875,033 07-Jun-01 P-3443-US A METHOD OF CELLULAR COMMUNICATION 10/012,360 12-Dec-01 P-3679-US PARTITIONING DIGITAL CIRCUITRY 09/867,571 31-May-01 P-3680-US METHOD FOR ESTIMATING CRYSTAL COEFFICIENT VALUES FOR A SIGNAL GENERATOR 10/105,922 26-Mar-02 P-3681-US APPARATUS AND METHOD OF PROVIDING OUTPUT VOLTAGE 09/995,796 29-Nov-01 P-3682-US SYSTEM AND METHOD FOR ERROR DETECTION IN ENCODED DIGITAL DATA 10/002,405 05-Dec-01 P-3734-US METHOD AND APPARATUS FOR DECODING DATA 10/025,969 26-Dec-01 P-3856-US PROGRAMMABLE BASEBAND MODULE 10/026,677 27-Dec-01 P-4028-US METHOD AND APPARATUS FOR GENERATING AN OUTPUT SIGNAL 09/983,776 25-Oct-01 P-4042-US METHOD AND APPARATUS OF ZERO DEFLECTION 10/026,678 27-Dec-01 P-4043-US METHOD AND SYSTEM FOR RECORDING CONVERSATION	09/438,475	12-Nov-99	P-2654-US	A CHANNEL ESTIMATOR
09/633,131 04-Aug-00 P-3148-US C CALCULATOR 09/661,127 13-Sep-00 P-3308-US DC OFFSET CANCELLATION 09/875,033 07-Jun-01 P-3443-US A METHOD OF CELLULAR COMMUNICATION 10/012,360 12-Dec-01 P-3679-US PARTITIONING DIGITAL CIRCUITRY 09/867,571 31-May-01 P-3680-US METHOD FOR ESTIMATING CRYSTAL COEFFICIENT VALUES FOR A SIGNAL GENERATOR 10/105,922 26-Mar-02 P-3681-US APPARATUS AND METHOD OF PROVIDING OUTPUT VOLTAGE 09/995,796 29-Nov-01 P-3682-US SYSTEM AND METHOD FOR ERROR DETECTION IN ENCODED DIGITAL DATA 10/002,405 05-Dec-01 P-3734-US METHOD AND APPARATUS FOR DECODING DATA 10/025,969 26-Dec-01 P-3856-US PROGRAMMABLE BASEBAND MODULE 10/026,677 27-Dec-01 P-4028-US METHOD AND APPARATUS FOR GENERATING AN OUTPUT SIGNAL 09/983,776 25-Oct-01 P-4042-US PILOT SIGNAL SYNCHRONIZATION VERIFIER 09/910,769 24-Jul-01 P-4043-US METHOD AND APPARATUS OF ZERO DEFLECTION 10/026,678 27-Dec-01 P-4045-US METHOD AND SYSTEM FOR RECORDING CONVERSAT	09/599,432	22-Jun-00	P-3087-US	DECODER AND METHOD THEREOF
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COMMUNICATION				
10/012,360 12-Dec-01 P-3679-US PARTITIONING DIGITAL CIRCUITRY	05/6/5,055	07-3411-01	1-34-5-00	
D9/867,571 31-May-01 P-3680-US METHOD FOR ESTIMATING CRYSTAL COEFFICIENT VALUES FOR A SIGNAL GENERATOR	10/012 360	12-Dec-01	P-3679-US	
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DECODING DATA	10/002 405	05-Dec-01	P-3734-IIS	
10/025,969 26-Dec-01 P-3856-US PROGRAMMABLE BASEBAND MODULE	10/002,405	1 05. 500 01	1 3/3/1-03	
10/026,677 27-Dec-01 P-4028-US METHOD AND APPARATUS FOR GENERATING AN OUTPUT SIGNAL	10/025 969	26-Dec-01	P-3856-TIS	
GENERATING AN OUTPUT SIGNAL 09/983,776 25-Oct-01 P-4042-US PILOT SIGNAL SYNCHRONIZATION VERIFIER 09/910,769 24-Jul-01 P-4043-US METHOD AND APPARATUS OF ZERO DEFLECTION 10/026,678 27-Dec-01 P-4045-US METHOD AND SYSTEM FOR RECORDING CONVERSATION				
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09/910,769 24-Jul-01 P-4043-US METHOD AND APPARATUS OF ZERO DEFLECTION 10/026,678 27-Dec-01 P-4045-US METHOD AND SYSTEM FOR RECORDING CONVERSATION	09/983 776	25-Oct-01	P-4042-US	
09/910,769 24-Jul-01 P-4043-US METHOD AND APPARATUS OF ZERO DEFLECTION 10/026,678 27-Dec-01 P-4045-US METHOD AND SYSTEM FOR RECORDING CONVERSATION	05,505,770			· · · · · · · · · · · · · · · · · · ·
DEFLECTION 10/026,678 27-Dec-01 P-4045-US METHOD AND SYSTEM FOR RECORDING CONVERSATION	09/910.769	24-Jul-01	P-4043-US	
10/026,678 27-Dec-01 P-4045-US METHOD AND SYSTEM FOR RECORDING CONVERSATION	45,510,705			
CONVERSATION	10/026-678	27-Dec-01	P-4045-US	
	10,020,070			
	10/025.959	26-Dec-01	P-4656-US	

Serial	Filing Date	Atty. Docket	Title
Number		Number	
10/109,009	29-Mar-02	P-4657-US	APPARATUS AND METHOD OF
			GENERATING OSCILLATIONS
10/026,662	27-Dec-01	P-4659-US	TRANSMITTER HAVING A SIGMA-DELTA
			MODULATOR WITH A NON-UNIFORM
			POLAR QUANTIZER AND METHODS
			THEREOF
10/274,916	22-Oct-02	P-4660-US	A METHOD TO REDUCE THE NUMBER OF
10/056.055	10 70 1 00	D 4661 TTG	BITS PER SOFT BIT
10/076,957	19-Feb-02	P-4661-US	RAKE RECEIVER INTERFACE
10/133,629	29-Apr-02	P-4812-US	APPARATUS AND METHOD OF
			TRANSMISSION LINK QUALITY
10/006 000	04 37 02	D 4012 TTG	INDICATOR
10/286,800	04-Nov-02	P-4813-US	CIPHER IMPLEMENTATION
10/331,635	31-Dec-02	P-4814-US	METHOD AND APPARATUS TO ENCODE
10/061 677	00 0 4 00	D 4055 TIG	LINEAR BLOCK CODES
10/261,677	02-Oct-02	P-4955-US	METHOD AND APPARATUS OF
10/010 1177	14 4 00	D 4056 TIO	DE-MULTIPLEXING DATA
10/218,117	14-Aug-02	P-4956-US	METHOD AND APPARATUS OF GENERATING A QUALITY INDICATOR
10/254,974	26-Sep-02	P-5065-US	METHOD AND APPARATUS OF
10/254,9/4	20-Sep-02	P-3003-08	CROSS-CORRELATION
10/330,537	30-Dec-02	P-5323-US	PRONUNCIATION NETWORK
10/330,337	09-Dec-02	P-5324-US	METHOD AND APPARATUS TO CONTROL
10/314,173	09-1060-02	F-3324-US	POWER OF TRANSMITTER
10/327,957	26-Dec-02	P-5325-US	METHOD AND APPARATUS OF MEMORY
10/32/,937	20-1000-02	1-3323-06	MANAGEMENT
10/310,985	06-Dec-02	P-5327-US	SEQUENTIAL DECODING OF PARITY
10,010,000		1 222, 02	CHECK CODES
09/621,388	21-Jul-00	P-5344-US	IQ MISMATCH CANCELLATION
09/387,310	31-Aug-99	P-5349-US	POWER SAVING IN COMMUNICATION
03.50,,510			TERMINALS
09/459,598	14-Dec-99	P-5351-US	BAHL DECODING
09/694,806	23-Oct-00	P-5365-US	pi/4 QPSK MODULATOR
10/331,607	31-Dec-02	P-5443-US	METHOD OF CORRECTING PHYSICAL
			IMPAIRMENTS OF A PROGRAMMABLE
		<u> </u>	FILTER
10/331,603	31-Dec-02	P-5444-US	TUNING SENSITIVITY LINEARIZATION OF
			METAL-OXIDE SEMICONDUCTOR (MOS)
			VARACTORS
10/327,058	24-Dec-02	P-5445-US	METHOD AND APPARATUS FOR
	<u> </u>	 	ADAPTING REFERENCE TEMPLATES
10/327,950	26-Dec-02	P-5480-US	METHOD AND APPARATUS TO REPLY TO
	125 00	7 1010 7701	CALL
09/458,715	13-Dec-99	P-1319-US1	METHOD FOR REDUCING POWER
00/205 555	1 01 7 00	D 1100 Trai	CONSUMPTION IN WAIT-MODE
09/583,900	01-Jun-00	P-1155-US1	METHOD AND DEVICE FOR DETECTING
00/502 907	01-Jun-00	D 1106 TIG1	RATE METHOD AND APPARATUS FOR
09/583,897	01-Jun-00	P-1185-US1	ACQUIRING AND TRACKING THE
			SAMPLING PHASE OF A SIGNAL
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