

PATENTS ONLY (ref OG 7/28/92)



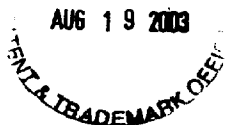
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8-19-03

To the Honorable Commissioner of Patents and Trademarks:
Please record the attached original document or copy thereof.

1. Name of Party(ies) conveying an interest: 2. Name and Address of Party(ies) receiving an interest:

Takeshi SAKATA
Tsugio TAKAHASHI
Katsutaka KIMURA
Tomonori SEKIGUCHI



HITACHI, LTD.
6, Kanda Surugadai 4-chome
Chiyoda-ku, Tokyo, JAPAN

3. Description of the interest conveyed: Assignr

08-22-2003

4. Application number(s) or patent number(s).



Additional sheet attached? Yes XXX No

102531942

If this document is being filed together with a new application,
the execution date of the application is:

DATE

- A. Patent Application No.(s)

- B. Patent No.(s)

09/349,761

5. Name and address of party to whom correspondence concerning document should be mailed:

John R. Mattingly
Mattingly, Stanger & Malur, P.C.
1800 Diagonal Road, Suite 370
Alexandria, Virginia 22314

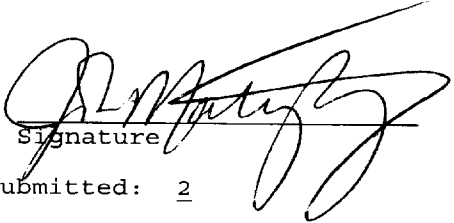
6. Number of applications and patents involved: One
7. Amount of fee enclosed or authorized to be charged: \$40.00
8. The Commissioner is hereby authorized to charge Deposit Account No. 50-1417 if no check is attached.

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9. Execution date of attached document: May 24, 1999

10. To the best of my knowledge and belief, I declare under penalty of perjury under the laws of the United States of America that the foregoing information is true and correct and that any attached copy is a true copy of the original document.

John R. Mattingly
Name of Person Signing


Signature

August 19, 2003
Date

Total number of pages being submitted: 2

ASSIGNMENT

(譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by HITACHI, LTD., a corporation organized under the laws of Japan, located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan, receipt of which is hereby acknowledged I do hereby sell and assign to said HITACHI, LTD., its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

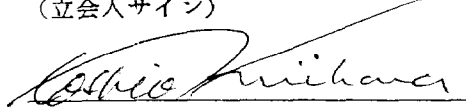

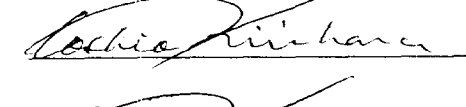
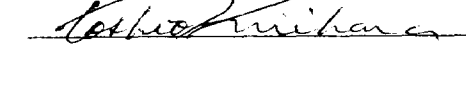
SEMICONDUCTOR INTEGRATED CIRCUIT WITH MEMORY REDUNDANCY CIRCUIT

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD., its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD.

Signed on the date(s) indicated aside signatures :

INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)	Witnesses (立会人サイン)
1) Takeshi Sakata	May 24, 1999	
2) Tuzio Takahashi	May 24, 1999	
3) Katsuhiko Kim	May 24, 1999	
4) Tomonori Sekiguchi	May 24, 1999	
5) _____	_____	_____
6) _____	_____	_____
7) _____	_____	_____
8) _____	_____	_____
9) _____	_____	_____
10) _____	_____	_____