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To the Honorable Commissioner of Patents and Trademarks: F	
Name(s) of conveying party(ies):	Name and address of receiving party:
1) WILLIAM THIES	Name: STMICROELECTRONICS LIMITED
2) NICOLAS FROIDEVAUX 3)	
4)	Internal Address:
5) 6)	
Additional names of conveying parties attached?Yes _XNo	· · · · · · · · · · · · · · · · · · ·
Nature of conveyance:     X Assignment Merger	Street Address: 1000 AZTEC WEST
Security Agreement Change of Name	ALMONDSBURY
Other	Postal Code & City: BRISTOL, BS32 4SQ
Execution Dates:	Country <u>UNITED KINGDOM</u>
1) MAY 8, 2003 4) 5) 3) 6) 6)	Additional names & addresses attached?Yes \big  XNo
4. Application number(s) or registration number(s):	<u> </u>
If this document is being filed the filing date of	together with a new application, the application is
A. Patent Application No(s).	B. Patent No(s).
10/229,337	:
Additional numbers attache	l ed? <u> </u>
<ol><li>Name and address of party to whom correspondence concerning document should be mailed;</li></ol>	6. Total number of applications and patents involved 1
Name: Seed Intellectual Property Law Group PLLC	
Internal Address: ROBERT IANNUCCI	7. Total Fee (37 CFR 3.41):
	Enclosed
Street Address: 701 Fifth Avenue, Suite 6300	X Authorized to be charged to deposit account
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City: Seattle State: WA ZIP: 98104-7092	8. Deposit account number:
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Total number of pages including cover sheet,	
MB No. 0651-0011 (exp 4/94) - Docket No. 851963.408	C:\NrPortbl\iManage\LAURAT\464340 1.DOC

## ASSIGNMENT

WHEREAS, we, William THIES and Nicolas FROIDEVAUX (hereinafter referred to as ASSIGNORS), having mailing addresses of 2 Bramley Close, Olveston, Bristol, BS35 4EA, United Kingdom and 8 Rue Peyresc, 13100 Aix En Provence, France, respectively, are the joint inventors of an invention entitled "SEMICONDUCTOR INPUT/OUTPUT CIRCUIT ARRANGEMENT," as described and claimed in the specification for which an application for United States letters patent was filed on August 26, 2002 and assigned Application No. 10/229,337. This application claims the priority to European Application No. 01307231.9, filed on August 24, 2001.

WHEREAS, STMicroelectronics Limited (hereinafter referred to as ASSIGNEE), a corporation of Great Britain having a place of business at 1000 Aztec West, Almondsbury, Bristol, BS32 4SQ, United Kingdom, is desirous of acquiring the entire right, title and interest in and to the invention and in and to any letters patent that may be granted therefor in the United States and in any and all foreign countries;

NOW, THEREFORE, in exchange for good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNORS hereby sell, assign and transfer unto shid ASSIGNEE the entire right, title and interest in and to said invention, said application and any and all letters patent which may be granted for said invention in the United States of America and its territorial possessions and in any and all foreign countries, and in any and all divisions, reissues and continuations thereof, including the right to file foreign applications directly in the name of ASSIGNEE and to claim priority rights deriving from said United States application to which said foreign applications are entitled by virtue of international convention, treaty or otherwise, said invention, application and all letters patent on said invention to be held and enjoyed by ASSIGNEE and its successors and assigns as fully and entirely as the same would have been held and enjoyed by ASSIGNORS had this assignment, transfer and sale not been made. ASSIGNORS hereby authorize and request the Commissioner of Patents and Trademarks to issue all letters patent on said invention to ASSIGNEE. ASSIGNORS agree to execute all instruments and documents required for the making and prosecution of applications for United States and foreign letters patent on said invention, for litigation regarding letters patent, or for the purpose of protecting title to said invention or letters paterit therefor.

Date

NICOLAS FROIDEVAUX

## ASSIGNMENT

WHEREAS, we, William THIES and Nicolas FROIDEVAUX (hereinafter referred to as ASSIGNORS), having mailing addresses of 2 Bramley Close, Olveston, Bristol, BS35 4EA, United Kingdom, and 8 Rue Peyresc, 13100 Aix En Provence, France, respectively, are the joint inventors of an invention entitled "SEMICONDUCTOR INPUT/OU PUT CIRCUIT ARRANGEMENT," as described and claimed in the specification for which an application for United States letters patent was filed on August 26, 2002 and assigned Application No. 10/229,337. This application claims the priority to European Application No. 01307231.9, filed on August 24, 2001.

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Date	<del></del>	WILLIAM THIES
Date August the 28th lo		Nicolas FROIDEVAUX

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PATENT REEL: 014421 FRAME: 0688