

Form: PTO-1595

**RECORDATION FORM COVER SHEET
PATENTS ONLY**

DOCKET NUMBER: 2020

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
General Semiconductor Ireland

2. Name and address of receiving party(ies)

Name: General Semiconductor, Inc.

Internal Address:

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ NoStreet Address: 10 Melville Park RoadCity: MelvilleState: NYZip: 11747-3113Additional name(s) & address(es) attached? ☐ Yes ☒ No

3. Name of conveyance:

☒ Assignment☐ Merger☐ Security Agreement☐ Change of Name☐ OtherExecution Date: 09/30/2003

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is _____

A. Patent Application No.(s)

10/617,343

B. Patent No.(s)

5,083,193

5,814,874

6,197,618

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Karin L. Williams

Internal Address: Mayer Fortkort & Williams, PC

Street Address: 251 North Avenue West, 2nd FloorCity: WestfieldState: New JerseyZip: 070906. Total number of applications and patents involved: ☐ 47. Total fee (37 CFR 3.41) \$160.00☐ Enclosed☒ Authorized to be charged to deposit account8. Deposit account number: 50-1047

(Attach duplicate copy of this page if paying by deposit account.)

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document

Marjorie Scariati

Name of Person Signing

Signature

Date

Total number of pages including cover sheet, attachments, and documents: ☐ 5Mail documents to be recorded with required cover sheet information to:
Commissioner for Patents, Mail Stop Assignment, P.O. Box 1450, Alexandria, VA 22313-1450

CH \$160.00 501047 10617343

ASSIGNMENT OF PATENTS

THIS ASSIGNMENT is made the 30th day of September 2003 **BY AND BETWEEN GENERAL SEMICONDUCTOR IRELAND** an Unlimited Liability Company organized and existing under the laws of the Republic of Ireland and having its registered office at C/O G.J. Moloney, Solicitors, Courthouse Chambers, 27/29 Washington Street, Cork, Republic of Ireland (hereinafter the "Assignor") of the one part and **GENERAL SEMICONDUCTOR, INC** a Company organized and existing under the laws of Delaware and having its place of business at 10 Melville Park Road, Melville, NY 11747 (hereinafter the "Assignee") of the other part.

WHEREAS:

- A. The Assignor is the legal and beneficial owner and registered proprietor of certain letters, patents and patent applications as set forth in Schedule One attached hereto and made a part hereof (hereinafter the "Patents").
- B. The Assignee is an affiliated Company of the Assignor.
- C. The Assignor has ceased to manufacture from its facility in Macroom, County Cork, Republic of Ireland and is in the process of winding down its operations generally and it has therefore been agreed between the parties that the Assignor will assign its interest in the Patents to the Assignee in accordance with the terms and conditions hereof.

NOW THIS DEED WITNESSES AS FOLLOWS:


- 1. The Assignor hereby assigns, transfers and sets over to the Assignee the Patents together with all the rights, title and interest of the Assignor in or in respect of the Patents, the inventions to which they relate and in any improvements thereto **TO HOLD, USE, EXERCISE AND ENJOY THE PATENTS** unto and by the Assignee, its Successors and Assigns absolutely.
- 2. In consideration of the assignment of the Patents the Assignee agrees to pay to the Assignor the amount of US\$1,000 (the receipt of which is hereby acknowledged).

3. This Assignment and the respective rights and obligations of the parties hereunder shall be governed by and construed in accordance with the laws of Ireland.
4. This Assignment may be executed in any number of counterparts with the same effect as if executed upon the same instrument.

IN WITNESS WHEREOF this Assignment has been duly executed the day and year first above written.

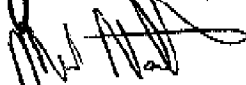
**Duly executed for and on behalf
of General Semiconductor Ireland**

By 
Rainer Kropf

In the presence of 
Gudrun Böhr
(Please type or print name)

**Duly executed for and on behalf
of General Semiconductor, Inc.**

By 
Richard N. Grubb

In the presence of 
Mark Valentino
(Please type or print name)

SCHEDULE ONE

1. U.S. Patent No. 5,083,193

Issued: January 21, 1992

Title: SEMICONDUCTOR PACKAGE, METHOD OF MANUFACTURING THE SAME, APPARATUS FOR CARRYING OUT THE METHOD, AND ASSEMBLY FACILITY

Inventor: Viktor Heitzler et al.

2. U.S. Patent No. 5,814,874

Issued: September 29, 1998

Title: SEMICONDUCTOR DEVICE HAVING A SHORTER SWITCHING TIME WITH LOW FORWARD VOLTAGE

Inventor: Guenter Igel

3. U.S. Patent No. 6,197,618

Issued: March 6, 2001

Title: SEMICONDUCTOR DEVICE FABRICATION USING ADHESIVES

Inventor: Marie Guillot et al.

4. U.S. Patent Application SN. 10/617,343

Filed: July 10, 2003

Title: SURFACE MOUNT MULTICHIP DISCRETE STRUCTURE

Inventor: Paddy O'Shea et al.