

RECORDATION FORM COVER SHEET
PATENTS ONLY

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

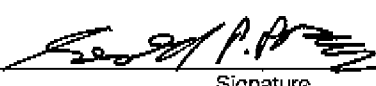
TO THE HONORABLE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE. PLEASE RECORD THE ATTACHED ORIGINAL DOCUMENTS OR COPY THEREOF.

1. Name of conveying party(ies): (a) Celestry Design Technologies, Inc. Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	2. Name and address of receiving party(ies): Name: Cadence Design Systems, Inc. Street Address: 2655 Seely Avenue City: San Jose State: CA Zip: 95134 Country: U.S.A.
3. Nature of Conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other Execution Date: (a) 02/27/04	Name and address of receiving party(ies): Name: Street Address: City: State: Zip: Country: Additional name(s) & address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No

4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is:	
A. Patent Application No.(s) - Title: See Attached Exhibit A	B. Patent No.(s)

Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
5. Name and address of party to whom correspondence concerning document should be mailed: Name: Gerald P. Parsons Internal Address: Parsons Hsue & de Runtz LLP Street Address: 655 Montgomery Street, Suite 1800 City San Francisco State CA Zip 94111	6. Total number of applications and patents involved: 10 7. Total fee (37 CFR 3.41): \$40.00 <input type="checkbox"/> A check is enclosed that includes the total fee. <input checked="" type="checkbox"/> Charge Deposit Account 502664 for any additional fees required for this conveyance and credit deposit account 502664 any amounts overpaid. This form is being submitted in duplicate.

DO NOT USE THIS SPACE

8. Statement and signature. <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i>		
Gerald P. Parsons, Reg. No.: 24,486 Name of Person Signing	 Signature	March 22, 2004 Date
Total number of pages including cover sheet, attachments, and documents: 3		

CH \$400.00 502664 60236865

EXHIBIT A

<u>Serial No.</u>	<u>Filed Date</u>	<u>Inventor(s)</u>	<u>Title</u>
~ 60/236,865	9/29/2000	Lifeng Wu et al.	Hot-Carrier Circuit Reliability Simulation
~ 60/236,587	9/29/2000	Zhihong Liu, Jeong Y. Choi, Lifeng Wu, Alvin I. Chen, Gang Zhang	Hot-Carrier Device Degradation Modeling and Extraction Methodologies
60/236,586	9/29/2000	Jeong Y., Choi, Lifeng Wu, Jingkun Fang, Alvin I. Chen	Hot-Carrier Reliability Design Rule Checker
~ 09/832,933	4/11/2001	Lifeng Wu, Zhihong Liu, Alvin I. Chen, Jeong Y., Choi, Bruce W. McGaughy	Hot-Carrier Circuit Reliability Simulation
~ 09/969,185	9/27/2001	Zhihong Liu, Lifeng Wu, Jeong Y. Choi, Ping Chen, Alvin I. Chen, Gang Zhang	Hot-Carrier Device Degradation Modeling And Extraction Methodologies
~ 09/969,186	9/27/2001	Lifeng Wu, Jeong Y. Choi, Alvin I. Chen, Jingkun Fang	Hot-Carrier Reliability Design Rule Checker
~ 60/387,027	6/6/2002	Won-Young Jung	Optimization Methods For On-Chip Interconnect Geometries Suitable For Ultra Deep Sub-Micron Processes
~ 60/387,294	6/7/2002	Jianlin Wei, Lifeng Wu, I-Hsien Chen	Shape Based Noise Tolerance Characterization And Analysis Of LSI
~ 60/387,272	6/7/2002	Lifeng Wu, Jianlin Wei, I-Hsien Chen	Shape Based Noise Characterization And Analysis Of LSI
10/616,765	7/9/2003	Xisheng Zhang, Hancheng Liang, Zhihong Liu, Jianhe Guo	MOSFET Modeling For IC Design Accurate For High Frequencies

PATENT**REEL: 014450 FRAME: 0138**

ASSIGNMENT

Celestry Design Technologies, Inc., a California corporation (hereinafter "Celestry"), is the record owner of the right, title and interest in and to the United States patent applications listed on Exhibit A hereto (hereinafter "the Patent Applications").

Cadence Design Systems, Inc, a Delaware corporation, (hereinafter "Cadence") desires to acquire such right, title and interest in the Patent Applications.

Therefore, effective January 15, 2003, for good and valuable consideration, the receipt of which is acknowledged, Celestry hereby assigns and transfers to Cadence its full right, title and interest in the Patent Applications, any patents resulting therefrom and to the technology described therein, including divisions, continuations, reissues, re-examined versions, extensions thereof, all rights of priority thereto, and the right to collect damages from any third parties incurred both before and after the effective date.

Executed this 27 day of February, 2004.



R. L. Smith McKeithen
Secretary


State of California

) ss

County of Santa Clara

On February 27, 2004 before me, Judith Casamajor personally appeared R. L. Smith McKeithen personally known to me or proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is(are) subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

WITNESS my hand and official seal.


SIGNATURE OF NOTARY

PARSONS HSUE & DE RUNTZ LLP

655 Montgomery Street, Suite 1800 San Francisco, Ca 94111
tel 415.318.1160 fax 415.693.0194

Date March 22, 2004
To Assignment Services Division
United States Patent and Trademark Office
Fax number (703) 306-5995
Tel number (800) 972-6382

From Gerald P. Parsons, Reg. No. 24,486
Total pages 5
Subject Assignment Recordation of 10 Patent Applications
Your Ref.
Our Ref.
