

10-07-2003



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B/O FORM PTO-105 (Rev. 4/00)

U.S. Department of Commerce
Patent and Trademark Office

To the Commissioner for Patents. Please record the attached original documents or copies thereof.

1. Name of Conveying Party(ies):

Acer Laboratories Incorporation

10-1-03

☐ Additional names of conveying parties attached.

2. Name and Address of Receiving Party(ies):

Name: ALI Corporation

Internal Address:

Street Address: 8F, 88, Sec. 1, Hsin Tai Wu Rd.
HsichihCity, State, Zip: Taipei Hsien 221
Taiwan, R.O.C.☐ Additional name(s) and address(es) attached.

3. Nature of Conveyance:

- ☐ Assignment ☐ Merger
☐ Security Agreement ☒ Change of Name
☐ Other:

Execution Date: May 7, 2002

4. (A) Patent Application Number(s):

SEE THE 3 ATTACHED PAGES

4. (B) Patent Number(s):

SEE THE 3 ATTACHED PAGES

If this document is being filed together with a new
application, the execution date of the application is:☐ Additional Numbers Attached.5. Name and Address of Party to whom Correspondence Concerning this
Document Should be Mailed:

Name: Richard E. Fichter

Address: Bacon & Thomas, PLLC
625 Slaters Lane Fourth Floor
Alexandria, VA 22314Total Number of Applications and
Patents Involved:

59

7. Total Fee:
(37 CFR 3.41)

\$2,360.00

☒ Enclosed☐ Authorized to be charged to deposit account

8. Deposit Account Number:

02-0200

ATTACH DUPLICATE COPY OF THIS PAGE IF PAYING BY DEPOSIT ACCOUNT

DO NOT USE THIS SPACE

9. Statement and Signature:

**To the best of my knowledge and belief, the foregoing is true and correct and any attached copy
is a true copy of the original document.**

Richard E. Fichter

October 1, 2003

Name of Person Signing

Signature

Date

Total number of pages including cover sheet, attachments and document: 6

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PATENT
REEL: 014523 FRAME: 0512

揚智科技英文公司名變更明細表

Title	CT	Our Ref. No	Appl. No	Appl. Date	Patent No	Iss. Date	Status
PIPELINED DUAL PORT RAM	US	PALI-CI-0001-USXX	07/668,543	12-Sep-88	5,206,833	27-Apr-93	ISSUED
FREQUENCY DETECTOR	US	PALI-CI-0002-USXX	07/316,360	27-Feb-89	4,945,303	31-Jul-90	ISSUED
INTERFACE CONTROL CIRCUIT WITH ACTIVE CIRCUIT CHARGE OR DISCHARGE	US	PALI-CI-0003-USXX	07/314,333	22-Feb-89	4,945,264	31-Jul-90	ISSUED
MOS ANALOG NOR AMPLIFIER AND CURRENT SOURCE THEREFOR	US	PALI-CI-0004-USXX	07/318,383	03-Mar-89	5,004,938	02-Apr-91	ISSUED
FULLY DIFFERENTIAL SAMPLE AND HOLD ADDER CIRCUIT	US	PALI-CI-0006-USXX	07/600,526	19-Oct-90	5,144,160	04-Sep-92	ISSUED
CIRCUIT FOR INITIALIZING REGISTERS USING TWO INPUT SIGNALS FOR WRITING DEFAULT VALUE INTO D-LATCH AFTER A RESET OPERATION	US	PALI-CI-0007-USXX	07/632,172	20-Dec-90	5,303,350	12-Oct-97	ISSUED
CLOCK SWITCHING CIRCUIT AND METHOD FOR PREVENTING GLITCH DURING SWITCHING	US	PALI-CI-0009-USXX	07/684,587	12-Apr-91	5,155,380	13-Oct-92	ISSUED
CACHE MEMORY SYSTEM, AND COMPARATOR AND MOS ANALOG XOR AMPLIFIER FOR USE IN THE SYSTEM	US	PALI-CI-0011-USX1	08/023,959	26-Feb-93	5,329,632	12-Jul-94	ISSUED
CACHE MEMORY SYSTEM, AND COMPARATOR AND MOS ANALOG XOR AMPLIFIER FOR USE IN THE SYSTEM	US	PALI-CI-0011-USX2	08/023,964	26-Feb-93	5,319,348	07-Jun-94	ISSUED
CACHE MEMORY SYSTEM, AND COMPARATOR AND MOS ANALOG XOR AMPLIFIER FOR USE IN THE SYSTEM	US	PALI-CI-0011-USX3	08/023,965	26-Feb-93	5,329,633	12-Jul-94	ISSUED
CACHE MEMORY SYSTEM, AND COMPARATOR AND MOS ANALOG XOR AMPLIFIER FOR USE IN THE SYSTEM	US	PALI-CI-0011-USXX	07/583,228	14-Sep-90	5,218,246	08-Jun-93	ISSUED
EASILY-TESTABLE HIGH SPEED DIGITAL COUNTERS	US	PALI-CI-0013-USXX	07/777,688	15-Oct-91	5,185,769	09-Feb-93	ISSUED
GROUND BOUNCING REDUCING CIRCUIT AND METHOD THEREOF	US	PALI-CI-0015-USXX	07/821,965	16-Jan-92	5,254,890	19-Oct-93	ISSUED
CONSTANT OUTPUT CIRCUIT	US	PALI-CI-0016-USXX	07/881,626	12-May-92	5,247,209	21-Sep-93	ISSUED
SELF-CONTROLLED OUTPUT STAGE WITH LOW POWER BOUNCING	US	PALI-CI-0018-USXX	07/925,507	05-Aug-92	5,315,187	24-May-94	ISSUED
METHOD AND DEVICE FOR IDENTIFYING A BUS MEMORY REGION	US	PALI-CI-0020-USXX	08/394,337	24-Feb-95	5,778,196	07-Jul-98	ISSUED
MEMORY MAPPING METHOD	US	PALI-CI-0031-USXX	09/598,837	21-Jun-00			PENDING
COMPUTER MOTHERBOARD FOR SUPPORTING VARIOUS MEMORIES	US	PALI-CI-0032-USXX	09/624,436	21-Jul-00			PENDING
PROGRAM-DOWNLOADABLE DATA PROCESSING SYSTEM AND METHOD FOR ACCESSING MEMORY BY USING A UNIFIED MEMORY SPACE THEREIN	US	PALI-CI-0033-USXX	09/779,557	09-Feb-01	6,516,398	04-Feb-03	ISSUED
VITERBI DETECTOR FOR PARTIAL RESPONSE MAXIMUM LIKELIHOOD SIGNAL PROCESSING	US	PALI-CI-0035-USXX	09/985,346	02-Nov-01			PENDING
ENCODING AND DECODING SYSTEM IN AN OPTICAL DISK STORAGE DEVICE	US	PALI-CI-0036-USXX	09/711,922	15-Nov-00			PENDING
DECODING SYSTEM AND METHOD IN AN OPTICAL DISK STORAGE DEVICE	US	PALI-CI-0037-USXX	09/826,884	06-Apr-01			PENDING

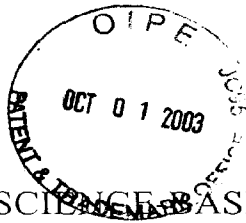
PATENT

DECODING SYSTEM AND METHOD IN AN OPTICAL DISK STORAGE DEVICE	US	PALJ-CI-0041-USXX	09/828,185	09-Apr-01		PENDING
DECODING SYSTEM AND METHOD IN AN OPTICAL DISK STORAGE DEVICE	US	PALJ-CI-0042-USXX	09/828,202	09-Apr-01		PENDING
SYSTEM FOR CONSTANT ANGULAR VELOCITY DISK RECORDING AND METHOD FOR LASER POWER CONTROL THEREOF	US	PALJ-CI-0048-USXX	09/919,849	02-Aug-01		PENDING
COMPUTER SYSTEM FOR MULTI-TYPE DRAM SUPPORT	US	PALJ-CI-0050-USXX	09/931,946	20-Aug-01		PENDING
LONG SEEK CONTROL SYSTEM AND METHOD IN AN OPTICAL STORAGE DEVICE	US	PALJ-CI-0053-USXX	09/988,656	20-Nov-01		PENDING
MULTIMODE FILTER	US	PALJ-CI-0054-USXX	09/893,438	29-Jun-01		PENDING
ERROR CORRECTION METHOD IN FULL RESPONSE READ CHANNELS	US	PALJ-CI-0055-USXX	09/984,745	31-Oct-01		PENDING
METHOD AND APPARATUS FOR ACCESSING TARGET DATA IN A DATA STORAGE MEDIUM	US	PALJ-CI-0058-USXX	10/022,156	14-Dec-01		PENDING
READ CHANNEL APPARATUS AND METHOD FOR AN OPTICAL STORAGE SYSTEM	US	PALJ-CI-0060-USXX	09/947,169	05-Sep-01		PENDING
LAYER JUMP CONTROL FOR AN OPTICAL DRIVE	US	PALJ-CI-0065-USXX	09/997,136	28-Nov-01		PENDING
METHOD OF LAYER JUMP BRAKING CONTROL FOR AN OPTICAL DRIVE	US	PALJ-CI-0067-USXX	10/121,287	12-Apr-02		PENDING
INTERNAL CIRCUIT STRUCTURE OF SEMICONDUCTOR CHIP WITH ARRAY-TYPE BONDING PADS AND METHOD OF FABRICATING THE SAME	US	PALJ-CI-0069-USX2	10/434,787			PENDING
INTERNAL CIRCUIT STRUCTURE OF SEMICONDUCTOR CHIP WITH ARRAY-TYPE BONDING PADS AND METHOD OF FABRICATING THE SAME	US	PALJ-CI-0069-USX3	10/434,788			PENDING
INTERNAL CIRCUIT STRUCTURE OF SEMICONDUCTOR CHIP WITH ARRAY-TYPE BONDING PADS AND METHOD OF FABRICATING THE SAME	US	PALJ-CI-0069-USXX	10/142,476	10-May-02		PENDING
OPTIMAL POWER CALIBRATION METHOD FOR AN OPTICAL DISC	US	PALJ-CI-0070-USXX	09/683,846	21-Feb-02		PENDING
IMPROVED ENCODING METHOD FOR AN OPTICAL RECORDER	US	PALJ-CI-0071-USXX	09/683,847	21-Feb-02		PENDING
PORTABLE PERIPHERAL APPARATUS WITH AN EMBEDDED STORAGE MODULE	US	PALJ-CI-0072-USXX	10/152,197	20-May-02		PENDING
METHOD AND RELATED APPARATUS FOR CONTROLLING TRANSMISSION INTERFACE BETWEEN AN EXTERNAL DEVICE AND A COMPUTER SYSTEM	US	PALJ-CI-0073-USXX	10/063,775	11-May-02		PENDING
ELECTRICALLY POLAR INTEGRATED CAPACITOR AND METHOD OF MAKING SAME	US	PALJ-CI-0080-USXX	10/065,043	13-Sep-02		PENDING
OPTICAL DISC DRIVE FOR RECORDING AT A CONSTANT ANGULAR VELOCITY	US	PALJ-CI-0081-USXX	10/065,765	18-Nov-02		PENDING
PIPELINED LOW COMPLEXITY FFT/IFFT PROCESSOR	US	PALJ-CI-0082-USXX	10/065,154	23-Sep-02		PENDING
ON-LINE JITTER MEASURE OF OPTICAL-DISC DEVICE	US	PALJ-CI-0083-USXX	10/604,426	21-Jul-03		PENDING
INTEGRATED CAPACITOR AND METHOD OF MAKING SAME	US	PALJ-CI-0084-USXX	10/065,663	07-Nov-02		PENDING
MATCHED FILTER WITH DECISION FEEDBACK EQUALIZER	US	PALJ-CI-0088-USXX	10/302,845	25-Nov-02		PENDING

PATENT

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DATA ALICER CAPABLE OF AUTOMATICALLY REMOVING CURRENT MISMATCH BETWEEN CURRENT PUMPS INCORPORATED THEREIN AND ITS OPERATING METHOD	US	PALI-CI-0089-USXX	10/248,444	21-Jan-03		PENDING
DAISY CHAINED ATA HOST CONTROLLERS IN A SINGLE PCI DEVICE	US	PALI-CI-0090-USXX	10/248,445	21-Jan-03		PENDING
DEVICE FOR WLAN BASEBAND PROCESSING WITH DC OFFSET REDUCTION	US	PALI-CI-0091-USXX	10/250,155	09-Jun-03		PENDING
METHOD AND SYSTEM FOR OPTICAL DISK DECODING	US	PALI-CO-0020-USXX	09/742,408	22-Dec-00		PENDING
VITERBI DETECTOR FOR EXTENDING TOLERABLE EXTENT OF DIRECT CURRENT BIAS	US	PALI-CO-0028-USXX	09/740,842	21-Dec-00		PENDING
DEADLOCK RESOLUTION WITH CACHE SNOOPING	US	PALI-CS-0003-USXX	07/308,206	08-Feb-89	5,133,074	21-Jul-92 ISSUED
BUS ARCHITECTURE WITH TWO LEVELS OF CONCURRENCY	US	PALI-CS-0043-USXX	08/033,402	19-Mar-93		PENDING
METHOD AND APPARATUS FOR SYNCHRONIZING CLOCK DISTRIBUTION OF A DATA PROCESSING SYSTEM	US	PALI-CS-0063-USXX	08/991,768	16-Dec-97	6,003,118	14-Dec-99 ISSUED
METHOD AND DEVICE FOR DOWN MIXING COMPRESSED AUDIO BIT STREAM HAVING MULTIPLE AUDIO CHANNELS	US	PALI-CS-0069-USXX	09/131,653	29-May-98	6,141,645	31-Oct-00 ISSUED
DISPLAY DEVICE CONTROLLER AND METHOD	US	PALI-DI-0004-USXX	07/755,305	05-Sep-91	5,229,758	20-Jul-93 ISSUED
REFERENCE TO RELATED APPLICATION	US	PALI-PE-0047-USXX	09/931,944	20-Aug-01		PENDING
METHOD AND APPARATUS OF CONTROLLING AN OPERATIONAL MODE OF A USB DEVICE	US	PALI-PE-0054-USXX	10/063,777	12-May-02		PENDING
SERIAL BUS DISK EXTENDER AND PORTABLE STORAGE DEVICE	US	PALI-PE-0067-USXX	10/065,903	27-Nov-02		PENDING



SCIENCE-BASED INDUSTRIAL PARK ADMINISTRATION
APPROVAL LETTER

Sender: James J. Lee, Director General
Science-Based Industrial Park Administration
Address: No.2, Hsin-An Road, Hsinchu Science-Based Industrial
Park, Hsinchu 300, Taiwan, R.O.C.
Telephone: (03)5773311
Fax: (03)5776221

Recipient: ALi Corporation

Priority:

Confidentiality/Declassifying Conditions:

Date: May 7, 2002

Docket No.: Park Investment No.: 0910010413

Attachment: None

Subject: Due to your company's business need, the application for the
change of your company name, from Acer Laboratories
Incorporation into ALi Corporation, has been approved by the
Administration upon your request.

Explanation: In response to your company's April 30, 2002 Yang-Kuan No.
91052 Letter.

Original Copy: ALi Corporation

Duplicate Copy: Investment Section / Industry-Commerce Section of this
Administration

I hereby declare that the version is in
conformity with the original document.
本人聲明上述英文譯本與中文原文相符
translator 譯者:

白 季 書

檔 號：
保存年限：

科學工業園區管理局 函

受文者：揚智科技股份有限公司

速別：

密等及解密條件：

發文日期：中華民國九十一年五月七日

發文字號：園投字第○九一○○一○四一三號

附件：無

主旨：貴公司因業務需要，申請將原英文名稱「Acer Laboratories Incorporation」變更為「ATI Corporation」乙案，本局同意，請查照。

說明：復 貴公司九十一年四月三十日揚管字第91052號函。

正本：揚智科技股份有限公司

副本：本局投資組、工商組

局長

李 昇 才

本局依分層負責規定
授權單位主管執行

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