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To the Honorabl	e Commissioner of Patents and Trademarks. Pl	lease record the attached original documents or copy thereof.
Additional name YUTAKA OC	onveying Party(ies): s of conveying parties attached: □ Yes ⋈ No GAWA, KAZUTOMO OGURA, ATOU AND KIYOTADA FUNANE	Name and Address of Receiving Party(ies): Name: Renesas Technology Corp. Address: 4-1, Marunouchi 2-chome City: Chiyoda-ku, Tokyo, Japan And
	ty Agreement	(2) Name: Hitachi ULSI Systems Co., Ltd. Address: 22-1, Josuihoncho 5-chome City: Kodaira-shi, Tokyo, Japan
If this document the execution da	Application Number(s): is being filed together with a new application, atc of the application is: Numbers Attached.	4. (b) Patent Numbers:
5. Name and Address of Party to whom Correspondence Concerning this Document Should be Mailed:		6. Total Number of Applications and Patents Involved: 1
Name:	Stanley P. Fisher	7. Total Fee: \$40.00 (37 C.F.R. § 3.41)
Address:	Reed Smith LLP 3110 Fairview Park Dr. Suite 1400 Falls Church, VA 22042	 ≅ Enclosed. ⊠ Authorized to be charged to deposit account.
		8. Deposit Account Number: 08-1480
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	and Signature:	
To the best original docume		September 26, 2003
Stanley P. Fishe	r, Registration No. 24, 344	uan Carfos A. Marquez, Registration No. 34,072
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PATENT REEL: 014548 FRAME: 0124

ASSIGNMENT

譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Renesas Technology Corp., and Hitachi ULSI Systems Co., Ltd.,

corporations organized under the laws of Japan,

located at 4-1, Marunouchi 2-chome, Chiyoda-ku, Tokyo, Japan,

and 22-1, Josuihoncho 5-chome, Kodaira-shi, Tokyo, Japan,

receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Technology Corp., and Hitachi ULSI Systems Co., Ltd.,

their successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Renesas Technology Corp., and Hitachi ULSI Systems Co., Ltd.,

their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made:

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said and Hitachi ULSI Systems Co., Ltd., Renesas Technology Corp.,

Signed on the date(s) indicated aside our signatures:

RECORDED: 09/26/2003

	INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
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5)_		
6)_		
7)		
8)		
9)_		
10)		

PATENT REEL: 014548 FRAME: 0125