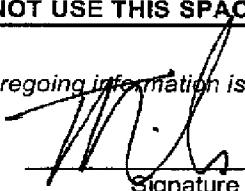


Docket No. 534334-026

Form PTO-1595 (Rev. 03/01) OMB No. 0651-0027 (exp. 5/31/2002) Tab settings ⇌ ⇌ ⇌	RECORDATION FORM COVER SHEET PATENTS ONLY	U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office
To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.		
1. Name of conveying party(ies): <u>Zarlink Semiconductor Limited</u> Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	2. Name and address of receiving party(ies) Name: <u>Zarlink Semiconductor V.N. Inc.</u> Internal Address: _____ _____ Street Address: <u>Suite 100, 121 Innovation Drive</u> _____ City: <u>Irvine</u> State: <u>CA</u> Zip: <u>92612</u> Additional name(s) & address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other _____ Execution Date: <u>February 21, 2002</u>		
4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is: _____ A. Patent Application No.(s) <u>10/068,593</u> B. Patent No.(s) _____ Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		
5. Name and address of party to whom correspondence concerning document should be mailed: Name: <u>Theodore D. Lienesch, Esq.</u> Internal Address: <u>Thompson Hine LLP</u> <u>2000 Courthouse Plaza NE</u> _____ Street Address: <u>10 West Second Street</u> _____ City: <u>Dayton</u> State: <u>OH</u> Zip: <u>45402</u>	6. Total number of applications and patents involved: <input type="text" value="1"/> 7. Total fee (37 CFR 3.41)..... \$ <u>40.00</u> <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to be charged to deposit account 8. Deposit account number: <u>20-0809</u> (Attach duplicate copy of this page if paying by deposit account)	
DO NOT USE THIS SPACE		
9. Statement and signature. <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i> <u>Theodore D. Lienesch, Reg. No. 28,235</u> Name of Person Signing  Signature <u>4/21/04</u> Date		
Total number of pages including cover sheet, attachments, and documents: <input type="text" value="4"/>		

CH \$40.00 200809 10068593

Mail documents to be recorded with required cover sheet information to:
 Commissioner of Patents & Trademarks, Box Assignments
 Washington, D.C. 20231

①

ASSIGNMENT

This Assignment is made the 21st day of February 2002, between:

Zarlink Semiconductor Limited, a United Kingdom company, of Cheney Manor, Swindon, Wiltshire SN2 2QW (hereinafter referred to as the Assignor); and

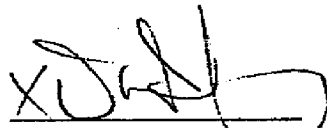
Zarlink Semiconductor V.N. Inc., an American corporation, of Suite 100, 121 Innovation Drive, Irvine, CA 92612, USA (hereinafter referred to as the Assignee).

The Assignor is the owner of the patents and patent applications set out in the Schedule hereto (hereinafter referred to as the Patents and Patent Applications) relating to the Inventions contained therein.


In consideration of the sum of Ten Pounds Sterling (£10) now paid to the Assignor by the Assignee (receipt of which is hereby acknowledged by the Assignor) the Assignor hereby assigns to the Assignee the Patents and Patent Applications and the Inventions referred to above, including all rights arising therefrom, including the right to file further applications in any country in respect of the Inventions, and including the right to bring proceedings for any previous infringement of the rights assigned by this Assignment.

It is also hereby agreed that the Assignor will, at the expense of the Assignee, execute all such documents, and do all such things, as may reasonably be required for the Assignee (or the nominee of the Assignee) to enjoy the full benefit of the property and rights hereby assigned, including (if required to do so by the Assignee) applying for patents in the name of the Assignor.

Signed for and on behalf of
Zarlink Semiconductor Limited


(signature, name & position)
Don McIntyre, Director

in the presence of:


(signature & name)

Signed for and on behalf of
Zarlink Semiconductor V.N. Inc.

in the presence of:

X J. Byrne

(signature, name & position)

Des Byrne, Asst. Secretary

X

Denise Oliveira

(signature & name of witness)

Denise Oliveira

SCHEDULE

Schedule to assignment dated the 21st day of February 2002, between:

Zarlink Semiconductor Limited and Zarlink Semiconductor V.N. Inc.

The Patent Applications included in the assignment are the following:

British Patent Application No. 0104332.2
United States Patent Application No. 10/068593
German Patent Application No. 10207305.8-51