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Tab settings		
1. Name of conveying party(ies):	2. Name and addre	ess of receiving party(ies)
Duality Semiconductor, Inc.	Name: <u>Gary H.</u>	Davison
	Internal Address	S:
Additional name(s) of conveying party(les) attached?	YesNo	
3. Nature of conveyance:		
Assignment Merg	jër Street Address:	7101 Sylvan Glen Lane
Security Agreement	nge of Name	
	City: Fairfax St	tation State: VA Zip: 22039
June 4, 2002 Execution Date:	Additional name(s)	& address(es) attached? Yes 🖌 No
4. Application number(s) or patent number(s):		
If this document is being filed together with a new application, the execution date of the application is:		
A. Patent Application No.(s) 60/293,176,	10/478,757 B. Patent No.(s	s)6,234,658; 6,360,283
PCT/US99/05778; PCT/US02/16560		
Additional numbers attached? Yes Vo		
Name and address of party to whom concorning document should be mailed:		applications and patents involved:
Name: Gray Cary Ware & Freidenric		R 3.41)\$_240
Attn: David Alberti	Enclosed	
	Authorized	to be charged to deposit account
		it number:
Street Address:153 Townsend Street		
Suite 800	07-1896	<u> </u>
City: San Francisco State: CA Zip: 941	07-1907	
DO NOT USE THIS SPACE		
9. Signature. Statement. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is true of the original document.		
David Alberti, Reg. No. 43,465	- Signature	$- \frac{1}{Date} - \frac{1}{Date} - \frac{1}{Date}$
Name of Person Signing	eges including cover sheet, attachments, a	nd documents: 3
Mail documents to be recorded with required cover sheet information to: Commissioner of Patents & Trademarks, Box Assignments		
Commissioner of Patents & Trademarks, Box Assignments Washington, D.C. 20231		

PATENT REEL: 014567 FRAME: 0310

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THIS ASSIGNMENT, by <u>DUALITY SEMICONDUCTOR, INC.</u>, residing at <u>1797 HAWTHORNE</u> <u>RIDGE C1., VIENNA, VA_22182</u> (hereinafter referred to as "the Assignor"), through its bankruptcy trustee, witnesseth:

WHEREAS, the Assignor owns certain new and useful improvements referred to herein as Intellectual Property, the Intellectual Property including without limitation, inventions, related hardware, software, algorithms, trade secrets, the following patents and patent applications, and all related technical information described in or related to inventions of, the following patents and patent applications:

> U.S. Patent No. 6,234,658 - Method and Apparatus For Producing Signal Processing Circuits in the Delta Sigma Domain;

Continuation Patent Application filed September 1999. which was awarded Patent No. 6,360,283 on March 19, 2002;

Related International Patent (PCT) Application WO/55746 filed March 17, 1999 - Method and Apparatus for Producing Signal Processing Circuits in the Delta Sigma Domain;

Provisional Patent Application assigned Serial No. 60/293,176, Docket No. 55688.000002, entitled: Delta-Sigma Signal Processing and its successor PCT and U.S. Patent Application entitled: Method and Apparatus For Managing Energy in Plural Energy Storage Units as filed on May 28, 2002.

WHEREAS, <u>Gary H. Davison</u>, having a principal place of business at <u>7101 Sylvan Glen</u> <u>Lane, Fairfax Station, VA. 22039</u> (hereinafter referred to as "the Assignee"), is desirous of acquiring the entire right, title, and interest in and to said Intellectual Property, the right to file applications on said Intellectual Property and the entire right, title and interest in and to any applications, including provisional applications for Letters Patent of the United States or other countries claiming priority to said applications, and in and to any Letters Patent or Patents, United States or foreign, obtained or to be obtained therefor and thereon.

NOW, THEREFORE, In consideration of good and sufficient consideration, the receipt of which is hereby acknowledged, the Assignor has sold, assigned, transferred, and set over, and by these presents does sell, assign, transfer, and set over, unto the Assignee, his successors, legal representatives, and assigns the entire right, title, and interest in and to the Intellectual Property, the right to file applications on said inventions and the entire right, title and interest in and to any, applications for Letters Patent of the United States or other countries claiming priority to said applications, and any, and all Letters Patent or Patents of the United States of America and all foreign countries granted or that may be granted therefor and thereon, and in and to any and all applications claiming priority to said applications, divisions, continuations, and continuations-in-part of said applications, and relissues and extensions of said Letters Patent or Patents, and all rights under the international Convention for the Protection of Industrial Property, the same to be held and enjoyed by the Assignee, for his own use and behalf and the use and behalf of his successors, legal representatives, and assigns, to the full end of the term or terms for which Letters Patent or Patents may be granted as fully and entirely as the same would have been held and enjoyed by the Assignor had this sale and assignment not been made;

AND for the same consideration, the Assignor hereby covenants and agrees to and with the Assignee, his successors, legal representatives, and assigns, that, at the time of execution

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and delivery of these presents, the Assignor is the sole and lawful owner of the entire right, title, and interest in and to the Intellectual Property, above-mentioned, and that the same is unencumbered, and that the Assignor has good and full right and lawful authority to sell and convey the same in the manner herein set forth;

AND for the same consideration, the Assignor hereby covenants and agrees to and with the Assignee, his successors, legal representatives, and assigns that the Assignor will, whenever counsel of the Assignee, or the counsel of his successors, legal representatives, and assigns, shall advise that any proceeding in connection with said Intellectual Property, including proceedings in connection with said inventions or said applications for Letters Patent or Patents, or any proceedings in connection with Letters Patent or Patents for said inventions in any country, including interference proceedings, Is lawful and desirable, or that any application claiming priority to said applications, division, continuation, or continuation-in-part of any applications for Letters Patent or Patents, or any release or extension of any Letters Patent or Patents to be obtained thereon, is lawful and desirable, sign all papers and documents, take all lawful oaths, and do all acts necessary or required to be done for the procurement, maintenance, enforcement and defense of the Intellectual Property, without charge to the Assignee, his successors, legal representatives, and assigns, but at the cost and expense of the Assignee, his successors, legal representatives, and assigns;

AND the Assignor hereby requests the Commissioner of Patents to issue any and all said Letters Patent of the United States to the Assignee, as the Assignee of said inventions and the Letters Patent to be issued thereon for the sole use and behalf of the Assignee, his successors, legal representatives, and assigns.

Date **G**i

Signature of Assignor

Robert O. Tyler, Trustee of Duality Semiconductor, Inc. (VSB#06271) Tyler, Barti, Gorman & Ramsdell, PLC 206 N. Washington St. Ste. 200 Alexandria, VA 22314 (703) 549-5000

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