


Form PTO-1595 (Rev. 10/02) OMB No. 0651-0027 (exp. 6/30/2005) Tab settings ⇌ ⇌ ⇌ ▼ ▼ ▼ ▼ ▼ ▼ ▼		RECORDATION FORM COVER SHEET <b>PATENTS ONLY</b>		U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office	
To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.					
1. Name of conveying party(ies):  TERACONNECT INC.		2. Name and address of receiving party(ies) Name: <u>Altera Corporation</u>  Internal Address: _____  _____  Street Address: <u>101 Innovation Drive</u>  _____  City: <u>Santa Clara</u> State: <u>CA</u> Zip: <u>95134</u>  Additional name(s) & address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No			
Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No					
3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other _____  Execution Date: <u>04-02-2004</u>					
4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is: _____ A. Patent Application No.(s) _____ B. Patent No.(s) <u>6,656,757</u>  _____ Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No					
5. Name and address of party to whom correspondence concerning document should be mailed: Name: <u>Scott J. Asmus (LSC140A-Div)</u>  Internal Address: <u>Maine &amp; Asmus</u>  _____  Street Address: <u>100 Main St., Suite 2</u>  <u>PO Box 3445</u>  City: <u>Nashua</u> State: <u>NH</u> Zip: <u>03061</u>		6. Total number of applications and patents involved: <u>1</u>  7. Total fee (37 CFR 3.41).....\$ <u>40.00</u> <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to be charged to deposit account  8. Deposit account number:  <u>500323</u>			
DO NOT USE THIS SPACE					
9. Signature.  Scott J. Asmus, Reg. No. 42,269 Name of Person Signing					
 <u>May 6, 2004</u> Signature Date					
Total number of pages including cover sheet, attachments, and documents: <u>10</u>					

Mail documents to be recorded with required cover sheet information to:  
Commissioner of Patents & Trademarks, Box Assignments  
Washington, D.C. 20231

CH \$40.00 500323 6656757

**PATENT ASSIGNMENT  
(US)**

**ASSIGNMENT AND TRANSFER OF PATENTS**

**WHEREAS**, TeraConnect, Inc., a Delaware corporation, with offices at 35 Forest Ridge Road, Suite 260, Concord, MA 01742 ("Assignor") owns certain patent applications and/or registrations, as listed in Exhibit A attached hereto and incorporated herein by this reference ("Patents"); and

**WHEREAS**, Altera Corporation, a Delaware corporation, with offices at 101 Innovation Drive., Santa Clara, CA 95134 ("Assignee"), desires to acquire all of the right, title and interest of Assignor in, to and under the Patents;

**WHEREAS**, Assignor and Assignee have entered into a certain Asset Purchase Agreement, dated as of April \_\_, 2004 ("Assignment Agreement"), assigning, among other things, all right, title and interest in and to the Patents and in and to the registrations for same from Assignor to Assignee;

**NOW, THEREFORE**, for good and valuable consideration described in the Assignment Agreement, the receipt and sufficiency of which are hereby acknowledged, Assignor does hereby irrevocably sell, assign, transfer and convey unto Assignee all of its right, title and interest in and to the Patents, including all divisions, continuations, continuations-in-part, reexaminations, substitutions, reissues, extensions and renewals of the applications and registrations for the Patents (and the right to apply for any of the foregoing); all rights to causes of action and remedies related thereto (including, without limitation, the right to sue for past, present or future infringement, misappropriation or violation of rights related to the foregoing); and any and all other rights and interests arising out of, in connection with or in relation to the Patents throughout the world, including without limitation all foreign counterparts and foreign equivalents of any of the foregoing.

Assignor authorizes and requests the patent officials in the United States and in any and all foreign jurisdictions to issue any and all letters patent and foreign counterparts or equivalents thereof to Altera Corporation, as assignee of the entire interest of Assignor therein, and covenants that Assignor has full right to convey the entire interest herein assigned and that Assignor has not executed and will not execute any agreements in conflict herewith.

Subject to Section 8.16 of the Assignment Agreement, Assignor further agrees, for itself, its successors and assigns, to execute such further documents and to perform such further lawful acts as may reasonably be required to effectuate this assignment.

IN WITNESS WHEREOF, Assignor and Assignee have caused this assignment to be duly executed by their respective authorized officers on this \_\_\_\_ day of April, 2004.

TERACONNECT, INC.

ALTERA CORPORATION

By: John K. Langevin  
Name: John K. Langevin  
Title: President & CEO

By: Lance Lissner  
Lance Lissner  
Senior Vice President Business Development

STATE OF Massachusetts )  
COUNTY OF Suffolk ) ss.

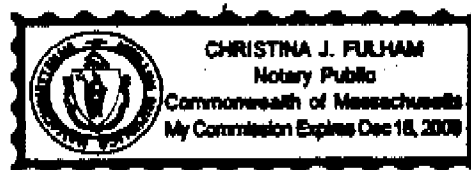
On April 2, 2004, before me, the undersigned notary public in and for said County and State, personally appeared John K. Langevin

\_\_\_\_ personally known to me [or]  
X proved to me on the basis of satisfactory evidence  
MA drivers license

to be the person(s) whose name(s) is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacity(ies) and that, by his signature(s) on the instrument, the person(s) or the entity(ies) upon behalf of which the person(s) acted executed the instrument.

Witness my hand and official seal.

Christina J. Fulham  
My commission expires on



STATE OF CALIFORNIA )

COUNTY OF Santa Clara ) ss.

On April 13, 2004, before me, the undersigned notary public in and for said County and State, personally appeared Lance Lissner,

☒ personally known to me [or]

☐ proved to me on the basis of satisfactory evidence

to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacity and that, by his signature on the instrument, the person upon behalf of which the person acted executed the instrument.

Witness my hand and official seal.

Jean M. Futrell

My commission expires on

January 28, 2008



EXHIBIT A  
PATENTS

Issued U.S. Patents and Pending Patent Applications

Patent Title	Patent No.	App. No.	Pub. No.	Pub. Date	Pub. Date
METHOD FOR INTEGRATION OF INTEGRATED CIRCUIT DEVICES	D-4438	LSC134J	09/653369	9/1/2000	# 6,337,265 dated 1/8/02
METHOD OF EQUALIZING DEVICE HEIGHTS ON A CHIP	D-4442	LSC135J	09/654425	9/1/2000	# 6,316,286 dated 11/13/01
A FIBER OPTIC CONNECTOR	D-4527	LSC138J	09/726138	11/29/2000	# 6,530,700 dated 3/11/2003
FIBER OPTIC CABLE CONNECTOR	D-4526	LSC139J	09/726150	11/29/2000	# 6,641,310 dated 11/04/2003
ELECTRO-OPTICAL TRANSCIVER SYSTEM WITH CONTROLLED LATERAL LEAKAGE AND METHOD OF MAKING IT	D-4462	LSC140J	09/653378	9/1/2000	# 6,344,664 dated 2/5/02
ELECTRO-OPTICAL TRANSCIVER SYSTEM WITH CONTROLLED LATERAL LEAKAGE AND METHOD OF MAKING IT	D-4462	LSC140J-DIV	10/016382	12/10/2001	# 6,485,995 dated 11/26/02
ELECTRO-OPTICAL TRANSCIVER SYSTEM WITH CONTROLLED LATERAL LEAKAGE AND METHOD OF MAKING IT	D-4462	LSC140A-DIV	10/241991	9/12/2002	# 6,656,757 dated 12/2/2003
ELECTRO-OPTICAL TRANSCIVER SYSTEM WITH CONTROLLED LATERAL LEAKAGE AND METHOD OF MAKING IT	D-4462	LSC140B-DIV	10/437133	5/13/2003	# 6,673,642 dated 1/6/2004

Patent Title	Patent Number	Patent Class	Patent No.	Patent Date	Patent Title	Patent No.	Patent Date
ELECTRO-OPTICAL TRANSDUCER SYSTEM WITH CONTROLLED LATERAL LEAKAGE AND METHOD OF MAKING IT	D-4462	LSC140C-DIV	10/437132	5/13/2003	# 6,686,216 dated 2/3/04		
MULTI-WAVELENGTH OPTICAL COMMUNICATION SYSTEM	D-4466	LSC141J	09/737226	12/14/2000	pending		
OPTOELECTRONIC CONNECTOR SYSTEM	D-4436	LSC142J	09/653647	9/1/2000	# 6,434,308 dated 8/13/02		
METHOD OF MAKING OPTOELECTRONIC DEVICES USING SACRIFICIAL DEVICES	D-4454	LSC143J	09/727940	12/1/2000	6,485,993 dated 11/26/02		
METHOD OF MAKING AN OPTOELECTRONIC DEVICE USING MULTIPLE ETCH STOP LAYERS	D-4443	LSC144J	09/693383	10/20/2000	# 6,423,560 dated 7/23/02		
OPTOELECTRONIC DEVICE WITH INTEGRATED PASSIVE OPTICAL ELEMENTS AND METHOD	D-4447	LSC145J	09/693244	10/20/2000	# 6,453,081 dated 9/17/02		
ACTIVE OPTICAL NETWORK	D-4450	LSC146J	09/713761	11/15/2000	# 6,447,174 dated 9/10/02		
HIGH RATE OPTICAL CORRELATOR IMPLEMENTED ON A SUBSTRATE	D-4459	LSC147J	09/723076	11/27/2000	# 6,693,712 dated 2/17/04		
METHOD AND APPARATUS FOR REAL TIME OPTICAL CORRELATION	D-4455	LSC148J	09/727941	12/1/2000	# 6,538,791 dated 3/25/03		
METHOD AND APPARATUS FOR READING OUT AND WRITING TO AN OPTICAL DISC	D-4460	LSC149J	09/727951	12/1/2000	# 6,614,744 dated 9/2/03		

Title	Patent Ref No.	Device Design No.	Application No.	Filing Date	Issue Date
CLUSTER INTEGRATION APPROACH TO OPTICAL TRANSCEIVER ARRAYS AND FIBER BUNDLES	D4444	SLM01-US	09/687337	10/13/2000	# 6,527,456 dated 3/4/03
OPTICAL COMMUNICATION NETWORK WITH RECEIVER RESERVED CHANNEL	D4434 & D4440	SLM02-US	09/653727	9/1/2000	# 6,674,971 dated 1/6/04
SECURITY MAPPING AND AUTO RECONFIGURATION	D4461	SLM04-US	09/732974	12/8/2000	pending
STAR TOPOLOGY NETWORK WITH FIBER INTERCONNECT ON CHIP	D4464	SLM08-US	09/732432	12/7/2000	pending
WAFER SCALE INTEGRATION AND REMOTE SUBSYSTEMS USING OPTO-ELECTRONIC TRANSCEIVERS	D4446 & D4449	SLM09-US	09/693664	10/20/2000	# 6,567,963 dated 5/20/2003
AN OPTICAL WAVEGUIDE ASSEMBLY FOR INTERFACING A TWO-DIMENSIONAL OPTOELECTRONIC ARRAY TO FIBER BUNDLES	TRACNT 506	TERA06-US	10/021978	12/13/2001	# 6,601,998 dated 8/5/2003
A PACKAGING SYSTEM FOR TWO-DIMENSIONAL OPTOELECTRONIC ARRAYS	TRACNT 507	TERA07-US	10/029434	12/13/2001	# 6,459,842 dated 10/1/2002
PRECISION GRID STANDOFF FOR OPTICAL COMPONENTS ON OPTO-ELECTRONIC DEVICES	D4523	TERA12-US	09/840,609	4/23/2001	# 6,614,949 dated 9/2/2003
SELF ALIGNING OPTICAL INTERCONNECT WITH MULTIPLE OPTO-ELECTRONIC DEVICES PER FIBER CHANNEL	D4540 & D4536	TERA14-US	09/851714	5/9/2001	# 6,398,425 dated 6/4/2002
SELF ALIGNING OPTICAL INTERCONNECT WITH MULTIPLE OPTO-ELECTRONIC DEVICES PER FIBER CHANNEL	D4540 & D4536	TERA14-CIP	10/162729	6/4/2002	pending

Patent Title	IPC Class. Ref.	Priority No.	App. No.	Pub. No.	Pub. Date
LASER BEAM STEERING CHIP		TERA15-US	10/145073	5/13/2002	# 6,665,111 dated 12/16/2003

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