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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Valence Semiconductor, Inc.

2. Name and address of receiving party(ies)

Name: Maxim Integrated Products, Inc.

Internal Address: _____

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance:



Assignment



Merger



Security Agreement



Change of Name



Other _____

Street Address: 120 San Gabriel Drive

City: Sunnyvale State: CA Zip: 94086

Execution Date: 02/26/2004

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: _____

A. Patent Application No.(s) _____

60/255,979, 09/883,554, 10/022,465

B. Patent No.(s) _____

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Blakely, Sokoloff, Taylor & Zafman LLP

Internal Address: _____

Street Address: 12400 Wilshire Boulevard,

Seventh Floor

City: Los Angeles State: CA Zip: 90025

6. Total number of applications and patents involved:

7. Total fee (37 CFR 3.41).....\$ 120.00

☐ Enclosed☒ Authorized to be charged to deposit account

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9. Signature.

Roger W. Blakely, Jr.

Name of Person Signing



Signature

May 26, 2004

Date

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PATENT
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ASSIGNMENT

This Nunc Pro Tunc Assignment is effective February 26, 2004 by and between the below identified Assignor and Assignee.

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, the undersigned:

Valence Semiconductor, Inc. ("Assignor")

hereby sells, assigns, and transfers to:

Maxim Integrated Products, Inc. ("Assignee")

a Corporation of Delaware having a principal place of business at 120 San Gabriel Drive, Sunnyvale, California 94086 USA, and its successors, assigns and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements that are disclosed in the U.S. provisional patent applications, U.S. non-provisional patent applications and International PCT applications as listed on the attached "Schedule A - U.S. Provisional Patent Applications," "Schedule B - U.S. Non-Provisional Patent Applications" and "Schedule C - International PCT Applications," respectively.

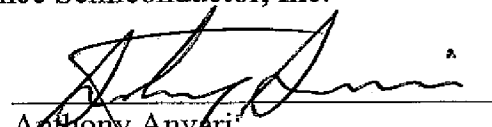
and in and to said applications and all design, utility, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all foreign countries on said inventions and improvements; and in and to all rights of priority from the filing of the applications set forth in Schedules A-C;

agrees that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all design, utility, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assignee, its successors, assigns, and representatives, all facts known to the undersigned relating to said inventions and improvements and the history thereof; and generally assist said Assignee, its successors, assigns or representatives in securing and maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said inventions and improvements, in said Assignee, its successors, assigns and legal representatives; and

covenants with said Assignee, its successors, assigns and legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Valence Semiconductor, Inc.

Date: February 26, 2004

By: 

Anthony Anyari
Vice President

Schedule A - U.S. Provisional Patent Applications

Application Number	Filing Date	Title
60/243,223	October 25, 2000	Utilizing Power Line Networking as a General Purpose Transport for a Variety of Signals
60/245,179	November 1, 2000	System and Method to Use a Wired Network to Extend Radio Coverage of a Wireless Network
60/254,702	December 11, 2000	Laptop Wireless Systems Integrated with a LCD Panel
60/255,979	December 15, 2000	Fully Digital Symbol Synchronization Technique
60/255,844	December 15, 2000	Frame Synchronization Technique for OFDM Based Modulation Scheme
60/255,843	December 15, 2000	Receiver Apparatus to Reduce Errors Based on Repeated Transmissions
60/255,830	December 15, 2000	Blind Channel Estimation and Data Detection for PSK OFDM-Based Receivers
60/257,085	December 20, 2000	Digital Audio Transmission Over a Digital Video Interface (DVI) Link
60/266,803	February 6, 2001	OFDM Spectrum Shaping Technique
60/287,532	April 30, 2001	Wideband Symbol Synchronization in the Presence of Multiple Strong Narrowband Interference
60/290,378	May 11, 2001	Narrowband Interference Canceller for Wideband Communication Systems
60/291,497	May 16, 2001	Adaptation Algorithm Based on Signal Statistics for Automatic Gain Control
60/293,403	May 24, 2001	Flexible Majority Logic Approach to Improve Detection of Transmitted Redundant Data
60/302,103	June 28, 2001	Closed Loop Digital Display Corrector
60/308,430	July 27, 2001	Apparatus to Decrease the Spurs Level in a Phase-Locked Loop
60/313,139	August 16, 2001	Low Noise Image-Reject GM-C Filter with New Transconductance
60/316,148	August 30, 2001	System and Method for Simultaneously Transporting Different Types of Information Over a Power Line
60/316,085	August 30, 2001	Voice Conferencing Over Power Line
60/333,322	November 26, 2001	Method of Switching Internet Protocol Packets
60/333,968	November 28, 2001	Enhanced Method of Encoding Progressive Video Sequences when Employing Interlaced CODECs

Schedule B - U.S. Non-Provisional Patent Applications

Application Number	Filing Date	Title
09/852,215	May 8, 2001	Digital Audio Transmission Over a Digital Visual Interface (DVI) Link
09/883,554	June 16, 2001	System and Method for Modulation of Non-Data Bearing Carriers in a Multi-Carrier Modulation System
09/900,087	July 6, 2001	System and Method of Signal Wave Shaping for Spectrum Control of an OFDM Signal
09/939,424	August 24, 2001	Wideband Symbol Synchronization in the Presence of Multiple Strong Narrowband Interference
09/940,381	August 27, 2001	Blind Channel Estimation and Data Detection for PSK OFDM-Based Receivers
09/941,255	August 28, 2001	Utilizing Powerline Networking as a General Purpose Transport for a Variety of Signals
09/942,298	August 29, 2001	Adaptation Algorithm Based on Signal Statistics for Automatic Gain Control
09/945,054	August 31, 2001	Flexible Majority Logic Approach to Improve Detection of Transmitted Redundant Data
09/944,559	August 31, 2001	Narrowband Interference Canceller for Wideband Communication Systems
10/021,339	October 30, 2001	System and Method to Use a Wired Network to Extend Radio Coverage of a Wireless Network
10/021,794	December 11, 2001	Laptop Wireless Systems Integrated with an LCD Panel
10/022,465	December 14, 2001	Fully Digital Symbol Synchronization Technique
10/017,607	December 14, 2001	Frame Synchronization Technique for OFDM Based Modulation Scheme
10/028,078	December 21, 2001	Apparatus and Method for a Low-Rate Data Transmission Mode Over a Power Line
10/185,679	June 27, 2002	Closed Loop Digital Display Corrector
10/207,007	July 26, 2002	Apparatus to Decrease the Spurs Level in a Phase-Locked Loop
10/222,748	August 16, 2002	Low Noise Image-Reject GM-C Filter
10/233,165	August 30, 2002	Voice Conferencing Over a Power Line
10/233,171	August 30, 2002	System and Method for Simultaneously Transporting Different Types of Information Over a Power Line
10/304,632	November 26, 2002	Method of Switching Internet Protocol Packets
10/306,210	November 27, 2002	Enhanced Method of Encoding Progressive Video Sequences when Employing Interlaced CODECs

Schedule C - International PCT Applications

Application Number	Filing Date	Title
PCT/US01/51313	October 25, 2001	Utilizing Powerline Networking as a General Purpose Transport for a Variety of Signals
PCT/US01/50918	November 1, 2001	System and Method to Use a Wired Network to Extend Radio Coverage of a Wireless Network
PCT/US01/48894	December 17, 2001	Blind Channel Estimation and Data Detection for PSK OFDM-Based Receivers
PCT/US01/48895	December 17, 2001	Fully Digital Symbol Synchronization Technique
PCT/US01/49098	December 17, 2001	Frame Synchronization Technique for OFDM Based Modulation Scheme
PCT/US01/49733	December 20, 2001	Digital Audio Transmission Over a Digital Video Interface (DVI) Link
PCT/US02/03695	February 6, 2002	System and Method of Signal Wave Shaping for Spectrum Control of an OFDM Signal
PCT/US02/18621	June 10, 2002	Control of Power Spectral Density by Modulation of Subcarriers Which do Not Carry Data
PCT/US02/40312	December 17, 2002	Apparatus and Method for a Low-Rate Data Transmission Mode Over a Power Line