


Form PTO-1596 (Rev. 10/02) OMB No. 0651-0027 (exp. 6/30/2005) Tab settings ⇨ ⇨ ⇨ ▼ ▼ ▼ ▼ ▼ ▼ ▼		RECORDATION FORM COVER SHEET PATENTS ONLY		U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office	
To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.					
1. Name of conveying party(ies): EiC Corporation 45738 Northport Loop West Fremont, CA 94538  Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No			2. Name and address of receiving party(ies) Name: EiC Enterprises Limited  Internal Address: Cedar House    Street Address: 41 Cedar Avenue Hamilton, HM 12 BERMUDA  City: _____ State: _____ Zip: _____  Additional name(s) & address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		
3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other _____  Execution Date: 05/18/2004					
4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is: _____ A. Patent Application No.(s) 10/267,215 10/820,343; 10/267,342 B. Patent No.(s) 6700076, 6521972 6326849, 6522201, 6424223, 6556082 Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No					
5. Name and address of party to whom correspondence concerning document should be mailed: Name: Don J. Pelfo  Internal Address: Preston Gates Ellis & Rouvelas Meeds LLP   Street Address: 1735 New York Avenue, NW Suite 500  City: Washington State: DC Zip: 20006			6. Total number of applications and patents involved: 9 7. Total fee (37 CFR 3.41).....\$ 360.00 <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to be charged to deposit account  8. Deposit account number: 50-1067		
DO NOT USE THIS SPACE					
9. Signature.  Don J. Pelfo, Reg. No. 33,754 Name of Person Signing   Signature  3 June 2004 Date  Total number of pages including cover sheet, attachments, and documents: 4					

Mail documents to be recorded with required cover sheet information to:  
Commissioner of Patents & Trademarks, Box Assignments  
Washington, D.C. 20231

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**MEMORANDUM OF ASSIGNMENT OF PATENTS**

THIS MEMORANDUM OF ASSIGNMENT OF PATENTS ("**Memorandum**") is entered into by EiC Corporation, a California corporation ("**Assignor**"), for the benefit of EiC Enterprises Limited, a corporation organized under the laws of the Islands of Bermuda ("**Assignee**").

Assignor hereby acknowledges that Assignor has assigned to Assignee all right, title and interest of Assignor in certain intangible personal property consisting of, among other things, U.S. patents and patent applications and the inventions covered thereby, all as evidenced by those certain Product Development Agreements dated as of November 1, 1996 and June 24, 2001, and those certain Manufacturing Development Agreements dated as of November 1, 1996 and November 1, 2000, all as amended to date, between Assignor and Assignee (the "**Technology Agreements**"), which Technology Agreements are incorporated herein by this reference. Without limiting the generality of the foregoing or the Technology Agreements, Assignor acknowledges and agrees that Assignor has assigned to Assignee all of the Assignor's rights, title and interest in the patents and patent applications listed on **Exhibit A** attached hereto and incorporated herein by this reference.

This Memorandum is executed on the date set forth below by a duly authorized officer or agent of Assignor and is submitted for recording in the U.S. Patent and Trademark Office.

Assignor's Address:

EiC Corporation  
45738 Northport Loop West  
Fremont, California 94538

ASSIGNOR:

EIC CORPORATION

By 

Its PRESIDENT

Date of Execution 5/18/04

Assignee's Address:

EiC Enterprises Limited  
Cedar House, 41 Cedar Avenue  
Hamilton, HM 12, Bermuda

CALIFORNIA ALL-PURPOSE ACKNOWLEDGMENT

State of California

County of: Alameda

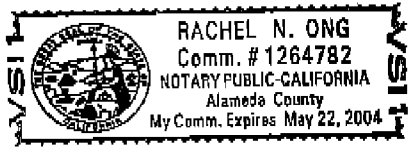
On May 18, 2004 before me, Rachel N. Ong  
Date Name, Title of Officer-e.g., Jane Doe, Notary Public

personally appeared Jerry Curtis  
Name of Signer(s)

☒ personally known to me - OR

☐ proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.  
WITNESS my hand and official seal

Rachel N. Ong  
Signature of Notary Public



**EXHIBIT A**

<u>Patent No.</u>	<u>Title</u>	<u>Inventor</u>	<u>Assignee</u>
6,700,076	MULTI-LAYER INTERCONNECT MODULE AND METHOD OF INTERCONNECTION	WANG, Nanlei SUN, Xiao-Peng	EiC Corporation
6,521,972	RF POWER TRANSISTOR HAVING LOW PARASITIC IMPEDANCE INPUT FEED STRUCTURE	ZHOU, Wei-Shu HSIAO, Shuo-Yuan WANG, Nanlei Larry	EiC Corporation
6,326,849	ISOLATION CIRCUIT FOR USE IN RF AMPLIFIER BIAS CIRCUIT	WANG, Nanlei Larry HSIAO, Shuo-Yuan ZHOU, Wei-Shu XU, Shihui	EiC Corporation
6,522,201	RF AMPLIFIER HAVING SWITCHED LOAD IMPEDANCE FOR BACK- OFF POWER EFFICIENCY	HSIAO, Shuo-Yuan ZHOU, Wei-Shu WANG, Nanlei Larry	EiC Corporation
6,424,223	MMIC POWER AMPLIFIER WITH WIREBOND OUTPUT MATCHING CIRCUIT	WANG, Nanlei Larry HSIAO, Shuo-Yuan SUN, Xiao-Peng	EiC Corporation
6,556,082	TEMPERATURE COMPENSATED CURRENT MIRROR	WANG, Nanlei Larry XU, Sarah HSIAO, Shuo-Yuan	EiC Corporation

**Patent Application**

<u>Serial No.</u>	<u>Title</u>	<u>Inventor</u>	<u>Assignee</u>
10/267,215	HETEROJUNCTION BIPOLAR TRANSISTOR HAVING NON-UNIFORMLY DOPED COLLECTOR FOR IMPROVED SAFE- OPERATING AREA	LEE, Chien Ping CHAU, Hin Fai WANG, Nanlei Larry DUNNROWICZ, Clarence John CHEN, Yan LIN, Barry Jia-Fu	EiC Corporation
10/820,343	HETEROJUNCTION BIPOLAR TRANSISTOR HAVING NON-UNIFORMLY DOPED COLLECTED FOR IMPROVED SAFE- OPERATING AREA	LEE, Chien Ping CHAU, Hin Fai WANG, Nanlei Larry DUNNROWICZ, Clarence John CHEN, Yan LIN, Barry Jia-Fu	EiC Corporation
10/267,342	HETEROJUNCTION BIPOLAR TRANSISTOR HAVING WIDE BANDGAP MATERIAL IN COLLECTOR	CHAU, Hin Fai CHEN, Yan DUNNROWIEZ, Clarence John LEE, Chien Ping	EiC Corporation