

11-24-2003

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To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

<p>1. Name of conveying party(ies): DOUGLAS J MCKENNEY STEVEN M. EMERSON</p> <p>Additional names of conveying party(ies) attached? <input type="checkbox"/> yes <input checked="" type="checkbox"/> no</p> <p>3. Nature of Conveyance <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other _____</p> <p>Execution Date: 14 November 2003</p>	<p>2. Name and address of receiving party(ies): Name: <u>LSI Logic Corporation</u> Internal Address: <u>Mail Stop D-106</u></p> <p>Street Address: <u>1551 McCarthy Boulevard</u> City <u>Milpitas</u> State <u>California</u> Zip <u>95035</u></p> <p>Additional name(s) & address(es) attached? <input type="checkbox"/> yes <input checked="" type="checkbox"/> no</p>
<p>4. Application number(s) or patent number(s): <u>101713492</u></p> <p>TITLE: FLEXIBLE DESIGN OF MEMORY USE IN INTEGRATED CIRCUITS</p> <p>If this document is being filed together with a new application, the execution date of the application is .</p> <p>A. Patent Application No(s). LSI Logic Docket 03-0085</p> <p>B. Patent No(s). 1</p> <p style="text-align: right;">Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>	
<p>5. Name and address of party to whom correspondence concerning document should be mailed:</p> <p style="text-align: center; font-size: 1.2em;">*24319*</p> <p style="text-align: center; font-size: 1.1em;">24319</p> <p style="text-align: center; font-size: 0.8em;">PATENT TRADEMARK OFFICE</p>	<p>6. Total number of applications and patents involved: <u>1</u></p> <p>7. Total fee (37 CVF 3.41): <u>\$ 40.00</u> <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to be charged to deposit account <input checked="" type="checkbox"/> Total fee due <input checked="" type="checkbox"/> Any deficiencies in the enclosed fees</p> <p>8. Deposit account number: <u>12-2252</u></p> <p>(Attach duplicate copy of this page if paying by deposit account)</p>
<p>DO NOT USE THIS SPACE</p>	
<p>9. Statement and signature</p> <p>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</p> <p><u>Karuna Ojanen</u> Name of Person Signing</p> <p style="text-align: center;"> Signature</p> <p style="text-align: right;"><u>14 November 2003</u> Date</p> <p style="text-align: right;">Total number of pages comprising cover sheet: <u>1</u></p>	

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A S S I G N M E N T

Whereas, we

(1) Douglas J McKenney,
County of Hennepin,City of Eden Prairie,
and State of Minnesota,and (2) Steven M. Emerson,
County of Carver,City of Chanhassen,
and State of Minnesota.

have invented certain improvements in

FLEXIBLE DESIGN OF MEMORY USE IN INTEGRATED CIRCUITS

and have executed, respectively, a United States patent application therefor on

Dates Inventors

Signed Declaration (1) 14 November 2003, and (2) 14 November 2003.

And whereas, LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at Milpitas, California (hereinafter called LSI Logic) desires to acquire the entire right, title and interest in the said application and invention, and to any United States and foreign patents to be obtained therefor;

Now therefore, for a valuable consideration, receipt whereof is hereby acknowledged, we the above named hereby sell, assign, and transfer to LSI Logic, its successors and assigns, the entire right, title and interest in the said application and invention therein disclosed for the United States and foreign countries, and all rights of priority resulting from the filing of said United States application, and we request the Commissioner of Patents to issue any Letters Patent granted upon the invention set forth in said application to LSI Logic, its successors and assigns; and we hereby agree that LSI Logic may apply for foreign Letters Patent on said invention and we will execute all papers necessary in connection with the United States and foreign applications when called upon to do so by LSI Logic.

Signed

Inventor at Bloomington, MN

(1)

on 11/14/2003


(Douglas J McKenney)

Inventor's Name

Inventor
SignatureInventor at Bloomington, MN

(2)

on 11/14/2003


(Steven M. Emerson)

Inventor's Name

Inventor
Signature