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1. Name of conveying parties: <p>Kochung Lee</p> <p>Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>	2. Name and address of receiving party: <p>Name: Lattice Semiconductor Corporation</p> <p>Street Address: 5555 N.E. Moore Ct.</p> <p>City: Hillsboro State: Oregon 97124-6421</p> <p>Country: U.S.A.</p> <p>Additional name(s) of receiving party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>
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3. Nature of Conveyance: <p><input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger</p> <p><input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name</p> <p><input type="checkbox"/> Other _____</p> <p>Execution Date: <u>May 28, 2004</u></p>	
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4. Application number(s) or patent number(s): <p>If this document is being filed together with a new application, the execution date of the application is: _____</p>	
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A. Patent Application No.(s) – 10/856,100 <p>Title: Current Mode Logic Buffer</p>	B. Patent No.(s) <p>Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No</p>
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5. Name and address of party to whom correspondence concerning document should be mailed: <p>Name: <u>Greg J. Michelson</u></p> <p>Internal Address: <u>MacPHERSON KWOK CHEN & HEID LLP</u></p> <p>Street Address: <u>1762 Technology Drive, Suite 226</u></p> <p>City <u>San Jose</u> State <u>CA</u> Zip <u>95110</u></p>	6. Total number of applications and patents involved: <u>1</u> 7. Total fee (37 CFR 3.41): \$40.00 <p><input checked="" type="checkbox"/> Authorized to be charged to Deposit Account 50-2257.</p> <p><input checked="" type="checkbox"/> Charge Deposit Account 50-2257 for any additional fees required for this conveyance and credit deposit account 50-2257 any amounts overpaid</p>
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8. Statement and signature. <p><i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i></p> <p><u>Gregory J. Michelson 44,940</u> <u>June 10, 2004</u> Name of Person Signing Signature Date</p> <p style="text-align: right;">Total number of pages including cover sheet: 3</p>	
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CH \$40.00 502257 10856100

Attorney Docket No.: M-15380 US

ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, I

Kochung Lee of San Jose, CA

hereby sell, assign and transfer to **Lattice Semiconductor Corporation**, a Delaware corporation, having a place of business at 5555 N.E. Moore Ct., Hillsboro, OR 97124-6421, its successors and assigns, the entire right, title and interest throughout the world in my invention in

CURRENT MODE LOGIC BUFFERU.S. Patent Application No. 10/856,100 Filing date May 28, 2004


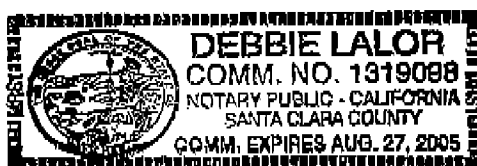
for which I have executed a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; I authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from me; I agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, their successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in all countries; and I request that the United States Patent and Trademark Office issue all patents granted for said invention to the above-named assignee, its successors and assigns.

I further hereby grant MacPherson Kwok Chen & Heid LLP or Lattice Semiconductor Corporation the power to insert on this Assignment any further information (including the U.S. Patent Application No. and filing date) that may be necessary or desirable in order to comply with all applicable foreign or domestic legal requirements, including the rules of the United States Patent and Trademark Office, for submitting and recording this document.

Executed this 28th day of May, 2004.
Kochung LeeState of CaliforniaCounty of Santa Clara) ss

On May 28th 2004, before me, Debbie Lalor personally appeared Kochung Lee personally known to me or proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is(are) subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

WITNESS my hand and official seal.


Signature of NOTARY

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MacPherson, Kwok Chen & Heid LLP

Date:	June 10, 2004		
To:	Automated Patent and Trademark Assignment System Director of the U.S. Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450	Fax Telephone #:	(703) 306-5995
From:	Greg Michelson	Date Sent:	
Re:	Serial No. 10/856,100 Filing Date: May 28, 2004 Applicant: Kochung Lee Title: Current Mode Logic Buffer	Time Sent:	
Our Ref.:	M-15380 US	Fax Operator:	Carolyn Merkley

This transmittal consists of 3 total page(s), including this cover sheet.

I hereby certify that the following documents are being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.


Carolyn Merkley

June 10, 2004
Date

Documents included with this cover sheet:

- (1) Recordation Cover Sheet (1 page); and
- (2) Assignment (1 page).

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