

11-26-2003

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Atty Dkt. No. 5201-27100

To the Honorable Commissioner of Patents and Trademarks. Please r

102609861

1. Name of conveying party(ies):

Subramanian Ramesh  
Ruggero Castagnetti  
Ramnath Venkatraman

Additional name(s) of conveying party(ies) attached?  Yes  No

2. Name and address of receiving party(ies):

Name: LSI Logic Corporation

Internal Address: \_\_\_\_\_

Street Address: 1621 Barber Lane

City: Milpitas State: CA ZIP 95035

Additional name(s) & address(es) attached?  Yes  No

3. Nature of Conveyance:

- Assignment
- Security Agreement
- Other \_\_\_\_\_
- Merger
- Change of Name

Execution Date: November 14, 2003

4. Application number(s) or patent number(s):

10715929

If this document is being filed together with a new application, the execution date of this application is: November 14, 2003

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached?  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Leo Peters

Internal Address: LSI Logic Corporation

Street Address: 1621 Barber Lane, MS D-106

City Milpitas State CA ZIP 95035

6. Total number of applications and patents involved: one (1)

7. Total fee (37 CFR 3.41): .....\$ 40.00

- Enclosed
- Authorized to be charged to deposit account

8. Deposit account number: 12-2252/03-1537  
(Attach a duplicate copy of this page if paying by deposit account)

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9. Statement and Signature.

*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*

Kevin L. Daffer

Name of Person Signing  
Reg. No. 34,146

Signature

November 18, 2003

Date

Total number of pages including cover sheet: 3

OMB No. 0651-011 (exp.4/94)

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Attorney Docket Number: 5201-27100  
03-1537

**ASSIGNMENT**

For Good And Valuable Consideration, the receipt and sufficiency of which is hereby acknowledged, we the undersigned, SUBRAMANIAM RAMESH, RUGGERO CASTAGNETTI, and RAMNATH VENKATRAMAN do hereby sell, assign and transfer to LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at 1621 Barber Lane, Milpitas, California 95035, ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements which are disclosed in the application for United States Letters Patent,

- ( X ) which has been executed by the undersigned concurrently herewith.
- ( ) which was filed on \_\_\_\_\_, 19\_\_ and assigned Serial No. \_\_\_\_\_

and is entitled "MEMORY CELL ARCHITECTURE FOR REDUCED ROUTING CONGESTION," and in and to said application and all divisional, continuing, substitute, renewal, reissue and all other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and

Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assignee, its successors, assigns, and representatives, all facts known to the undersigned relating to said improvements and the history thereof; and generally do everything possible which said Assignee, its successors, assigns or representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications to patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives; and

Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.


Inventor Signature:   
SUBRAMANIAM RAMESH

Date: Nov. 14<sup>th</sup>, 2003

Witness Signature: \_\_\_\_\_

Date: \_\_\_\_\_, 2003

Witness Printed Name: \_\_\_\_\_

Inventor Signature:   
RUGGERO CASTAGNETTI

Date: November 14, 2003

Witness Signature: \_\_\_\_\_

Date: \_\_\_\_\_, 2003

Witness Printed Name: \_\_\_\_\_

Attorney Docket Number: 5201-27100  
03-1537

Inventor Signature: *Ramnath V.*  
RAMNATH VENKATRAMAN

Date: November 14, 2003

Witness Signature: \_\_\_\_\_

Date: \_\_\_\_\_, 2003

Witness Printed Name: \_\_\_\_\_