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SHEET

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To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

- (a) Zamir Ahmad
(b) Jeffrey F. Wong

11-19-03

Additional name(s) of conveying party(ies) attached?

☐ Yes ☒ No

2. Name and address of receiving party(ies):

Name: Sun Microsystems, Inc.

Internal Address: _____

Street Address: 4150 Network CircleCity/State/Zip: Santa Clara, CA 950542241 U.S. PTO
10/716690Additional name(s) & address(es) attached? ☐ Yes ☒ No

3. Nature of Conveyance:

- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
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Execution Date(s): 11-17-2003

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: 11-17-2003

A. Patent Application No.:

10716690

B. Patent No.:

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Nicole Teitler Cave, Reg. No. 54,021Internal Address: Zagorin, O'Brien & Graham, L.L.P.Street Address: 7600B N. Capital of Texas Hwy, Suite 350City/State/Zip: Austin, TX 787316. Total number of applications and patents involved: 17. Total Fee (37 CFR 3.41): \$ 40.00

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Nicole Teitler Cave, Reg. No. 54,021
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Nicole Teitler Cave
Signature

11/19/03
Date

Total number of pages including cover sheet: 3

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PATENT

REEL: 014729 FRAME: 0818

ASSIGNMENT OF INVENTION

For good and valuable consideration, the receipt of which is hereby acknowledged,
I, an ASSIGNOR named below,

Zamir Ahmad of Newark, CA
Jeffrey F. Wong of Fremont, CA

hereby sell, assign and transfer to

ASSIGNEE: Sun Microsystems, Inc., a Delaware corporation, having a principal
place of business at 4150 Network Circle, Santa Clara, CA 95054,

its successors, assigns and legal representatives, my entire right, title and interest throughout
the world, including all rights to claim priority, in and to the invention(s) disclosed in:

- ☒ U.S. Patent Application executed on or about the date of this Assignment,
- ☐ U.S. Patent Application executed on _____, or
- ☐ U.S. Provisional Application
- ☐ U.S. Application No.: _____, filed on _____
- ☐ International Application No.: PCT/_____, filed on _____

ENTITLED: **REDUCED DELAY POWER FAIL-SAFE CIRCUIT**, and
naming as

INVENTOR(S): Zamir Ahmad and Jeffrey F. Wong

including without limitation, my entire right, title and interest in and to any and all United
States and foreign applications (including international, regional and foreign national
applications) for said invention(s), including divisions, continuations, continuations-in-part,
renewals, substitutes and extensions thereof, and in and to any and all patents of every
country or region that may be granted or have been granted for said invention(s), including
any reissues and reexaminations thereof.

To comply with 37 C.F.R. § 3.21 for recording this Assignment, I authorize and request that
the application number and filing date be inserted here, if not already shown above,
(_____/_____, filed _____) when they become known.

I authorize ASSIGNEE to apply for patents of foreign countries for said invention(s), and to
claim all rights of priority without further authorization from me.

I agree to execute any and all papers useful in connection with any and all United States and
foreign applications (including international, regional, and foreign national applications), and
generally to do everything possible to aid ASSIGNEE, its successors, assigns and nominees,
at their request and reasonable expense, in obtaining and enforcing patents for said
invention(s) in all countries.

I hereby covenant that no assignment, sale, license, agreement, or encumbrance has been or
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I authorize and request the Commissioner of Patents and Trademarks to issue any U.S.
Letters Patent that may be granted for said invention(s) to ASSIGNEE, its successors or
assigns.

ASSIGNMENT OF INVENTION

Executed on 11/17, 2003 by Zamir Ahmad,
Zamir Ahmad

Executed on 11/17, 2003 by Jeff Wong
Jeffrey F. Wong