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| INSERT | (1) Han-Choon LEE | | (2) | | | |
| NAME(S) OF | (3) | | (4) | | | |
| INVENTOR(S) | (5) | | | x box if continued on page 2 | | |
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| the receipt and suff and transfer(s) unto | | nowledged by the | e undersigned who at th | e request of, hereby sell(s), assign(s) | | |
| INSERT | DONGBU ELECTRONICS CO | O., LTD. | | | | |
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| ASSIGNEE(S) | | | | | | |
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| | ASSIGNEE") the entire right, title and interest tinuations, substitutes, and reissues thereof; a | | | c. 100, in the invention and all applications including | | |
| TITLE OF INVENTION | Method for Forming Barrier Metal of Semiconductor Device | | | | | |
| | for which the undersigned exe | ecuted an applic | ation for Letters Patents | of the United States of America: | | |
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| INVEN | NTORS Hay-Cho | m /22 | DATE SIGNED November 11,200 | WITNESSES | | |
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Assignment(Han_Choon LEE)

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