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HEET

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(1) Min-Chung CHOU  
  
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2. Name and address of receiving party(ies)  
  
Name: Elite Semiconductor Memory Technology, Inc.  
  
Street Address: No. 23, Industry E. Rd. IV Science-Based Industrial Park Hsinchu, Taiwan, R.O.C.  
  
Additional name(s) & address(es) attached?  Yes  No

10270 U.S.PTO  
101724657  
120103

3. Nature of conveyance:  
  
 Assignment  Merger  
 Security Agreement  Change of Name  
 Other:  
  
Execution Date: (1) November 13, 2003

4. Application number(s) or patent number(s): 101724657  
  
If this document is being filed together with a new application, the execution date of the application is:  
(1) November 13, 2003  
  
A. Patent application No.(s) B. Patent No.(s)  
  
Additional numbers attached?  Yes  No

5. Name and address of party to whom correspondence concerning document should be mailed:  
  
Daniel R. McClure  
Thomas, Kayden, Horstemeyer & Risley, L.L.P.  
100 Galleria Parkway, Suite 1750  
Atlanta, Georgia 30339-5948

6. Total number of applications/patents involved: [ 1 ]  
  
7. Total fee (37 CFR 3.41) \$ 40.00  
  
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*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*

Daniel R. McClure

Dec 1, 2003

Typed Name

Signature

Date

Docket #: 250507-1010

Total number of pages including cover sheet, attachments, and document: [ 3 ]

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PATENT  
REEL: 014759 FRAME: 0917

**ASSIGNMENT**

This assignment agreement is applicable to an invention entitled (Invention Title) \_\_\_\_\_  
Memory Device and Method for Burn-in Test

The PATENT RIGHTS referred to in this agreement are:

(check one)  a patent application for this invention, executed by the ASSIGNOR(S)  
 concurrently with this assignment.

U.S. patent application Serial No. \_\_\_\_\_, filed \_\_\_\_\_

a U.S. patent application based on PCT International Application  
 No. \_\_\_\_\_ filed on (date) \_\_\_\_\_ (U.S. patent application  
 Serial No. \_\_\_\_\_, if known).

U.S. patent No. \_\_\_\_\_, issued \_\_\_\_\_.

The PATENT RIGHTS also include all divisions reissues, continuations and extensions of  
 the patents and patent applications identified above.

The PATENT RIGHTS assigned under this agreement are:

(check one)  U.S. patent rights only.

Worldwide patent rights. In this case, the assignee shall have the right to  
 claim the benefit of the filing date of any U.S. or foreign patent application  
 for this invention.

The ASSIGNOR(S) referred to in this agreement is (or are) the inventor(s) whose signatures  
 appear on page 2 of this Assignment and any Supplemental Sheet(s).

The ASSIGNEE referred to in this agreement is:

(Name of Assignee) Elite Semiconductor Memory Technology Inc.

(Address) NO.23, INDUSTRY E. RD. IV. SCIENCE-BASED INDUSTRIAL PARK,  
HSINCHU, TAIWAN, R.O.C.

The ASSIGNEE is:

(check one)  An individual.

A Partnership.

A Corporation of TAIWAN, R.O.C. (specify state or country)

(other) \_\_\_\_\_

The ASSIGNOR(S), in consideration of \$10.00 paid by the ASSIGNEE, and other good and  
 valuable consideration, receipt of which is acknowledged, hereby assign(s) the following  
 rights to the ASSIGNEE, its successors and assigns:

the full and exclusive right to the invention;

the entire right, title and interest in and to the PATENT RIGHTS;

the right to sue and recover for any past infringement; and

the right to claim priority under 35 USC 119, 35 USC 120, or any other applicable  
 provisions, based on any earlier patent applications for this invention.

THIS IS PAGE 2 OF AN ASSIGNMENT FROM THE INVENTOR(S) TO

ASSIGNEE: Elite Semiconductor Memory Technology Inc.

INVENTION TITLE: Memory Device and Method for Burn-in Test

As to all U.S. patent applications assigned under this agreement, the ASSIGNOR(S) hereby authorize(s) and request(s) the Commissioner of Patents and Trademark to issue all Letters Patent to the ASSIGNEE as the assignee of the entire right, title and interest, for the sole use and enjoyment of said ASSIGNEE, its successors and assigns.

Further, the ASSIGNOR(S) agree(s) to communicate to said ASSIGNEE, or its representatives, any fact known to the ASSIGNOR(S) with respect to said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, substitute, renewal, reexamination and reissue applications, execute all necessary assignment papers to cause and all Letter Patent to be issued to said ASSIGNEE, make all rightful oaths and generally do everything necessary or desirable to aid said ASSIGNEE, its successors and assigns, to obtain and enforce proper protection for said invention.

The ASSIGNOR(S) authorize(s) the attorneys and agents who have the power of attorney in this application to check any appropriate boxes and to insert the Serial Number and filing date in this document after it has been executed.

Min-Chung CHOU

Min-Chung Chou

11/13 November 13, 2003

Name of sole or first inventor

Signature

Date

Name of second inventor, if any

Signature

Date

Name of third inventor, if any

Signature

Date

Name of fourth inventor, if any

Signature

Date

Name of fifth inventor, if any

Signature

Date

Name of sixth inventor, if any

Signature

Date

Name of seventh inventor, if any

Signature

Date

Name of eighth inventor, if any

Signature

Date