

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
ADVANCED MICRO DEVICES, Inc.	05/14/2004

RECEIVING PARTY DATA

Name:	AMD (U.S.) HOLDINGS, INC.
Street Address:	One AMD Place, P. O. Box 3453
Internal Address:	Mail Stop 68
City:	Sunnyvale
State/Country:	CALIFORNIA
Postal Code:	94088-3453

PROPERTY NUMBERS Total: 16

Property Type	Number
Patent Number:	5867430
Patent Number:	5852576
Patent Number:	5801579
Patent Number:	5912489
Patent Number:	6266275
Patent Number:	5999452
Patent Number:	5821800
Patent Number:	6143608
Patent Number:	6005803
Patent Number:	6033955
Patent Number:	5995415
Patent Number:	6633949
Patent Number:	6470414
Patent Number:	6275894

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Patent Number:	5995417
Patent Number:	5978267

**CORRESPONDENCE DATA**

Fax Number: (408)774-7483

*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*

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Correspondent Name: Daniel R. Collopy

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Address Line 2: Mail Stop 68

Address Line 4: Sunnyvale, CALIFORNIA 94088-3453

NAME OF SUBMITTER:

Daniel R. Collopy

Total Attachments: 4

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## ASSIGNMENT AGREEMENT

This Assignment Agreement by and between **ADVANCED MICRO DEVICES, INC.**, a corporation of Delaware having a place of business at One AMD Place, P. O. Box 3453, Sunnyvale, California 94088-3453, U.S.A. (hereinafter "ASSIGNOR") and **AMD (U.S.) HOLDINGS, INC.**, a corporation of Delaware, having a place of business at One AMD Place, P. O. Box 3453, Sunnyvale, California, 94088-3453, U.S.A. (hereinafter "ASSIGNEE") is effective on the 30<sup>TH</sup> day of June, 2003.

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR hereby assigns, transfers and conveys all of its right, title and interest in the United States and throughout the world in and to all invention disclosures, United States patent applications, United States patents, foreign patents, and foreign patent applications (including any Patent Cooperation Treaty (PCT) applications) identified on Schedule A attached hereto and made a part hereof, and the entire right, title and interest in the United States and throughout the world in and to the inventions to which said invention disclosures, patents and patent applications pertain, in and to all worldwide rights of priority, and in and to any corresponding United States and foreign patents, extensions and renewals of patents, results of reexaminations, substitutions, reissues, patent applications, divisionals, continuations, continuations-in-part, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates, together with all the rights and privileges granted and secured by said patents, extensions and renewals of patents, results of reexaminations, substitutions, reissues, patent applications, divisionals, continuations, continuations-in-part, utility models, petty patents, patents of importation, invention registrations and inventor's certificates, including without limitation all claims, demands, rights and causes of action that ASSIGNOR may have against others on account of any past, present and future infringement of any of said patents, patent applications, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates with the right in ASSIGNEE to sue for and obtain all relief to which ASSIGNOR may have been entitled by reason of any such infringement. ASSIGNOR further agrees that ASSIGNEE may apply for and receive Letters Patent, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates for said inventions in its own name.

Said entire right, title, and interest is to be held and enjoyed by ASSIGNEE for its own use and benefit and for the use and benefit of its successors and assigns, to the full end of the term for which said patents, registrations or certificates may be granted, as fully and entirely as the same would have been held and enjoyed by ASSIGNOR had this assignment and sale not been made.

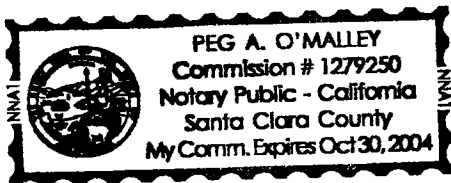
**ASSIGNOR**  
**ADVANCED MICRO DEVICES, INC.**

By: Thomas M. McCoy  
Name: Thomas M. McCoy  
Title: Sr. Vice President, Chief Administrative Officer

Date: 5-14-04

State of **CALIFORNIA** )  
County of **SANTA CLARA** )

On May 14, 2004, before me, Peg A. O'Malley, a Notary Public, personally appeared Thomas M. McCoy, personally known to me ~~or proved to me on the basis of satisfactory evidence~~ to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.



Witness my hand and official seal.

Peg A. O'Malley  
SIGNATURE OF NOTARY

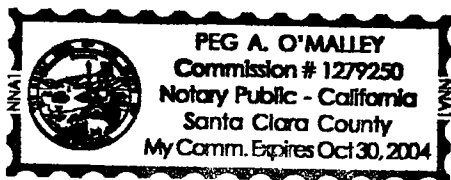
**ASSIGNEE**  
**AMD (U.S.) HOLDINGS, INC.**

By: Thomas M. McCoy  
Name: Thomas M. McCoy  
Title: Vice President and Secretary

Date: 5-14-04

State of **CALIFORNIA** )  
County of **SANTA CLARA** )

On May 14, 2004, before me, Peg A. O'Malley, a Notary Public, personally appeared Thomas M. McCoy, personally known to me ~~or proved to me on the basis of satisfactory evidence~~ to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.



Witness my hand and official seal.

Peg A. O'Malley  
SIGNATURE OF NOTARY

## Attachment A

Docket No.	Rel. Type	Country	Status	Firm	App. No.	App. Date	Patent No.	Grant Date	Title
C196596		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY	08/772,131	12/20/1996	5,867,430	2/2/1999	BANK ARCHITECTURE FOR A NON-VOLATILE MEMORY ENABLING SIMULTANEOUS READING AND WRITING
C144496	P	USA	Granted	FLIESLER DUBB MEYER & LOVEJOY	08/944,904	10/6/1997	5,852,576	12/22/1998	HIGH VOLTAGE NMOS PASS GATE FOR INTEGRATED CIRCUIT WITH HIGH VOLTAGE GENERATOR AND FLASH NON-VOLATILE MEMORY
C144496		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY	08/808,237	2/28/1997	5,801,579	9/1/1998	HIGH VOLTAGE NMOS PASS GATE FOR INTEGRATED CIRCUIT WITH HIGH VOLTAGE GENERATOR AND FLASH NON-VOLATILE MEMORY
C141496		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY	08/940,674	9/30/1997	5,912,489	6/15/1999	A DUAL SOURCE SIDE POLYSILICON SELECT GATE STRUCTURE AND PROGRAMMING METHOD UTILIZING SINGLE TUNNEL OXIDE
C141496	D	USA	Granted	FLIESLER DUBB MEYER & LOVEJOY	09/410,512	9/30/1999	6,266,275	7/24/2001	A DUAL SOURCE SIDE POLYSILICON SELECT GATE STRUCTURE AND PROGRAMMING METHOD UTILIZING SINGLE TUNNEL OXIDE
C141496	D	USA	Granted	FLIESLER DUBB MEYER & LOVEJOY	09/063,688	4/21/1998	5,999,452	12/7/1999	A DUAL SOURCE SIDE POLYSILICON SELECT GATE STRUCTURE AND PROGRAMMING METHOD UTILIZING SINGLE TUNNEL OXIDE
0061296		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY	08/799,074	2/11/1997	5,821,800	10/13/1998	HIGH VOLTAGE CMOS LEVEL SHIFTER
00335		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY	09/283,308	3/31/1999	6,143,608	11/7/2000	BARRIER LAYER DECREASES NITROGEN CONTAMINATION OF PERIPHERAL GATE REGIONS DURING TUNNEL OXIDE NITRIDATION
00117		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY	09/159,342	9/23/1998	6,005,803	12/21/1999	MEMORY ADDRESS DECODING CIRCUIT FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE WITH A FLEXIBLE BANK

Pocket No.	Rel. Type	Country	Status	Firm	App. No.	App. Date	Patent No.	Grant Date	Title
D1169		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY,	09/159,023	9/23/1998	6,033,955	3/7/2000	METHOD OF MAKING FLEXIBLY PARTITIONED METAL LINE SEGMENTS FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE
D1168		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY,	09/159,142	9/23/1998	5,995,415	11/30/1999	SIMULTANEOUS OPERATION FLASH MEMORY DEVICE WITH A FLEXIBLE DEVICE WITH A FLEXIBLE BANK PARTITION ARCHITECTURE
D139	D	USA	Granted	FLIESLER DUBB MEYER & LOVEJOY,	09/892,431	6/26/2001	6,633,949	10/14/2003	BANK SELECTOR CIRCUIT FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE WITH A FLEXIBLE BANK PARTITION ARCHITECTURE
D139	D	USA	Granted	FLIESLER DUBB MEYER & LOVEJOY,	09/893,247	6/26/2001	6,470,414	10/22/2002	BANK SELECTOR CIRCUIT FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE WITH A FLEXIBLE BANK PARTITION ARCHITECTURE
D139		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY,	09/159,489	9/23/1998	6,275,894	8/14/2001	BANK SELECTOR CIRCUIT FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE WITH A FLEXIBLE BANK PARTITION ARCHITECTURE
D138		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY,	09/175,646	10/20/1998	5,995,417	11/30/1999	SCHEME FOR PAGE ERASE AND ERASE VERIFY IN A NON-VOLATILE MEMORY ARRAY
D2016		USA	Granted	FLIESLER DUBB MEYER & LOVEJOY,	09/175,647	10/20/1998	5,978,267	11/2/1999	BIT LINE BIASING METHOD TO ELIMINATE PROGRAM DISTURBANCE IN A NON-VOLATILE MEMORY DEVICE AND MEMORY DEVICE

PATENT