

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
EFFECTIVE DATE:	06/16/2004
CONVEYING PARTY DATA	
Name	Execution Date
EIC Enterprises, Limited	06/16/2004
RECEIVING PARTY DATA	
Name:	WJ Communications, Inc.
Street Address:	401 River Oaks Parkway
City:	San Jose
State/Country:	CALIFORNIA
Postal Code:	95134
PROPERTY NUMBERS Total: 4	
Property Type	Number
Application Number:	10267215
Application Number:	10820343
Application Number:	10267342
Patent Number:	6521972
CORRESPONDENCE DATA	
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<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
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NAME OF SUBMITTER:	Francine L. Hewes

OP \$160.00 10267215

Total Attachments: 4

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PATENT

REEL: 014770 FRAME: 0295

ASSIGNMENT

WHEREAS, EIC ENTERPRISES, LIMITED, a Bermuda corporation having an address of Cedar House, 41 Cedar Avenue, Hamilton, HM 12, Bermuda ("Assignor") is the owner of the entire right, title, and interest in and to the following patent applications with the below listed serial numbers and filing dates (the "Patent Applications"), and patents which were issued by the United States Patent and Trademark Office under the below listed patent numbers and dates (the "Patents"):

<u>Patent Application/Patent:</u>	<u>Serial No./ Patent No.:</u>	<u>Filing Date/ Issue Date:</u>	<u>Inventor(s)</u>
RF power transistor having low parasitic impedance input feed structure	6,521,972	02/18/2003	Zhou; Wei-Shu; Hsiao; Shuo-Yuan; Wang; Nanlei Larry
Heterojunction bipolar transistor having non-uniformly doped collector for improved safe-operating area	10/267,215	10/08/2002	Lee, Chien Ping; Chau, Hin Fai; Wang, Nanlei Larry; Dunnrowicz, Clarence John; Chen, Yan; Lin, Barry Jia-Fu
Heterojunction bipolar transistor having non-uniformly doped collector for improved safe-operating area	10/820,343	04/07/2004	Lee, Chien Ping; Chau, Hin Fai; Wang, Nanlei Larry; Dunnrowicz, Clarence John; Chen, Yan; Lin, Barry Jia-Fu
Heterojunction bipolar transistor having wide bandgap material in collector	10/267,342	10/08/2002	Chau, Hin Fai; Dunnrowicz, Clarence John; Chen, Yan; Lee, Chien Ping

WHEREAS, Assignor has developed, maintained, utilized, and owned the proprietary processes (the "Processes"), which are further set forth in Exhibit A, including the process steps, documentation, and operational know-how connected therewith, in association with its business of designing and manufacturing power amplifiers for wireless infrastructure and handset applications (the "Business");

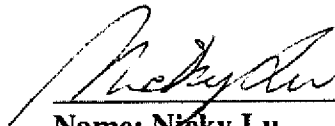
WHEREAS, Assignor has agreed pursuant to that certain Asset Purchase Agreement of even date herewith (the "Purchase Agreement"), which Purchase Agreement is incorporated herein by reference, to assign its entire right, title and interest in and to the Patent Applications, Patent, and Processes as developed, maintained, utilized, and owned by Assignor, to WJ COMMUNICATIONS, INC., a Delaware corporation having an address of 401 River Oaks Parkway, San Jose, California 95134 ("Assignee") and Assignee has agreed pursuant to the Purchase Agreement to acquire the Patent Applications, Patent, and Processes, as developed, maintained, utilized, and owned by Assignor; and

WHEREAS, Assignor represents and warrants to Assignee, that the Patent Applications, Patent, and Processes, as developed, maintained, utilized, and owned by Assignor, are not subject to any lien, security interest, license of third parties, or other claim, and that with respect thereto: (i) Assignor has the right to assign such rights to Assignee; (ii) Assignor has executed no agreement in conflict with this Assignment; and (iii) Assignor has not granted to any other person, firm or corporation any right, license, or privilege with regard to the Patent Applications, Patent, and Processes, as developed, maintained, utilized, and owned by Assignor.

NOW, THEREFORE, in consideration of the mutual premises and covenants set forth herein, and other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor hereby assigns to Assignee all of its right, title and interest in and to the Patent Applications, Patent, and Processes, as developed, maintained, utilized, and owned by Assignor, including the right to sue and collect for past infringement of the Patent and any patent that issues from the Patent Applications, the same to be held and enjoyed by Assignee for its own use and behoof, and for its legal representatives and assigns, to the full end of the term for which the Patent Applications and Patent are granted, as fully and entirely as the same would have been held by Assignor had this assignment and sale not been made, and it authorizes the United States Patent and Trademark Office to record the Patent Applications and Patents as the property of Assignee.

Signed at Fremont, California, this 16 day of June, 2004.

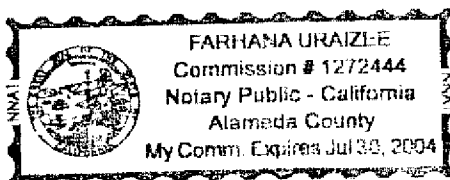
EIC ENTERPRISES, LIMITED



Name: Nicky Lu
Title: President and CEO

STATE OF California
COUNTY OF Alameda

The foregoing instrument was acknowledged before me this 16th day of June 2004, by Nicky Lu on behalf of EiC Enterprises, Limited. He is personally known to me or has produced CA. drivers license as identification.



Farhana Uraizee
NOTARY PUBLIC
Name: FARHANA URAIZEE
My Commission Expires: July 30, 2004

(notary seal)

EXHIBIT A
PROCESSES

EiC Doc. #	Doc. Title	Description
PD-W00073-000	HBT Process "A" Run Sheet	5V AlGaAs HBT AND MMIC process without backside via hole. Process A is a subset of Process C.
PD-W00074-000	HBT Process "B" Run Sheet	5V AlGaAs HBT AND MMIC process without backside via hole but with an additional "TG" mask layer.
PD-W00012-000	HBT Process "C" Run Sheet	5V AlGaAs HBT AND MMIC process with backside via hole.
PD-W00244-000	HBT Process "E" Run Sheet	5V InGaP HBT process (no MMIC part). Process E covers only the InGaP transistor part; it is used in combination with the passive element and interconnect portion (i.e. part II) from either process A or C to make a product.
PD-W00648-000	HBT Process "F" Run Sheet	12V InGaP HBT process AND MMIC process with backside via hole.
PD-W00645-000	HBT Process "G" Run Sheet	28V InGaP HBT process AND MMIC process with backside via hole.