


I hereby certify that this transmittal of the below described document is being facsimile transmitted to the Patent and Trademark Office, on the below date of deposit.

Fax Number:	(703) 306-5995
Date of Deposit:	07/2/04
Name of Person Making the Deposit:	Kerry Erin Kelly
Signature of the Person Making the Deposit:	

Form PTO-1595 (Rev. 03/01) OMB No. 0651-0027 (exp. 5/31/2002)	Request for CORRECTION of RECORDATION FORM COVER SHEET PATENTS ONLY	U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office
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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

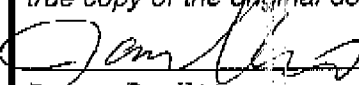
1. Name of conveying party(ies): Advanced Micro Devices, Inc. One AMD Place P.O. Box 3453 Sunnyvale, CA 94088-3453 U.S.A.	2. Name and address of receiving party(ies): AMD (U.S.) Holdings, Inc. One AMD Place P.O. Box 3453 Sunnyvale, CA 94088-3453 U.S.A.
3. Nature of conveyance: * Assignment, Correction of Recordation Document ID No: 700095780 (Notice of Non-Recordation) Previously recorded on Reel/Frame 014782/0080 6,125,058 incorrectly recorded as 6,215,058 6,111,787 title contains typo of "transistion" > transition 6,622,003 mistakenly entered by applicant as 6,662,003 6,212,108 omitted from recordation. Execution Date: 05/15/04, 5/15/04	Additional name(s) & address(es) attached? <input type="checkbox"/> yes <input type="checkbox"/> no

4. Application number(s) or patent number(s):
If this document is being filed together with a new application, the execution date is:
A. Patent Application No.(s): B. Patent No.(s):
SEE ATTACHED List of Four (4) corrections requested and copy of Notice of Non-Recordation of Assignment transmitted via facsimile 7/02/04.
Additional numbers attached? * yes no

5. Name and address of party to whom correspondence concerning the document should be mailed: Wagner, Murabito & Hao LLP 2 N. Market Street Third Floor San Jose, CA 95113 ph. (408) 938-9060 fax (408) 938-9069	6. Total number of applications involved: 4 7. Total fee (37 CFR 3.41) \$160.00 <input type="checkbox"/> enclosed * Authorized to be charged to Deposit Account 8. Deposit Account number: 23-0085 (Attach duplicate copy of this page if paying by deposit account)
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DO NOT USE THIS SPACE

9. Statement and Signature.
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.


James P. Hao
Reg. No.: 36,398

July 2, 2004
Date

Total number of pages including cover sheet, attachments, and documents: 34 pages

CH \$160.00 230085 6125058

Requested Corrections of Recordation of Assignment

	<u>App. No.</u>	<u>App. Date</u>	<u>Status</u>	<u>Patent No.</u>	<u>Grant Date</u>	<u>Inventors</u>	<u>Title</u>	<u>Docket No.</u>
27	09/421,982	10/19/99	Granted	6,125,058	9/26/00	KUO, TIAO-HUA 22492 (CA) LEONG, NANCY S. 22609 AKAOGI, TAKAO N/E CHEN, JOHNNY CHUNG-LEE 75232	SYSTEM FOR OPTIMIZING THE EQUALIZATION PULSE OF A READ SENSEAMPLIFIER FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01020
30	09/421,776	10/19/99	Granted	6,111,787	8/29/00	KURIHARA, KAZUHIRO (FUJITSU) CHEN, TIEN-MIN AKAOGI, TAKAO N/E	ADDRESS TRANSITION DETECT TIMING ARCHITECTURE FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01025
40	09/724,675	11/28/00	Granted	6,222,003	9/16/03	YANO, MASARU N/E HOLLMER, SHANE C. 22154 (CA) CHUNG, MICHAEL S.C. 23278	MULTI-SET BLOCK ERASE	AF01041
59	09/490,340	1/24/00	Granted	6,212,108	4/3/01	AKAOGI, TAKAO N/E CLEVELAND, LEE 22191	DISTRIBUTED VOLTAGE CHARGE CIRCUITS TO REDUCE SENSING TIME IN A MEMORY DEVICE	AF01084

06/30/2004
700095780

I hereby certify that this transmittal of the below described document is being facsimile transmitted to the Patent and Trademark Office, on the below date of deposit.

Fax Number:	703 308-8895		
Date of Deposit:	06/30/04	Name of Person Making the Deposit:	Kerry Erin Kelly
		Signature of the Person Making the Deposit:	

Form PTO-1595 (Rev. 03/01) OMB No. 0661-0027 (exp. 5/31/2002)	Request for CORRECTION of RECORDATION FORM COVER SHEET PATENTS ONLY	U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office
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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

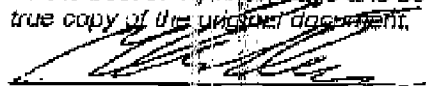
1. Name of conveying party(ies): Advanced Micro Devices, Inc. One AMD Place P.O. Box 3453 Sunnyvale, CA 94088-3453 U.S.A.	2. Name and address of receiving party(ies): AMD (U.S.) Holdings, Inc. One AMD Place P.O. Box 3453 Sunnyvale, CA 94088-3453 U.S.A.
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3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Other: Execution Date: <u>05/15/04, 5/15/04</u>	Additional name(s) & address(es) attached? <input type="checkbox"/> yes <input type="checkbox"/> no
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4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date is: A. Patent Application No.(s): B. Patent No.(s):	SEE ATTACHED List of Four (4) corrections requested and copy of Notice of Recordation of Assignment transmitted via facsimile 6/28/04.
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5. Name and address of party to whom correspondence concerning the document should be mailed: Wagner, Murabito & Hao LLP 2 N. Market Street Third Floor San Jose, CA 95113 ph. (408) 938-9060 fax (408) 938-9069	6. Total number of applications involved: 4 7. Total fee (37 CFR 3.41) \$160.00 <input type="checkbox"/> enclosed <input checked="" type="checkbox"/> Authorized to be charged to Deposit Account 8. Deposit Account number: <u>23-0005</u> (Attach duplicate copy of this page if paying by deposit account)
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9. Statement and Signature: <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i>  Glenn D. Barnes Reg. No.: 42,293	June 30, 2004 Date Total number of pages including cover sheet, attachments, and documents: 24 pages
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Requested Corrections of Recordation of Assignment

App. No.	App. Date	Status	Patent No.	Grant Date	Inventors	Title	Docket No.	
27	09/421,982	10/19/99	Granted	6,125,058	9/26/03	KUO, TIAO-HUA 22492 (CA) LEONG, NANCY S. 22809 AKAOGI, TAKAO N/E CHEN, JOHNNY CHUNG-LEE 75232	SYSTEM FOR OPTIMIZING THE EQUALIZATION PULSE OF A READ SENSEAMPLIFIER FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01020
30	09/421,776	10/19/99	Granted	6,111,787	8/29/00	KURIHARA, KAZUHIRO (FUJITSU) CHEN, TIEN-MIN AKAOGI, TAKAO N/E	ADDRESS TRANSITION DETECT TIMING ARCHITECTURE FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01025
40	09/724,075	11/28/00	Granted	6,222,003	9/16/03	YANO, MASARU N/E HOLLMER, SHANE C. 22154 (CA) CHUNG, MICHAEL S.C. 28278	MULTI-SET BLOCK ERASE	AF01041
59	09/490,340	1/24/05	Granted	6,212,108	4/3/01	AKAOGI, TAKAO N/E CLEVELAND, LEE 22192	DISTRIBUTED VOLTAGE CHARGE CIRCUITS TO REDUCE SENSING TIME IN A MEMORY DEVICE	AF01084

06/24/2004
700094269

I hereby certify that the transmission of the below described document is being furnished herewith to the Patent and Trademark Office, on the below date of deposit:	
File Number: 23-0085	
Date of Deposit: 06/24/04	Name of Person Making the Deposit: Kerry Ellis Kelly
	Signature of the Person Making the Deposit:
Form PTO-155 (Rev. 02/01) CASE No. 2551-0037 Date Filed 06/24/04	<p align="center">RESUBMISSION of RECORDATION FORM COVER SHEET PATENTS ONLY</p> <p align="right">U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office</p>
To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.	
1. Name of conveying party(ies): Advanced Radio Devices, Inc. One AMD Place P.O. Box 3453 Sunnyvale, CA 94088-3453 U.S.A.	2. Name and address of receiving party(ies): AMD (U.S.) Holdings, Inc. One AMD Place P.O. Box 3453 Sunnyvale, CA 94088-3453 U.S.A.
3. Nature of conveyance: * Assignment <input type="checkbox"/> Other: Execution Date: 05/15/04, 5/15/04	Additional name(s) & address(es) attached? <input type="checkbox"/> yes <input checked="" type="checkbox"/> no
4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date is: A. Patent Application NO.(S): B. Patent NO.(S): Additional numbers attached? <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	
5. Name and address of party to whom correspondence concerning the document should be mailed: Wagner, Murabito & Han LLP 2 N. Market Street Third Floor San Jose, CA 95113 ph. (408) 280-4600 fax (408) 280-6069	6. Total number of applications involved: 02 7. Total fee (37 CFR 3.41) \$2,480.00 <input type="checkbox"/> enclosed <input checked="" type="checkbox"/> Authorized to be charged to Deposit Account 8. Deposit Account number: 23-0085 (Attach duplicate copy of this page if paying by deposit account)
DO NOT USE THIS SPACE	
9. Statement and Signature: To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.	
June 24, 2004 Date	
Glenn D. Barnes Res. No. 1, 32, 233 Total number of pages including cover sheet, attachments, and documents: 22 pages	

06/24/04 15:54 4089389069

ASSIGNMENT AGREEMENT

This Assignment Agreement by and between **ADVANCED MICRO DEVICES, INC.**, a corporation of Delaware having a place of business at One AMD Place, P. O. Box 3453, Sunnyvale, California 94088-3453, U.S.A. (hereinafter "ASSIGNOR") and **AMD (U.S.) HOLDINGS, INC.**, a corporation of Delaware, having a place of business at One AMD Place, P. O. Box 3453, Sunnyvale, California, 94088-3453, U.S.A. (hereinafter "ASSIGNEE") is effective on the 30TH day of June, 2003.

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR hereby assigns, transfers and conveys all of its right, title and interest in the United States and throughout the world in and to all invention disclosures, United States patent applications, United States patents, foreign patents, and foreign patent applications (including any Patent Cooperation Treaty (PCT) applications) identified on Schedule A attached hereto and made a part hereof, and the entire right, title and interest in the United States and throughout the world in and to the inventions to which said invention disclosures, patents and patent applications pertain, in and to all worldwide rights of priority, and in and to any corresponding United States and foreign patents, extensions and renewals of patents, results of reexaminations, substitutions, reissues, patent applications, divisionals, continuations, continuations-in-part, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates, together with all the rights and privileges granted and secured by said patents, extensions and renewals of patents, results of reexaminations, substitutions, reissues, patent applications, divisionals, continuations, continuations-in-part, utility models, petty patents, patents of importation, invention registrations and inventor's certificates, including without limitation all claims, demands, rights and causes of action that ASSIGNOR may have against others on account of any past, present and future infringement of any of said patents, patent applications, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates with the right in ASSIGNEE to sue for and obtain all relief to which ASSIGNOR may have been entitled by reason of any such infringement. ASSIGNOR further agrees that ASSIGNEE may apply for and receive Letters Patent, utility models, petty patents, patents of importation, invention registrations, and inventor's certificates for said inventions in its own name.

Said entire right, title, and interest is to be held and enjoyed by ASSIGNEE for its own use and benefit and for the use and benefit of its successors and assigns, to the full end of the term for which said patents, registrations or certificates may be granted, as fully and entirely as the same would have been held and enjoyed by ASSIGNOR had this assignment and sale not been made.

**ASSIGNOR
ADVANCED MICRO DEVICES, INC.**

By Thomas M. McCoy
Name: Thomas M. McCoy
Title: Sr. Vice President, Chief Administrative Officer

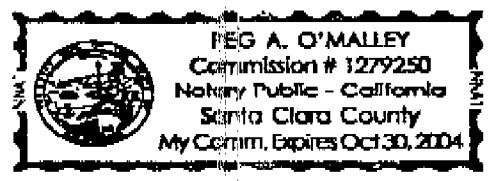
Date May 15, 2004

State of CALIFORNIA)
County of SANTA CLARA)

On May 15, 2004, before me, Peg A. O'Malley, a Notary Public, personally appeared Thomas M. McCoy, personally known to me or proved to me on the basis of ~~satisfactory evidence~~ to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

Witness my hand and official seal.

Peg A. O'Malley
SIGNATURE OF NOTARY



**ASSIGNEE
AMD (U.S.) HOLDINGS, INC.**

By Thomas M. McCoy
Name: Thomas M. McCoy
Title: Vice President and Secretary

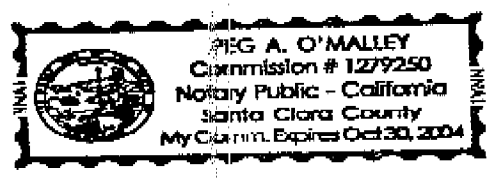
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Witness my hand and official seal.

Peg A. O'Malley
SIGNATURE OF NOTARY



ASSIGNMENT AGREEMENT

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Said entire right, title, and interest is to be held and enjoyed by ASSIGNEE for its own use and benefit and for the use and benefit of its successors and assigns, to the full end of the term for which said patents, registrations or certificates may be granted, as fully and entirely as the same would have been held and enjoyed by ASSIGNOR had this assignment and sale not been made.

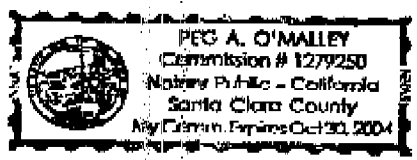
ASSIGNOR
ADVANCED MICRO DEVICES, INC.

By: Thomas M. McCoy
Name: Thomas M. McCoy
Title: Sr. Vice President, Chief Administrative Officer

Date: May 15, 2004

State of CALIFORNIA)
County of SANTA CLARA)

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Witness my hand and official seal.
Peg A. O'Malley
SIGNATURE OF NOTARY

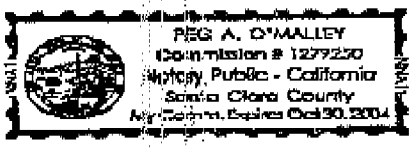
ASSIGNEE
AMD (U.S.) HOLDINGS, INC.

By: Thomas M. McCoy
Name: Thomas M. McCoy
Title: Vice President and Secretary

Date: May 15, 2004

State of CALIFORNIA)
County of SANTA CLARA)

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Witness my hand and official seal.
Peg A. O'Malley
SIGNATURE OF NOTARY

Attachment A

App. No.	App. Date	Status	Patent No.	Grant Date	Inventors	Title	Despat. No.	Pub. No.	Pub. Type	Country	Location	Chg.	Att.
1	09/04/03	Filed			KURIHARA, KAZUHIRO (FUJITSU) CHEN, PAUL-ING 15886	NO BASED COLUMN REDUNDANCY FOR VIRTUAL GROUND WITH 2-3D CELL FLASH MEMORY	AF01045	01-40694		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
2	09/24/03	Filed			NGUYEN, KENDRA LIN, JIN-LIEN AKAGI, TAKAO NE AL-GHAMBA, ALI K. CLEVELAND, LEE 22191 TEH, BOON-TANG (PNC)	INITIAL LATENCY ELIMINATION FOR BURST MODE READ IN A BURST MODE MEMORY DEVICE	AF01079	01-40321		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
3	10/31/03	Filed			KURIHARA, KAZUHIRO (FUJITSU) LE, BINH QUANG 23328 (CA) CHEN, PAUL-ING 15888 HAMILTON, DARLENE G. 21331 (CA) HELA, EDWARD 22517	NON-VOLATILE MEMORY READ CIRCUIT WITH END OF LIFE SIMULATION	AF01134			USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
4	10/20/03	Filed			YANG, TIEN-CHUN SHIH, MING-HUEI KURIHARA, KAZUHIRO (FUJITSU) CHEN, PAUL-ING 15885	SYSTEM AND METHOD FOR REDUCING IN A FLASH MEMORY DEVICE	AF01185	02-41622		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
5	10/24/03	Filed			YANG, TIEN-CHUN YAMADA, SHIGEKAZU (FUJITSU) SHIH, MING-HUEI CHEN, PAUL-ING 15886	GROUND STRUCTURE FOR PAGE READ AND PAGE WRITE FOR FLASH MEMORY	AF01198	02-41024		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
6	10/31/03	Filed			RINNION, ED YANG, TIEN-CHUN LE, BINH QUANG 23328 (CA) YAMADA, SHIGEKAZU (FUJITSU) HAMILTON, DARLENE G. 21331 (CA) SHIH, MING-HUEI	METHOD TO OBTAIN TEMPERATURE INDEPENDENT PROGRAM THRESHOLD VOLTAGE DISTRIBUTION USING TEMPERATURE DEPENDENT VOLTAGE REFERENCE	AF01182			USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP

Attachment A

App. No.	App. Date	Status	Patent No.	Grant Date	Inventor(s)	Title	Doccl. No.	Publ. (App. No.)	Publ. Date	Country	Applicant	Div.	Class
7	10/30/74	5/13/04			OCAWA, HIROYUKI (FUJITSU) SUN, YU 10603 HUI, ANGELAT. 21184 (CA)	MEMORY DEVICE HAVING REVERSE LDD METHOD	AF01174			USA	FASL LLC	NVT	WAGNER, MURABITO & HAO, LLP
8	10/06/112	2/27/03	6,730,108	6/19/04	MAFAAG, HIGASHITANI (FUJ) RANDOLPH, MARK 53221	NRAM CELL WITH N-LESS CHANNEL	AF01057	03-41135		USA	FASL LLC	NVT	WAGNER, MURABITO & HAO, LLP
9	10/04/702	8/15/03	6,714,800	3/17/05	TAGHARENI, SANTOSH K KURIHARA, KAZUHIRO KAZUHIRO (FUJITSU) SHEN, MING-HUEL CHON, PAULUNG 15808	METHOD AND SYSTEM TO MINIMIZE PAGE PROGRAMMING TIME FOR FLASH MEMORY DEVICES	AF01164	04-41622		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
10	08/422,100	10/19/95	6,260,833	7/10/01	CHEN, TIEN-MIN KURIHARA, KAZUHIRO (FUJITSU) AKAOGI, TAKAO N/E	SENSE AMPLIFIER ARCHITECTURE FOR SLIDING BANKS FOR A SIMULTANEOUS OPERATION FLASH MEMORY	AF01001	01-40669		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
11	08/421,985	10/19/95	6,359,808	8/12/02	CHEN, TIEN-MIN AKAOGI, TAKAO N/E KURIHARA, KAZUHIRO (FUJITSU)	LOW VOLTAGE READ CIRCUITRY FOR 2V/3V AND DIFFERENT BANK COMBINATIONS WITHOUT METAL OPTIONS FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01002	01-40323		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
12	08/421,775	10/19/95	6,327,131	12/4/01	KURIHARA, KAZUHIRO (FUJITSU) CHEN, TIEN-MIN AKAOGI, TAKAO N/E	REFERENCE CELL BITLINE PATH ARCHITECTURE FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01003	01-40599		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
13	08/421,774	10/19/95	6,163,473	12/18/00	KASA, YASUSHI (FUJ) LAI, FAN W (CA)	COMMON FLASH INTERFACE IMPLEMENTATION FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01005	02-40906		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
14	08/650,491	8/28/00	6,273,412	8/14/01	KASA, YASUSHI (FUJ) LAI, FAN W (CA)	COMMON FLASH INTERFACE IMPLEMENTATION FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01006	01-40508		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP

Attachment A

App. No.	App. Date	Status	Patent No.	Grant Date	Inventors	Title	Doc/Pat. No.	Pub. No.	E. Type	Country	Location	Rev.	Item
15	06/22/2003	Granted	6,387,318	5/28/02	KASA, YASUSHI (FJ) WANG, GUOWEI (EDDIE)	REDUNDANT DUAL BANK ARCHITECTURE FOR A SIMULTANEOUS OPERATION FLASH MEMORY	AP01005	00-42815		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
16	09/21/2003	Granted	6,725,035	07/26/00	KASA, YASUSHI (FJ) WANG, GUOWEI (EDDIE)	SECTOR WRITE PROJECT CAMS FOR A SIMULTANEOUS OPERATION FLASH MEMORY	AP01007			USA	FASL LLC		WAGNER, MURABITO & HAO, LLP
17	09/21/2003	Granted	6,560,026	04/15/03	KUO, TIAO-HUA 22482 (CA) AKAOGI, TAKAO N/A LA, FAN W (CA)	ARRAY V1 BE/LIC IMPLEMENTATION FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AP01008	00-40806		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
18	09/08/2003	Granted	6,276,421	07/14/01	CHEN, TIEN-MIN KURIHARA, KAZUHIRO (FUJITSU)	CHP ENABLE INPUT BUFFER	AP01009	00-40721		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
19	09/08/2003	Granted	6,483,618	10/08/02	LEONG, NANCY S. 22800 CHEN, JOHNNY CHUNG-LEE 75200 KUO, TIAO-HUA 22482 (CA) KURIHARA, KAZUHIRO (FUJITSU)	VARIABLE SECTOR SIZE FOR A HIGH DENSITY FLASH MEMORY DEVICE	AP01010	01-40557		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
20	09/08/2003	Granted	6,285,627	07/01	KURIHARA, KAZUHIRO (FUJITSU) SHIH, THOMAS T	ADDRESS TRANSITION DETECTOR (ATD) ARCHITECTURE FOR HIGH DENSITY FLASH MEMORY DEVICE	AP01011	01-40666		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
21	09/08/2003	Granted	6,353,366	03/02	AKAOGI, TAKAO N/A KURIHARA, KAZUHIRO (FUJITSU) SHIH, THOMAS T	METHOD FOR TRACKING SENSING SPEED BY AN EQUALIZATION PULSE FOR A HIGH DENSITY FLASH MEMORY DEVICE	AP01012	01-40600		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
22	09/12/2003	Granted	6,297,363	10/20/02	CHEN, JOHNNY CHUNG-LEE 75232 KASA, YASUSHI (FJ) PHAM, TRUNG S	ACCELERATION VOLTAGE IMPLEMENTATION FOR HIGH DENSITY FLASH MEMORY DEVICE	AP01014	01-40322		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP

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App. No.	App. Date	Status	Patent No.	Grant Date	Inventors	Title	Docket No.	Pub. No.	P. Year	Country	Location	IPC	Firm
23	09/24/98	Granted	6,327,194	12/4/01	KURIHARA, KAZUHIRO (FALITSU) CHEN, TIEN-HSI	PRECISE REFERENCE WORDLINE LORING COMPENSATION FOR A HIGH DENSITY FLASH MEMORY DEVICE	AF01015	01-41220		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
24	09/20/99	Granted	6,882,202	12/3/03	KASA, YASUHI (FJ) CHEN, JOHNNY CHUNG-LEE 75232 WANG, GUOWEI (EDDIE) KUO, TIAO-HUI 22492 (CA)	OTP SECTOR DOUBLE PROTECTION FOR A SIMULTANEOUS OPERATION FLASH MEMORY	AF01018	00-40532		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
25	09/21/98	Granted	6,671,207	6/27/03	KUO, TIAO-HUI 22492 (CA) LEONG, HANG S. 22809 AKAGI, TAKAO NIIE KASA, YASUHI (FJ)	MULTIPLE PURPOSE DRIS FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01018	99-40507		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
26	09/21/98	Granted	6,351,299	12/18/01	KUO, TIAO-HUI 22492 (CA) KASA, YASUHI (FJ) CHEN, JOHNNY CHUNG-LEE 75232	WRITE PROTECT INPUT IMPLEMENTATION FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01019	99-40508		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
27	09/21/98	Granted	6,125,058	6/28/03	KUO, TIAO-HUI 22492 (CA) LEONG, HANG S. 22809 AKAGI, TAKAO NIIE CHEN, JOHNNY CHUNG-LEE 75232	SYSTEM FOR OPTIMIZING THE EQUALIZATION PULSE OF A READ SENSE AMPLIFIER FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01020	99-40509		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
28	09/21/98	Granted	6,201,753	3/13/01	AKAGI, TAKAO NIIE KURIHARA, KAZUHIRO (FALITSU)	LATCH CAM DATA ON A FLASH MEMORY DEVICE	AF01023	99-40527		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
29	09/21/98	Granted	6,185,128	2/9/01	AKAGI, TAKAO NIIE KURIHARA, KAZUHIRO (FALITSU) CHEN, JOHNNY CHUNG-LEE 75232	REFERENCE CELL FOUR-WAY SWITCH FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01024	99-40279		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
30	09/21/98	Granted	6,111,787	2/24/01	KURIHARA, KAZUHIRO (FALITSU) CHEN, TIEN-HSI AKAGI, TAKAO NIIE	ADDRESS TRANSITION DETECT TIMING ARCHITECTURE FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01025	99-40343		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP

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31	08/647,566	4/12/04	Granted	6,308,566	3/27/04	KURIHARA, KAZUHIRO (FUJITSU) CHEN, TIEN-MIN (AKAGI), TAKAO (NE)	ADDRESS TRANSITION DETECT TIMING ARCHITECTURE FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01026	09-408499	C	USA	FASL LLC	NYO	WAGNER, MURABITO & HAO, LLP
32	08/421,471	10/18/03	Granted	6,285,585	5/4/04	KURIHARA, KAZUHIRO (FUJITSU) CHEN, TIEN-MIN	OUTPUT SWITCHING IMPLEMENTATION FOR A FLASH MEMORY DEVICE	AF01025	09-408660		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
33	09/422,198	10/18/03	Granted	6,118,595	8/12/03	KURIHARA, KAZUHIRO (FUJITSU) CHEN, TIEN-MIN (AKAGI), TAKAO (NE)	OUTPUT MULTIPLEXING IMPLEMENTATION FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01027			USA	FASL LLC	NYO	WAGNER, MURABITO & HAO, LLP
34	08/881,336	9/14/03	Granted	6,358,282	7/24/04	KURIHARA, KAZUHIRO (FUJITSU) CHEN, TIEN-MIN	OUTPUT BUFFER FOR VOCE	AF01028	09-407722		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
35	08/978,502	10/27/03	Granted	6,310,600	10/03/03	KASA, YASUSHI (FJ) SHEH, MING-HUI	ARCHITECTURE FOR A DUAL-BANK PAGE MODE MEMORY WITH REDUNDANCY	AF01029	01-40276		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
36	08/652,742	3/31/03	Granted	5,298,007	10/26/01	SANTURKAR, VIKRAM D (KASA, YASUSHI) (FJ)	METHOD AND APPARATUS FOR ELIMINATING FALSE DATA IN A PAGE MODE MEMORY DEVICE	AF01030	01-402877		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
37	08/597,891	9/22/03	Granted	6,324,108	11/27/01	BILL, COLIN (BAUTISTA, JR., EDUARDO V. 22872) YAMADA, SHIGEKAZU (FUJITSU)	APPLICATION OF EXTERNAL VOLTAGE DURING ARRAY V _T TESTING	AF01032	02-408016		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
38	08/688,101	9/22/03	Granted	6,458,041	3/20/03	YAMADA, SHIGEKAZU (FUJITSU) BILL, COLIN (BAUTISTA, JR., EDUARDO V. 22872)	NEGATIVE VOLTAGE REGULATION	AF01033	01-40668		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
39	08/650,344	10/5/03	Granted	6,400,638	6/4/02	YAMADA, SHIGEKAZU (FUJITSU) AKAGI, TAKAO (NE) BILL, COLIN (BAUTISTA, JR., EDUARDO V. 22872)	WORDLINE DRIVER FOR FLASH MEMORY READ MODE	AF01036	02-42240		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
40	08/724,875	11/28/03	Granted	6,862,003	6/19/02	YANG, MASARU (NE) HOLLNER, SHANE G. 22164 (CA) CHUNG, MOHAPIL S.D. 23278	MULTI-BIT BLOCK ERASE	AF01041	02-40869		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP

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41	08/30/04	Granted	6,291,737	3/18/01	HOLLMER, SHANE G. 22154 (CA) KURIHARA, KAZUHIRO (FUJITSU)	APPARATUS AND METHOD TO CHARACTERIZE THE THRESHOLD DISTRIBUTION IN AN NROM VIRTUAL GROUND ARRAY	AF01042	G1-40319		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, U
42	09/09/06	Granted	6,573,742	4/16/02	KURIHARA, KAZUHIRO (FUJITSU) HOLLMER, SHANE G. 22154 (CA) CHEN, PAU-LING 15806	TWO SIDE DECODING OF A MEMORY ARRAY	AF01043	G1-40320		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, U
43	09/08/06	Granted	6,590,478	5/24/02	FASTOW, RICHARD 29087 HOLLMER, SHANE G. 22154 (CA) CHEN, PAU-LING 15806 VAN BUSHORC, MICHAEL A. 02048 HIGASHITANI, MASAYUKI	SIDENALL NROM AND METHOD OF MANUFACTURE THEREOF FOR NON-VOLATILE MEMORY CELLS	AF01050			USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, U
44	02/19/07	Granted	6,620,080	3/4/03	KOMORI, HIDEKI (T) 16322 (CA) WANG, FEI 53099 RANGARAJAN, BHARATH	PROCESS FOR FABRICATING A NON-VOLATILE MEMORY DEVICE	AF01057	G2-41200		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, U
45	09/10/06	Granted	6,579,120	02/03	OGURA, TOSUKE (FUJITSU) IZUMI, KYOSHI (FUJITSU) YANO, MASARU (NE) KOMORI, HIDEKI (T) 23353 (CA) HUI, ANGELA T. 21484 (CA)	PROCESS FOR MAKING A DUAL BIT MEMORY DEVICE WITH ISOLATED POLYSILICON FLOATING GATES	AF01063	G2-40851		USA	FASL LLC	NVT	WAGNER, MURABITO & HAO, U
46	09/09/06	Granted	6,713,009	03/07/04	OGURA, TOSUKE (FUJITSU) KURIHARA, KAZUHIRO (FUJITSU) YANO, MASARU (NE) KOMORI, HIDEKI (T) 23353 (CA) HUI, ANGELA T. 21484 (CA)	DUAL BIT MEMORY DEVICE WITH ISOLATED POLYSILICON FLOATING GATES	AF01067	G2-40902		USA	FASL LLC	NVT	WAGNER, MURABITO & HAO, U

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47	08/08/03	10/18/03	6,507,050	02/25/04	SIDULAS, JEFFREY 22523 PHAN, TUAN DUC (Y) 23043 (CA) OSURA, JUSUKE (FUMITSU) RAMARAJAN, BHARATH CHAN, SIMON SIU SING, 22412	METHOD OF FORMING NARROW INSULATING SPACERS FOR USE IN REDUCING MINIMUM COMPONENT SIZE	AP01058	09-40306		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
48	05/30/01	23001	6,243,316	6/5/01	AKAGI, TAKAO (NE) AL-SHAMMA, ALI K. CLEVELAND, LEE 22181 KIM, YONG K. 23062 (CA) LIN, JIN-LIEN TEH, BOON-TANG (PING)	IMPROVED VOLTAGE BOOST RESET CIRCUIT FOR A FLASH MEMORY	AP01071	09-40349		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
49	08/25/03	10/17/03	6,351,421	2/2/02	AKAGI, TAKAO (NE) AL-SHAMMA, ALI K. CLEVELAND, LEE 22181 KIM, YONG K. 23062 (CA) LIN, JIN-LIEN TEH, BOON-TANG (PING)	VOLTAGE BOOST LEVEL CLAMPING CIRCUIT FOR A FLASH MEMORY	AP01072	09-41026		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
50	08/25/03	10/17/03	6,347,052	2/12/03	AKAGI, TAKAO (NE) AL-SHAMMA, ALI K. KIM, YONG K. 23062 (CA) LIN, JIN-LIEN TEH, BOON-TANG (PING) NGUYEN, KENDRA	WORD LINE DECODING ARCHITECTURE IN A FLASH MEMORY	AP01073	09-40797		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
51	08/25/03	2/1/03	6,463,003	10/8/02	AL-SHAMMA, ALI K. AKAGI, TAKAO (NE)	POWER SAVING SCHEME FOR BURST MODE IMPLEMENTATION DURING READING OF DATA FROM A MEMORY DEVICE	AP01074	01-40216		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
52	08/25/03	2/1/03	6,243,040	6/20/01	AKAGI, TAKAO (NE) CLEVELAND, LEE 22181 NGUYEN, KENDRA	MULTIPLE BANK SIMULTANEOUS OPERATION FOR A FLASH MEMORY	AP01075	01-40743		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP

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App. No.	App. Date	Status	Parent No.	Grant Date	Inventors	Title	Doc. No.	Pub. Ref. No.	Pub. Date	Country	Location	Div.	Entity	
53	08/26/01	4/8/01	Granted	5,521,761	08/08/03	AKAOGI, TAKAO WIE CLEVELAND, LEE 22181 NGUYEN, KENDRA	BURST ARCHITECTURE FOR A FLASH MEMORY	AF01076	03- 40639		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
54	08/24/00	11/28/00	Granted	5,507,787	08/28/01	AL-SHAMMA, ALI K. AKAOGI, TAKAO WIE	BURST READ INCORPORATING OUTPUT BASED RE-REDUNDANCY	AF01078	03- 40738		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
55	08/28/01	10/27/00	Granted	5,507,527	08/14/03	CLEVELAND, LEE 22181 LIN, JUN-LEW AKAOGI, TAKAO WIE AL-SHAMMA, ALI K. TCH, BOON-TANG SPNG NGUYEN, KENDRA	MEMORY LINE DISCHARGE REFRESH SENSING	AF01080	01- 40818		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
56	08/28/01	07/11/01	Granted	5,229,735	08/01/01	AKAOGI, TAKAO WIE NGUYEN, KENDRA KIM, YONG K. 22082 (KCA) CLEVELAND, LEE 22181	BURST READ WORDLINE BOOSTING	AF01081	00- 40753		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
57	09/07/01	07/28/00	Granted	6,400,633	08/02/01	AL-SHAMMA, ALI K. AKAOGI, TAKAO WIE CLEVELAND, LEE 22181	POWER-SAVING MODES FOR MEMORIES	AF01082	01- 40281		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
58	10/08/02	07/02	Granted	6,811,473	08/28/03	AL-SHAMMA, ALI K. AKAOGI, TAKAO WIE CLEVELAND, LEE 22181	POWER-SAVING MODES FOR MEMORIES	AF01083	01- 40281B		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
59	09/08/01	1/24/00	Granted	6,212,108	08/01/01	AKAOGI, TAKAO WIE CLEVELAND, LEE 22181	DISTRIBUTED VOLTAGE CHARGE CIRCUITS TO REDUCE SENSING TIME IN A MEMORY DEVICE	AF01084	00- 43814		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP
60	10/24/03	07/12/02	Granted	6,728,180	02/27/04	YANG, TIEN- CHUN KUROHARA, NAZUMIRO (REIITSU) CHEN, PALLING 18998	PATH GATE DRIVER CIRCUIT	AF01187	02- 41821		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP

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App. No.	App. Date	Status	Patent No.	Grant Date	Inventors	Title	Doc. No.	Publ. Ref. No.	Publ. Type	Country	Location	Dir.	Class
03419,385	10/6/99	Granted	6,184,148	10/17/00	BILL COLIN 15372 SU, JONATHAN 22517 AKAOGI, TAKAO NE GITAIA, RAVI P. 21656 (T)	WORDLINE DRIVER FOR FLASH ELECTRICALLY ERASABLE PROGRAMMABLE EAD-ONLY MEMORY (EEPROM)	0715497	03- 02345		USA	FASL LLC	NVT	WAGNER, MURABITO & HAO, LLP
03419,385	10/14/99	Granted	6,147,908	11/14/00	BILL COLIN 15372 YAMADA, SHIGEKAZU (F/JM/L)	METHOD AND SYSTEM FOR SAVING OVERHEAD PROGRAM TIME IN A MEMORY DEVICE	02248	03- 10866		USA	FASL LLC	NVD	WAGNER, MURABITO & HAO, LLP

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Requested Corrections of Recordation of Assignment

	App. No.	App. Date	Status	Patent No.	Grant Date	Inventors	Title	Docket No.
27	09/421,962	10/19/99	Granted	6,128,056	8/28/00	KUO, TIAO-HUA 22492 (CA) LEONG, NANCY S. 22609 AKAOGI, TAKAO N/E CHEN, JOHNNY CHUNG-LEE 75232	SYSTEM FOR OPTIMIZING THE EQUALIZATION PULSE OF A READ SENSEAMPLIFIER FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01020
30	09/421,776	10/19/99	Granted	6,111,787	8/29/00	KURIHARA, KAZUHIRO (FUJITSU) CHEN, TIEN-MIN AKAOGI, TAKAO N/E	ADDRESS TRANSITION DETECT TIMING ARCHITECTURE FOR A SIMULTANEOUS OPERATION FLASH MEMORY DEVICE	AF01026
40	09/724,676	11/28/00	Granted	6,222,003	9/16/03	YANO, MASARU N/E HOLLMER, SHANE C. 22154 (CA) CHUNG, MICHAEL S.C. 23278	MULTI-SET BLOCK ERASE	AF01041
50	09/400,340	1/24/00	Granted	6,212,108	4/3/01	AKAOGI, TAKAO N/E CLEVELAND, LEE 22181	DISTRIBUTED VOLTAGE CHARGE CIRCUITS TO REDUCE SENSING TIME IN A MEMORY DEVICE	AF01004