#### D TRADEMARK OFFICE

Docket No.: NVID-P000784

hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Mail Stop Assignments, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the

below date of deposit.

Date of 12/18/03 Deposit:

Name of Person Making the Deposit: ANTHONY CHOU

Signature of the Person

Making the Deposit:

Mail Stop Assignments Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Please record the attached original document(s) or copy(ies) thereof:

#### Transmittal of an Assignment

1. Name of conveying Michael B. Diamond, John S. Montrym, James M. Van Dyke, Michael B. Nagy and Sean J. Treichler

party(ies):

Group Art Unit:

Serial No.:

Filed:

12/18/03

Examiner:

Name and Address of NVIDIA CORPORATION

Receiving party(ies)

2701 San Tomas Expressway, Santa Clara, CA 95050

**3.** Nature of Conveyance: Assignment

Execution Date:

12/16/03, 12/16/03, 12/15/03, 12/15/03, 12/17/03

New Patent Application entitled:

A SYSTEM AND METHOD FOR CONFIGURING SEMICONDUCTOR

FUNCTIONAL CIRCUITS

Name and address of party to whom correspondence concerning document should be mailed:

Name:

John F. Ryan

Address

Wagner, Murabito & Hao LLP

Two North Market Street, Third Floor, San Jose, CA 95113

Phone

(408) 938-9060

6. Total Number of applications and patents involved: ONE

7. Fee Calculation (for other than a small entity)

Assignment Recordation Fee, per property

**Total Fees** (37 CFR 3.41)

1 X \$40.00

The amount of \$ 40.00

is enclosed

8. At any time during the pendency of this application, please charge any additional fees required or credit any overpayments to Deposit Account 23-0085. A duplicate copy of this transmittal is enclosed.

9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

18/29/2003 MDAMTE1 00000033 10740722

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40.00 OP

Date:

December 18, 2003

ame John F. Rvar

Rea. No. 47.050

Page 1 of 1

**PATENT** 

REEL: 014827 FRAME: 0092

\$40.00

524044/01

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: NVID-P000784

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Mail Stop Assignments Commissioner for Patents

P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please record the attached original document(s) or copy(ies) thereof:

### Transmittal of an Assignment

1. Name of conveying Michael B. Diamond, John S. Montrym, James M. Van Dyke, Michael B. Nagy and Sean J. Treichler

party(ies):

Filed:

Serial No.:

12/18/03

Group Art Unit:

Examiner:

2. Name and Address of NVIDIA CORPORATION

2701 San Tomas Expressway, Santa Clara, CA 95050 Receiving party(ies)

Assignment 3. Nature of Conveyance:

12/16/03, 12/16/03, 12/15/03,12/15/03, 12/17/03 Execution Date:

A SYSTEM AND METHOD FOR CONFIGURING SEMICONDUCTOR 4. New Patent Application entitled:

FUNCTIONAL CIRCUITS

5. Name and address of party to whom correspondence concerning document should be mailed:

Name:

John F. Ryan

Address

Wagner, Murabito & Hao LLP

Two North Market Street, Third Floor, San Jose, CA 95113

Phone

(408) 938-9060

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> John F. Ryan 'ne

Reg. No. 47,050

December 18, 2003 Date:

Page 1 of 1

**PATENT** 

1 X \$40.00

\$40.00

REEL: 014827 FRAME: 0093

## **Assignment to NVIDIA Corporation**

	nd valuable consideration, receipt of which	
do hereby sell, assign, an naving its principal place	d transfer unto NVIDIA Corporation (hereing e of business at 2701 San Tomas Exp the entire right, title, and interest for the Un	inafter called NVIDIA), a Delaware Corporation pressway, Santa Clara, CA 95050, and its nited States and all foreign countries, in and to d under, the application for the United States
A SYSTEM AND METHOD	FOR CONFIGURING SEMICONDUCTOR	FUNCTIONAL CIRCUITS
X filed herewith and the United States and o	e inventions set forth and described therein f countries foreign thereto which may be gr	n, and any and all Letters Patent of the ranted thereon or therefor; or
Serial No.:	· filed on	and the inventions set forth
and described there which may be grante	in, and any and all Letters Patent of the U ad thereon or therefor;	and the inventions set forth United States and of countries foreign thereto
execute and deliver withon continuation, divisional or its successors and assign States and in any cour proceedings involving sai	out further compensation any power of attor reissue, or other papers which may be ne ns, the inventions described in said applica atry foreign thereto, and to cooperate a	uest of NVIDIA, its successors or assigns, to brney, assignment, application, whether original, ecessary or desirable fully to secure to NVIDIA, cation and all patent rights therein, in the United and assist in the prosecution of interference examination of said Letters Patent provided the ation and assistance are paid by NVIDIA.
mortgage, license, or oth	er agreement affecting the rights and prope	egal representatives that no assignment, grant, perty herein conveyed has been made to others expressed is possessed by the undersigned;
	nereunto set my/our hand(s) and seal:	
Inventor's Signature:	costicue to dime	Date: 12/16/03

Page 1 of 1

rev. 11/97 cpn

Date: 12/16/03

Assignment to NVIDIA Corporation In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we John S. Montrym do hereby sell, assign, and transfer unto NVIDIA Corporation (hereinafter called NVIDIA), a Delaware Corporation having its principal place of business at 2701 San Tomas Expressway, Santa Clara, CA 95050, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled: A SYSTEM AND METHOD FOR CONFIGURING SEMICONDUCTOR FUNCTIONAL CIRCUITS X filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or and the inventions set forth filed on Serial No.: and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; And for the above consideration, I/we agree promptly upon request of NVIDIA, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to NVIDIA, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by NVIDIA. I/we further covenant with NVIDIA, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned: In witness whereof, I/we hereunto set my/our hand(s) and seal:

Page 1 of 1

Inventor's Signature:

rev. 11/97 epn

PATENT REEL: 014827 FRAME: 0095

## **Assignment to NVIDIA Corporation**

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we James M. Van Dyke
do hereby sell, assign, and transfer unto NVIDIA Corporation (hereinafter called NVIDIA), a Delaware Corporation having its principal place of business at 2701 San Tomas Expressway, Santa Clara, CA 95050, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:
A SYSTEM AND METHOD FOR CONFIGURING SEMICONDUCTOR FUNCTIONAL CIRCUITS
filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor, or
Serial No.:  filed on  and the inventions set forth  and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;
And for the above consideration, I/we agree promptly upon request of NVIDIA, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to NVIDIA, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by NVIDIA.
I/we further covenant with NVIDIA, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;
n witness whereof, I/we hereunto set my/our hand(s) and seal:  nventor's Signature:  Date: 17-15-03

€age 1 of 1

rev. 11/97 epn

### **Assignment to NVIDIA Corporation**

In consideration of good	and valuable consideration, recei	ot of which is hereby	acknowledwed I/we
Michael 3. Nagy do hereby sell assign Corporation having its pi its successors and assig	and transfer unto NVIDIA C incipal place of business at 2701 ns, the entire right, title, and into	Corporation (hereinal San Tomas Express erest for the United S	Rer called NVIDIA), a Delaware sway, Santa Clara, CA 95050, and States and all foreign countries, in ader, the application for the United
A SYSTEM ADD MEDI <b>HO</b> .	) FOR CONFIGURING SEMICON	BUCTOR FUNCTION,	AL CIRCUITS
	e inventions set forth and describ of countries foreign thereto which		
	filed on in, and any and all Letters Pater ed thereon or therefor;	nt of the United State	and the inventions set forth es and of countries foreign thereto
execute and deliver wi original, continuation, di- to NVIDIA, its successor the United States and interference proceedings	hout further compensation any isional or reissue, or other paper is and assigns, the inventions des- in any country foreign thereto involving said inventions and in	power of attorney, ers which may be need cribed in said applical, and to cooperate the adjudication or re-	DIA, its successors or assigns, to assignment, application, whether cessary or desirable fully to secure tion and all patent rights therein, in and assist in the prosecution of examination of said Letters Patent peration and assistance are paid by
mortgage, license, or o	her agreement affecting the rig	hts and property he	ntatives that no assignment, grant, rein convoyed has been made to in expressed is possessed by the
In witness whereof, t/we	hereunto set my/our hand(s) and	seal:	, ,
Inventor's Signature	Mal. AB	Nozy Da	ne. 12/15/03

Page 1 of 1

rev. 11/97 opn

### **Assignment to NVIDIA Corporation**

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we Sean J. Treichler

do hereby sell, assign, and transfer unto NVIDIA Corporation (hereinafter called NVIDIA), a Delaware Corporation having its principal place of business at 2701 San Tomas Expressway, Santa Clara, CA 95050, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

- A SYSTEM AND METHOD FOR CONFIGURING SEMICONDUCTOR FUNCTIONAL CIRCUITS
- K filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

Serial No.: filed on and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;

And for the above consideration, I/we agree promptly upon request of NVIDIA, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to NVIDIA, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by NVIDIA.

I/we further covenant with NVIDIA, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

in witness whereof, I/v	ve hereunto set my/our hand(s) and seal:					
Inventor's Signature:		Date:	17	2)-	200	

Page 1 of 1

RECORDED: 12/18/2003

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