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(1817-1817-1817-1818-1818)	
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To the Honorable Commissioner of Patents 10265	
1. Name of conveying party(ies):	2. Name and address of receiving party(ies):
JINN-YEH CHIEN	Name: Taiwan Semiconductor Manufacturing
1.12.04	Company
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dditional name(s) or conveying party(ies) attached? Yes N	Internal Address:
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3. Nature of conveyance	No. 8, Li-Hsin Rd. 6
X Assignment Merger	Science-Based Industrial Park Hsin-Chu, Taiwan 300-77
	Taiwan, R.O.C.
Security Agreement Change of Name	
other	
Execution Date: 11/7/03	Additional name & address attacked \$ \tag{2} \tag{3}
Execution Date: <u>11/7/03</u>	Additional name & address attached? Yes X No
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4. Application number(s) or patent number(s):	ion, the execution date of the application is: 11/7/03  B. Patent No.(s)
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PATENT REEL: 014896 FRAME: 0965

TS02-798	For X U.S. and/or X Foreign Rights				
1302-796	For X Application or U.S. Patent				
	By x Inventor(s) or Present Owner				
ASSIGNMENT OF INVENTION					
In consideration of the payment by ASSIGNEE to ASSIGNOR of the sum of One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for other good and valuable consideration,					
ASSIGNOR:					
JINN-YEH CHIEN Type or print name of ASSIGNOR	•				
Address 2nd Fl. No 7, Lane 63, Yuan-Yang St. Taipei, Taiwan	Jung-Li, Tao-Yuan Taiwen				
Republic of China, Timan					
ASSIGNEE:					
Taiwan Semiconductor Manufacturing Co No. 8, Li-Hsin-Road 6 Science-Based Industrial Park Hsin-Chu, Taiwan, R.O.C. Taiwan, R.O.C.	·				
and the successors, assigns and legal represe	entatives of the ASSIGNEE				
(complete one of the following)					
x the entire right, title and interest					
an undivided percent (%) i  (check the following box/f foreig)  x and in all foreign countries	nterest for the United States and its territorial possessions in rights are also to be assigned)				
in and to any and all improvements which are	disclosed in the invention entitled:				

Memory Built-In Self Test Circuit With Full Error Mapping Capability

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(a)	х	U.S. patent application executed on even d	ate herewith
(b)		U.S. patent application executed on	
(c)			filed on
(d)		U.S. patent no.	
		(also check (e) if fore	ign application(s) is also being assigned)
(e)	x	and any legal equivalent thereof in a foreigr	n country, including the right to clam priority
	inuati		r said invention by the above application or any ad as to letters patent any re-issue or re-examination
be m	ASS nade	SIGNOR hereby covenants that no assignmen or entered into which would conflict with this	t, sale, agreement or encumbrance has been or will assignment.
may or pr any a appli or de	be k ocee and a icatio esirat	nown and accessible to ASSIGNOR and will ding related thereto and will promptly execute all papers, instruments or affidavits required to an, said invention and said Letters Patent and ple to carry out the purposes thereof.  ESS WHEREOF, I / We have signed this	n and said Letters Patent and legal equivalents as testify as to the same in any interference, litigation and deliver to ASSIGNEE or its legal representatives apply for, obtain, maintain, issue and enforce said disaid equivalents thereof which may be necessary signing)
WAF	NINS	G Date of signing must be the same as the date of exe	cution of the application if item (a) was checked above.
			Jim - Jeh Chien Signature of ASSIGNOR(S)
f AS	SIGN	NOR is a legal entity complete the following inform	mation
			Type or print the name of the above person authorized to sign on
			behalf of ASSIGNOR
	Note:	No witnessing, notarization or legalization is processed, if the analysis	Title
	261).	Use next page if notarization is desired.	ment is notarized or legalized then it will only be prima facie evidence (35 USC

and which is found in

**RECORDED: 01/12/2004** 

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