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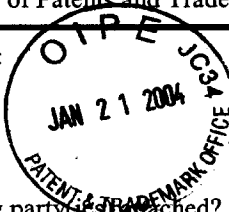
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If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s)

10/623,559

B. Patent No.(s)

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In consideration of value received, I, having a mailing address at c/o Intellectual Property Division, Toshiba Corporation, 1-1, Shibaura 1-chome, Minato-ku, Tokyo 105-8001, Japan and a residence as stated below next to my name, the sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are listed below) of an invention described in an application for United States patent entitled:

SEMICONDUCTOR APPARATUS HAVING LOGIC LEVEL DECISION CIRCUIT AND INTER-SEMICONDUCTOR APPARATUS SIGNAL TRANSMISSION SYSTEM

said application having been filed on July 22, 2003
and assigned Application No. 10/623,559 ;
sell and assign to
KABUSHIKI KAISHA TOSHIBA
a corporation of Japan, having a business address at
1-1 Shibaura 1-chome, Minato-Ku, Tokyo, Japan

its successors, assigns or nominees, hereinafter referred to as "Assignee", my entire right, title and interest in and to said invention as disclosed, shown and described in said application for United States patent: (check one)

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02S1434

Inventor: (Signature)

Date

Residence

Yoshihisa Iwata

AUG. 18. 2003

Yokohama-shi, Japan

Yoshihisa Iwata

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