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10/647,242

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invention described in an application for United States patent entitled:

SEMICONDUCTOR MEMORY DEVICE INCLUDING MOS TRANSISTORS EACH HAVING A FLOATING GATE
AND A CONTROL GATE

said application having been filed on August 26, 2003
and assigned Application No. 10/647,242 ;

sell and assign to

KABUSHIKI KAISHA TOSHIBA

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T.H.

SEP. 17. 2003

Takehiro Hasegawa
Takehiro Hasegawa

Yokohama-shi, Japan

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