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To the Honorabl	e Commissioner of Patents and Trademarks. Ple	ease record the attached original documents or copy thereof.	
(1) KENJI H	nnveying Party(ies): ANADA AND (2) AKIO ISHIZU s of conveying parties attached: □ Yes ⊠ No	2. Name and Address of Receiving Party(ies): Name: Renesas Technology Corp. Address: 4-1, Marunouchi 2-chome City: Chiyoda-ku, Tokyo	
3. Nature of C	Jonvavanca	Japan	
Assign ■ Assign	nment	Name: Renesas Eastern Japan Semiconductor Inc. Address: 3-2, Fujihashi 3-chome City: Ome-shi, Tokyo, Japan	
Execution D	ates: (1) October 10, 2003 and (2)		
October 15,			
4. (a) Patent A	Application Number(s): 10/659,433	4. (b) Patent Numbers:	
If this document is being filed together with a new application, the execution date of the application is:			
5. Name and Address of Party to whom Correspondence Concerning this Document Should be Mailed:		6. Total Number of Applications and Patents Involved: 1	
Name:	Stanley P. Fisher	7. Total Fee: \$40.00 (37 C.F.R. § 3.41)	
Address:	Reed Smith LLP 3110 Fairview Park Dr. Suite 1400 Falls Church, VA 22042	Enclosed.Authorized to be charged to deposit account.	
		8. Deposit Account Number: 08-1480	
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To the best original docume		e and correct and any attached copy is a true copy of the February 3, 2004	
Stanley P. Fisher, Registration No. 24, 344 Juan Carlos A. Marquez, Registration No. 34,072			
Total number of pages comprising cover sheet:			
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PATENT REEL: 014961 FRAME: 0137

ASSIGNMENT (譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Renesas Technology Corp.,

and Renesas Eastern Japan Semiconductor, Inc.,

corporations organized under the laws of Japan,

located at 4-1, Marunouchi 2-chome, Chiyoda-ku, Tokyo, Japan,

and 3-2, Fujihashi 3-chome, Ome-shi, Tokyo, Japan,

receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Technology Corp.,

and Renesas Eastern Japan Semiconductor, Inc.,

their successors and assigns, all my right, title and interest, in and for the United States of America, in and to

MANUFACTURING METHOD OF SOLID-STATE IMAGE SENSING DEVICE

Serial No. 10/659,433

filed on 11/September/2003

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Renesas Technology Corp., and Renesas Eastern Japan Semiconductor, Inc.,

their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Technology Corp., and Renesas Eastern Japan Semiconductor, Inc.,

Signed on the date(s) indicated aside our signatures:

RECORDED: 02/03/2004

INIVENITORIS

(発明者フルネームサイン)	(署名日)
1) Kenji HANADAKenji HANADA	10/October/2003
2) Akio ISHIZU AKIO ISHIZU	15/October/2003
3)	
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PATENT REEL: 014961 FRAME: 0138

Data Signed