Docket No.: NSC-P05793

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Mail Stop Assignments, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

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02/13/04 Deposit:

Mail Stop Assignments Commissioner for Patents

Alexandria, VA 22313-1450

Name of Person Making the Deposit:

Anthony Chou

Signature of the Person

Making the Deposit:

02-24-2004

102676588

Please record the attached original document(s) or copy (ies) thereof:

Transmittal of an Assignment

1. Name of Conveying Party(ies):

Wayne Rewinkel

Serial No.: Filed: Group Art Unit:

Examiner:

2. Name and Address of Receiving

party(ies):

**National Semiconductor Corporation** 

2900 Semiconductor Drive Santa Clara, CA 95051

3. Nature of Conveyance:

Assignment

Execution Date:

02/13/04.

02/13/04

4. New Patent Application entitled:

METHOD AND SYSTEM FOR PROVIDING A BIAS VOLTAGE FOR

A POWER INPUT SUB-CIRCUIT OF A SWITCHED MODE POWER

SUPPLY

5. Name and address of party to whom correspondence concerning document should be mailed:

Wagner, Murabito & Hao LLP Two North Market Street, Third Floor San Jose, CA 95113 (408) 938-9060

10779220

2-13-04

6. Total Number of applications and patents involved: ONE

7. Fee Calculation (for other than a small entity).	FEES
[X] Assignment Recordation Fee, per property	1 x \$ <u>40.00</u>
[X] Total Fees (37 CFR 3.41)	\$ <u>40.00</u>
[X] The amount of \$40.00 is enclosed	

8. At any time during the pendency of this application, please charge any additional fees required or credit any over payments to Deposit Account 23-0085. A duplicate copy of this transmittal is enclosed.

9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date: February 12, 2004

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02/20/2004 HLE333 03 FC:8021

40.00 DP

Page 1 of 1

mald & Qu Name: Reginald A. Ratliff

Reg. No.: 48,098

rev. 6/97 ipw

PATENT REEL: 014994 FRAME: 0011

Attorney	Docket No.	- NS	C-P05793
AROHES	DOCKEL NO.	· IIO	C-I U3/23

## **Assignment to National Semiconductor Corporation**

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we Wayne REWINKEL do hereby sell, assign, and transfer unto National Semiconductor Corporation (hereinafter called National Semiconductor), a Delaware Corporation having its principal place of business at 2900 Semiconductor Drive, Santa Clara, CA 95051, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled: METHOD AND SYSTEM FOR PROVIDING A BIAS VOLTAGE FOR A POWER INPUT SUB-CIRCUIT OF A SWITCHED MODE POWER SUPPLY X filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or Serial No.: and the inventions set forth filed on and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; And for the above consideration, I/we agree promptly upon request of National Semiconductor, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to National Semiconductor, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by National Semiconductor. I/we further covenant with National Semiconductor, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned; In witness whereof, I/we hereunto set my/our hand(s) and seal: Whype Kewinkel Date: 2-13-2004 Inventor's Signature: State of County of Before me this day of personally appeared who is personally known or proved to me on the basis of satisfactory evidence to be the person day of who acknowledged the foregoing instrument of assignment to be \_\_\_\_\_ his or \_\_\_\_ her free act and deed. Notary Stamp/Seal Notary Public

Page 1 of 1

rev. 11/97 epn

Docket No.: NSC-P05793

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- Statement and signature:

RECORDED: 02/13/2004

To the best of my knowledge and belief, the foregoing information is true and correct and

any attached copy is a true copy of the original document.

Date: February 12, 2004

Name: Reginald A. Ratliff

ginald & ga

Reg. No.: 48,098

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**PATENT**