

02/18/04

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To the Honorable Commissioner

102677783

attached original documents or copy thereof.

1. Name of conveying party(ies): Paresh Chatterjee, Chayan Biswas, Ragendra Mishra, Senthil Thangaraj. Additional name of conveying party(ies) attached? [] Yes [X] No

2. Name and address of receiving party(ies): Name: LSI Logic Corporation, Internal Address: Street Address: 1621 Barber Lane, City: Milpitas State: CA Zip: 95035. Additional Name(s) & address(es) attached? [] Yes [X] No

15535 U.S. PTO 10780971 021804

3. Nature of conveyance: [X] Assignment [] Merger, [] Security Agreement [] Change of Name, [] Other

Execution Date: 2/11/2004

4. Application number(s) or patent number(s): 10/780971. If this document is being filed together with a new application, the execution date of the application is: 2/11/2004. A. Patent Application No.(s) B. Patent No.(s)

Additional numbers attached? [] Yes [X] No

5. Name and address of party to whom correspondence concerning this document should be mailed: Name: LSI Logic Corporation, Internal Address: Intellectual Property Law Dept. M/S D-106, Street Address: 1621 Barber Lane, City: Milpitas State: CA Zip: 95035

6. Total number of applications and patents involved: 1. 7. Total fee (37 CFR 3.41) \$ 40.00. [] Enclosed [X] Authorized to be charged to deposit account. 8. Deposit account number: 12-2252. (Attach duplicate copy of this page if paying by deposit account)

02/25/2004 DBYRNE 00000182 122252 10780971 01 FC:021 40.00 DA

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9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Daniel N. Fishman, Reg. No. 35,512, February 18, 2004. Name of Person Signing, Signature, Date

Total number of pages including cover sheet, attachments, and documents: 4

Mail documents to be recorded with required cover sheet information to: Commissioner of Patents & Trademarks, Box Assignments, Washington, D.C. 20231

**ASSIGNMENT AND AGREEMENT IN AN APPLICATION FOR
LETTERS PATENT OF THE UNITED STATES OF AMERICA**

I hereby declare that I am the sole inventor (if only one inventor is named below) or an original, first and joint inventor (if plural inventors are named below) of the invention relating to "SYSTEMS AND METHODS FOR CACHE SYNCHRONIZATION BETWEEN REDUNDANT STORAGE CONTROLLERS" described in an application for Letters Patent of the United States of America filed on _____, Serial No. _____/_____,_____, or, if not identified here by filing date and serial number, executed by me on even date with my execution of this Assignment.

For valuable consideration received, I hereby sell, assign and transfer to LSI LOGIC CORPORATION, duly organized and existing under the laws of the State of Delaware, United States of America, its successors and assigns, my entire right, title and interest, for all countries in and to said inventions and all the rights and privileges under any and all Letters Patent that may be granted therefore, and any divisions, continuations, reissues and extensions thereof.

I agree that, when requested, I will, without charge to said corporation, but at its expense, sign all papers, take all rightful oaths, make all rightful declarations and do all acts which may be necessary, desirable or convenient for securing and maintaining patents or other forms of protection for said inventions in any and all countries and for vesting title thereto in said corporation, its successors, assigns or nominees.

I agree that I will communicate to said corporation or its representatives any facts known to me respecting said inventions and when requested by said corporation and at its expense will testify in any legal proceedings, and generally do everything possible to aid said corporation, its successors, assigns and legal representatives or nominees, to obtain or enforce proper protection for said inventions in any and all countries.

I authorize and empower said corporation, its successors, assigns or nominees, to make application for patent or other form of protection for said inventions in its or their own name, or in my/our name, in any and all countries to invoke and claim for any application for patent or other form of protection for said inventions filed by it or them, the benefit of the right of priority provided by the International Convention for the Protection of Industrial Property, as amended, or by any convention which may henceforth be substituted for it, and to invoke and claim such right of priority without further written or oral authorization from me.

I hereby consent that a copy of this Assignment shall be deemed a full legal and formal equivalent of any assignment, consent to file or like document which may be required in any country for any purpose and more particularly in proof of the right of the said corporation or its successors, assigns or nominees to apply for patent or other proper protection for said inventions, and to claim the aforesaid benefits of the right of priority provided by the International Convention for the Protection of Industrial Property, as amended, or by any convention which may henceforth be substituted for it.

I covenant with LSI LOGIC CORPORATION, its successors, assigns, legal representatives, and nominees, that to the best of my knowledge the right, title and interest herein conveyed by me are free and clear of any encumbrance, and that I have the full right to convey the same as herein expressed.

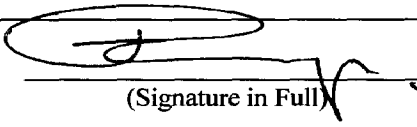
This Agreement and Assignment supersedes all other assignments, agreements and addendums related to the above-identified application.

After this Assignment has been recorded, please return it to :

Attn: LSI Logic Corporation
Intellectual Property Law Dept. M/S D-106
1621 Barber Lane
Milpitas, CA 95035

SOLE OR JOINT

Inventor (1) PARESH CHATTERJEE


(Signature in Full)

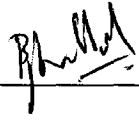
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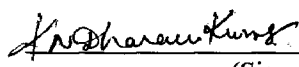
Post Office Address: 22660 Monticello Terrace, Fremont, CA 94539

Residence: Fremont, California

1st Witness: Basavaraj Hallyal
(Type or Print)

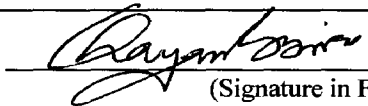
 02/11/04
(Signature)

2nd Witness: NARASIMHULU D KOTTE
(Type or Print)

 02/11/04
(Signature)

SOLE OR JOINT

Inventor (2) CHAYAN BISWAS


(Signature in Full)

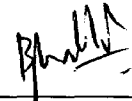
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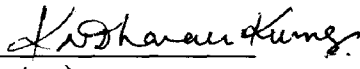
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SOLE OR JOINT

Inventor (3) RAGENDRA MISHRA


(Signature in Full)


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Date: 02/11/04

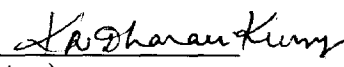
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(Signature)

2nd Witness: NARASIMHULU D KOTTE
(Type or Print)

[Signature] 02/11/04
(Signature)