

02-27-2004

ASSIGNMEN



ET

102679638

22278 U.S. PTO
10780950



2-17-04

To: Honorable Commissioner of Patents and Trademarks:

Please record the attached original document(s) or copy thereof.

1. Name of conveying party(ies)
a) Hardayal Singh Gill
2. Name and address of receiving party(ies):
a) Name: Hitachi Global Storage Technologies Netherlands B.V.
Address: Locatellikade 1
Parnassustoren
1076 AZ Amsterdam
The Netherlands
3. Nature of conveyance

| | | | |
|-------------------------------------|--------------------|--------------------------|-------------------|
| <input checked="" type="checkbox"/> | Assignment | <input type="checkbox"/> | Merger |
| <input type="checkbox"/> | Security Agreement | <input type="checkbox"/> | Change of Name |
| <input type="checkbox"/> | Other _____ | <input type="checkbox"/> | License Agreement |

Execution Date: February 12, 2004

4. Application Number(s) or Patent Number(s): Unassigned

10780950

The title of the (new) application is:

**MAGNETORESISTIVE SENSOR HAVING IMPROVED
ANTIPARALLEL TAB FREE LAYER BIASING**

5. Please send all correspondence concerning this (these) documents to:

Silicon Valley IP Group, PC
P.O. Box 721120
San Jose, California 95172-1120

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41): \$40.00

- ☐ Enclosed
☒ Authorized to be charged to Deposit Account No. 50-2587
(Order No. HSJ920040006US1/HIT1P072)

8. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date:

2/17/04

Ronald B. Feece

Registration No. 46,327

02/26/2004 LMUELLER 00000131 502587 10780950

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40.00 DA

Attorney Docket No. HIT1P072/HSJ920040006US1

(Revised 01/96)

PATENT
REEL: 015005 FRAME: 0395

ASSIGNMENT OF PATENT APPLICATION

(Accompanying Application)

Whereas I/we the undersigned inventor(s) have invented certain new and useful improvements as set forth in the patent application entitled:

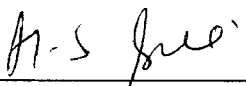
MAGNETORESISTIVE SENSOR HAVING IMPROVED ANTIPARALLEL TAB FREE LAYER BIASING

(Atty. Docket No.: HIT1P072/HSJ920040006US1) for which I (we) have executed an application for a United States Letters Patent.

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/we the undersigned inventor(s) hereby:

- 1) Sell(s), assign(s) and transfer(s) to **Hitachi Global Storage Technologies Netherlands B.V.**, a corporation having a place of business at Locatellikade 1, Parnassustoren, 1076 AZ Amsterdam, The Netherlands, (hereinafter referred to as "ASSIGNEE"), the entire right title and interest in any and all improvements and inventions disclosed in, application(s) based upon, and Patent(s) (including foreign patents) granted upon the information which is disclosed in the above referenced application.
- 2) Authorize and request the Commissioner of Patents to issue any and all Letters Patents resulting from said application or any division(s), continuation(s), substitutes(s) or reissue(s) thereof to the ASSIGNEE.
- 3) Agree to execute all papers and documents and, entirely at the ASSIGNEE's expense, perform any acts which are reasonably necessary in connection with the prosecution of said application, as well as any derivative and applications thereof, foreign applications based thereon, and/or the enforcement of patents resulting from such applications.
- 4) Agree that the terms, covenants and conditions of this assignment shall inure to the benefit of the Assignee, its successors, assigns and other legal representative, and shall be binding upon the inventor(s), as well as the inventor's heirs, legal representatives and assigns.
- 5) Warrant and represent that I/we have not entered, and will not enter into any assignment, contract, or understanding that conflicts with this assignment.

Signed on the date(s) indicated beside my (our) signature(s).

1) Signature:  Date: 2/12/04
Typed Name: Hardayal Singh Gill