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Assignment

Whereas, I/ We , Mitsuh	iro SHIMAMOTO of				
hereinafter called assignor(s), have a SEMICONDUCTOR INTEGRATION	invented certain improvements in				
and executed an application for Lett March 15, 2004; and	ers Patent of the United States of America therefor on				
Whereas,	NEC Electronics Corporation,				
	1753 Shimonumabe, Nakahara-ku,				
	Kawasaki, Kanagawa 211-8668,				
	Japan				
(assignee), desires to acquire invention, and to any United States	e the entire right, title, and interest in the application and patents to be obtained therefor;				
Now therefore, for valuable consideration, receipt whereof is hereby acknowledged,					
any Letters Patent granted upon the invention set forth in the application to the assignee, its successors and assigns; and I/we will execute without further consideration all papers deemed necessary by the assignee in connection with the United States application when called upon to do so by the assignee. I/We hereby authorize and request our attorneys SUGHRUE MION, PLLC of 2100 Pennsylvania Avenue, NW, Washington, DC 20037-3213 to insert here in parentheses (Application number and Confirmation number					
, file	d) the filing date				
and application number of said applicate: March 15, 2004	Mitsuhiro Shimamoto Mitsuhiro Shimamoto				
s/ Mits	suhiro SHIMAMOTO				
Date:					
s/					
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