

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT												
NATURE OF CONVEYANCE:	Corrective Assignment to correct the Assignee's name from Elitemt Semiconductor Memory Technology Inc. to Elite Semiconductor Memory Technology Inc. previously recorded on Reel 015048 Frame 0355. Assignor(s) hereby confirms the Assignment of Assignor's Interest.												
CONVEYING PARTY DATA													
<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 70%; text-align: center;">Name</td> <td style="width: 30%; text-align: center;">Execution Date</td> </tr> <tr> <td>Chung Zen Chen</td> <td>07/27/2004</td> </tr> </table>		Name	Execution Date	Chung Zen Chen	07/27/2004								
Name	Execution Date												
Chung Zen Chen	07/27/2004												
RECEIVING PARTY DATA													
<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 20%;">Name:</td> <td>Elite Semiconductor Memory Technology Inc.</td> </tr> <tr> <td>Street Address:</td> <td>No. 23, Industry E. Rd IV</td> </tr> <tr> <td>Internal Address:</td> <td>Science-Based Industrial Park</td> </tr> <tr> <td>City:</td> <td>Hsinchu</td> </tr> <tr> <td>State/Country:</td> <td>TAIWAN</td> </tr> <tr> <td>Postal Code:</td> <td>300</td> </tr> </table>		Name:	Elite Semiconductor Memory Technology Inc.	Street Address:	No. 23, Industry E. Rd IV	Internal Address:	Science-Based Industrial Park	City:	Hsinchu	State/Country:	TAIWAN	Postal Code:	300
Name:	Elite Semiconductor Memory Technology Inc.												
Street Address:	No. 23, Industry E. Rd IV												
Internal Address:	Science-Based Industrial Park												
City:	Hsinchu												
State/Country:	TAIWAN												
Postal Code:	300												
PROPERTY NUMBERS Total: 1													
<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 30%; text-align: center;">Property Type</td> <td style="width: 70%; text-align: center;">Number</td> </tr> <tr> <td>Application Number:</td> <td>10792120</td> </tr> </table>		Property Type	Number	Application Number:	10792120								
Property Type	Number												
Application Number:	10792120												
CORRESPONDENCE DATA													
<p>Fax Number: (215)979-1020 <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i></p> <p>Phone: 215-979-1842</p> <p>Email: japowers@duanemorris.com, eorleman@duanemorris.com</p> <p>Correspondent Name: Joseph A. Powers</p> <p>Address Line 1: Duane Morris LLP, One Liberty Place</p> <p>Address Line 4: Philadelphia, PENNSYLVANIA 19103</p>													
NAME OF SUBMITTER:	Joseph A. Powers												
<p>Total Attachments: 2</p> <p>source=e1821-00001#page1.tif</p> <p>source=e1821-00001#page2.tif</p>													

CH \$40.00 10792120

500007131

PATENT
REEL: 015136 FRAME: 0553

ASSIGNMENT AND AGREEMENT

Elite Chung Zen Chen 7/27/04

✓ For value received, I, Chung Zen Chen, hereby sell, assign and transfer to ~~Elite~~ Semiconductor Memory Technology Inc., with its principal place of business located at No. 23, Industry E. Rd IV, Science-Based Industrial Park, Hsinchu 300, Taiwan, Republic of China, and its successors, assigns and legal representatives, the entire right, title and interest, for the United States of America, in and to certain inventions related to **BIT SWITCH VOLTAGE DROP COMPENSATION DURING PROGRAMMING IN NONVOLATILE MEMORY** described in an application for Letters Patent of the United States and all the rights and privileges in said application and under any and all Letters Patent and any divisions, continuations, continuations-in-part, reexamination certificates, reissues, and extensions thereof that may be granted in the United States for said inventions; and I also concurrently hereby sell, assign and transfer to ~~Elite~~ Semiconductor Memory Technology Inc. the entire right, title and interest in and to said inventions for all countries foreign to the United States, including all rights of priority arising from the application aforesaid, and all the rights and privileges under any and all forms of protection, including Letters Patent, that may be granted in said countries foreign to the United States for said inventions.

Elite
Chung Zen Chen
7/27/04

Elite Chung Zen Chen 7/27/04

I authorize ~~Elite~~ Semiconductor Memory Technology Inc. to make application for such protection in its own name and maintain such protection in any and all countries foreign to the United States, and to invoke and claim for any application for patent or other form of protection for said inventions, without further authorization from me, any and all benefits, including the right of priority provided by any and all treaties, conventions, or agreements.

I hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country in proof of the right of ~~Elite~~ Semiconductor Memory Technology Inc. to apply for patent or other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.

Elite Chung Zen Chen 7/27/04

I request that any and all patents for said inventions be issued to ~~Elite~~ Semiconductor Memory Technology Inc. in the United States and in all countries foreign to the United States, or to such nominees as ~~Elite~~ Semiconductor Memory Technology Inc. may designate.

Elite Chung Zen Chen 7/27/04

Elite
Chung Zen Chen 7/27/04

EXPRESS MAIL LABEL NO.: EV398574579US

ATTORNEY DOCKET NO. E1821-80001

Elite Chung Zen Chen 7/27/04

I agree that, when requested, I shall, without charge to ~~Elite~~ Semiconductor Memory Technology Inc. but at its expense, sign all papers, and do all acts which may be necessary, desirable or convenient in connection with said applications, patents, or other forms of protection.

(Sole) Inventor

Dated: 2, 27, 2004

Chung Zen Chen
Chung Zen Chen

Residence: 2F-2 TA HSUEH Road
Hsinchu 300, Taiwan, Republic of China

Hsinchu Chung Zen Chen 7/27/04