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10/749,103
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CONFIRMATORY INSTRUMENT

Application for: "Method of Forming a Multi-Layer Semiconductor Structure
Incorporating a processing Handle Member"

Inventor(s): L Rafael Reif and Andy Fan

Serial Number: 10/749,103

Filing Date: December 30, 2003

Grant/Contract No.: Subcontract No. B-12-D00-S1, between G.I.T. and M.I.T., Effective
October 1, 1998, under Prime DARPA Grant No. MDA972-99-1-0002

Contractor: MASSACHUSETTS INSTITUTE OF TECHNOLOGY

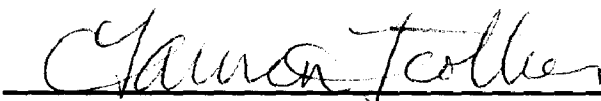
M.I.T. Case No.: 10003

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Signed this 26th day of February, 2004.

MASSACHUSETTS INSTITUTE OF TECHNOLOGY

By 
Lauren Trottier
Patent Compliance Administrator
Technology Licensing Office

77 Massachusetts Avenue, Room NE25-230, Cambridge, MA 02139

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