

04-09-2004

4.2-04

Tab settings → → → ▼



102718353

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

AMIT KHANUJA

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance:

☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Other

Execution Date(s): MARCH 29, 2004

2. Name and address of receiving party(ies):

Name: Virage Logic Corp.

Internal Address:

Street Address: 47100 Bayside Parkway

City: Fremont

State: CA

ZIP: 94538

Additional name(s) & address(es) attached? ☐ Yes ☒ No

22857 U.S. PTO
10/816763

040204

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: MARCH 29, 2004; entitled PARTITIONED SOURCE LINE ARCHITECTURE FOR ROM (ATTY. DOCKET NO.: 1263-0025US)

A. Patent Application No. (s)

10/816763

B. Patent No. (s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Shreen K. Danamraj

Internal Address:

Street Address:

Danamraj & Youst, P.C.

5910 N. Central Expressway

Suite 1450

City: Dallas

State: TX

ZIP: 75206

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41)

\$ 40.00

☒ Enclosed

☐ Authorized to be charged to deposit account

8. Deposit account number:

(Attach duplicate copy of this page if paying by deposit account)

04/08/2004 BTOM11 00000016 10816763

01 FC:8021

40.00 00

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Shreen K. Danamraj

Name of Person Signing

Shreen K. Danamraj

Signature

April 2, 2004

Date

Total number of pages including cover sheet: 4

ASSIGNMENT

WHEREAS, AMIT KHANUJA ("ASSIGNOR"), an individual who resides at the address set forth below, is the sole owner of the entire interest in the invention for:

PARTITIONED SOURCE LINE ARCHITECTURE FOR ROM

for which application for United States Letters Patent is being made; and

WHEREAS, Virage Logic Corp. ("ASSIGNEE"), a corporation organized and existing under the laws of the State of California, having its principal place of business at 47100 Bayside Parkway, Fremont, CA 94538, is desirous of acquiring ASSIGNOR'S entire interest in the invention, and in the said application and any Letters Patent that may issue therefrom;

NOW, THEREFORE, in consideration of One Dollar (\$1.00) and other good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, ASSIGNOR hereby assigns unto ASSIGNEE, its successors, assigns and legal representatives, ASSIGNOR'S entire interest in said invention and in said application, including any provisional applications upon which this application claims priority, and all patents which may be granted therefor, and all divisions, reissues, substitutions, continuations, continuations-in-part and extensions thereof. ASSIGNOR hereby authorizes and requests the Commissioner of Patents and Trademarks to issue all patents for said invention, insofar as ASSIGNOR'S interest is concerned, to the said ASSIGNEE.

ASSIGNOR also hereby assigns to said ASSIGNEE, its successors, assigns and legal representatives, the full and exclusive interest in the invention disclosed in said application throughout the world, including the right to file applications and obtain patents, utility models, industrial models and designs for said invention in its own name throughout the world, including all rights of priority, all rights to publish cautionary notices reserving ownership of said invention and all rights to register said invention in appropriate registries.

ASSIGNOR further agrees to execute any powers-of-attorney, applications, assignments, declarations, affidavits, and any other papers necessary to perfect such interest in ASSIGNEE, its successors, assigns and legal representatives.

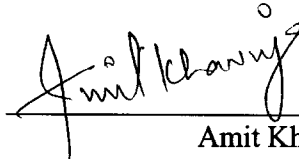
ASSIGNOR hereby further agrees that he will communicate to said ASSIGNEE, or its successors, assigns and legal representatives, any facts known to him respecting any improvements in said invention. At the expense of said ASSIGNEE, ASSIGNOR agrees to testify in any legal proceedings; sign all lawful papers; execute all divisional, continuation, continuation-in-part, reissue, and substitute applications; make all lawful oaths; and generally do everything possible to vest title in said ASSIGNEE and to aid said ASSIGNEE, its successors, assigns and legal representatives to obtain and enforce proper protection for said invention in all countries.

PATENT APPLICATION
DOCKET NO.: 1263-0024US

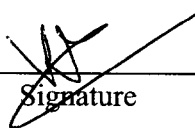
IN TESTIMONY WHEREOF, I have duly executed this assignment this 29th day of
March, 2004.

Address:

1401 Red Hawk Cir #O-102
Fremont, CA 94538
USA


Amit Khanuja

SIGNATURE OF AMIT KHANUJA WITNESSED BY:


Signature

03-29-04
Date

VIPIN TIWARI
Printed Name
4875 MOWRY AVE, #215
FREMONT CA, 94538
Address